Vincula Trouble

You made some mistakes on equations in my article, "Circuit Design From Scratch" (Nov. 1984). On page 60, the lower equation in column 1 should read "$C = A\overline{B} + \overline{A}B + \overline{A}\overline{B}$." In column 2 of the same page, replace the formulas labeled 1 through 6 with the following:

1. $\overline{X} + X\overline{Y} = \overline{X} + \overline{Y}$
2. $\overline{XY} = \overline{X} + \overline{Y}$
3. $X(Y + Z) = XY + XZ$
4. $X + \overline{X} = 1$
5. $X + \overline{X}Y = X + Y$
6. $X + Y = \overline{\overline{X}\overline{Y}}$

and change the immediately following equation in the same colum to read "$C = A\overline{B} + \overline{A}B + \overline{A}\overline{B}$." In columns 3, change the second equation to read "$D = B(A\overline{C} + AC + \overline{A}\overline{C} + \overline{A}C) + A\overline{B}C$." On page 61, column 1, change the last equation to read "$D = \overline{B}A\overline{C}$." Finally, in Fig. 7 (page 60), there should be a NOT gate in the lowest A input line; the last gate should be an OR gate; and the first line of the caption for this drawing should read "Fig. 7. By using a combination of AND and OR gates and inverters . . ."

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