## designideas

# LED bar-graph display represents two digits 

Ajoy Raman, Bangalore, India

』
This circuit uses two National Semiconductor (www.national. com) LM3914 dot/bar-display-driver ICs to implement a two-digit, 0 to 5 V LED voltmeter that mimics a subranging flash ADC. An LED bar graph comprising five LEDs, each representing 1 V of input signal, represents the MSD (most-significant digit). Nine LEDs in dot mode, in which only one LED lights, represent the LSD (least-significant digit). The circuit senses the operation of the MSD LEDs and uses them to change the input reference ladder of the chip that drives the LSD. The input signal ranges from

0 to 5 V , and accuracy is better than $\pm 50$ mV . The circuit operates over a supply voltage range of 5 to 8 V .
$R_{1}$ and $R_{2}$ divide the input voltage in half, such that a 5 V maximum input is 2.5 V at the $\mathrm{LM} 3914 \mathrm{~s}, \mathrm{IC}_{1}$ and $\mathrm{IC}_{2}$ (Figure 1). You strap the mode pin of $\mathrm{IC}_{1}$ high, so it operates as a bar graph, and use $\mathrm{V}_{\mathrm{R} 1}$ to adjust the REFOUT pin of $\mathrm{IC}_{1}$ to 2.5 V . Thus, each of the $\mathrm{IC}_{1}$ output pins lights successively in 0.5 V increments. Because this IC makes the MSD, you wire in only five LEDs on every other output, starting at output $\mathrm{D}_{2}$, meaning that the five LEDs will light at 1 V inter-
vals from 1 to 5 V . The LM3914's data sheet explains how you can use $\mathrm{R}_{3}$ to set a constant-current output on the LED pins (Reference 1). The current in each LED is approximately 10 times the current that you draw from the REFOUT output pin. The part maintains 1.25 V between the REFADJ and REFOUT pins. The $\mathrm{V}_{\mathrm{R} 2} / \mathrm{R}_{10} / \mathrm{R}_{13}$ voltage divider causes a load, which, along with the $1.5-\mathrm{k} \Omega$ value of $R_{3}$, sets a fixed output current in LEDs $\mathrm{D}_{1}$ through $\mathrm{D}_{5}$. You should select these LEDs from the same batch so that their forward voltage drops match.

You then wire a resistor and a transistor around each of the four LEDs. The voltage across the LED also presses across the resistors, so these LEDs form four constant-current sources that operate in conjunction with the LEDs. Adjust $V_{R 3}$ such that each LED when on


Figure 1 This voltmeter displays 1 to 5 V as a bar graph from $\mathrm{IC}_{1}$. A dot display from $\mathrm{IC}_{2}$ represents the least-significant digit, with the LEDs representing 0.1 to 0.9 V .


Figure 2 The LM3914 IC has an internal reference and a resistor ladder you can configure to make bar or dot LED displays (courtesy National Semiconductor).
adds 500 mV to their summed output. You send this signal to $\mathrm{R}_{\mathrm{LO}}$, the bottom of the internal resistor string in the second LM3914 (Figure 2). You then send the $50 \%$-divided input signal to the SIG Pin of $\mathrm{IC}_{2}$. Use an op amp, $\mathrm{IC}_{3}$, to add a fixed $500-\mathrm{mV}$ offset plus the summedcurrent signal from the outputs of $\mathrm{IC}_{1}$. $R_{1}$ and $R_{2}$ reduce the input signal to the circuit by $50 \%$, so a $500-\mathrm{mV}$ excursion at IC 's SIG Pin input represents 1 V of the input excursion.

## LEAVE THE MODE PIN ON IC 2 FLOATING SO THAT THE PART OPERATES IN DOT MODE, NOT BAR MODE.

As the input to the circuit goes from 0 to 1 V , the SIG inputs to both bargraph ICs go from 0 to 0.5 V . No LEDs light on $\mathrm{IC}_{1}$, meaning that $\mathrm{IC}_{2}$ has $\mathrm{R}_{\mathrm{L}}$ at 0 V and $\mathrm{R}_{\mathrm{HI}}$ at the $500-\mathrm{mV}$ offset you adjusted with $\mathrm{V}_{\mathrm{R} 2}$. The LED outputs of $\mathrm{IC}_{2}$ now light in sequence as the input to the chip goes from 0 to 0.45 V , corresponding to a 0 to 0.9 V input at the Signal-in Port. When the input signal is high enough to light LED $D_{1}$, the value at $\mathrm{IC}_{2}$ 's $\mathrm{R}_{\mathrm{L}}$ jumps to 500 mV , and the input at $\mathrm{R}_{\mathrm{HI}}$ jumps to just 500 mV higher than $\mathrm{R}_{\mathrm{L}}$, or 1 V . Because $\mathrm{IC}_{2}$ 's internal resistor ladder is now biased between 0.5 and $1 \mathrm{~V}, \mathrm{IC}_{2}$ indicates 0.1 V steps between 1 and 2 V at the Signal-in Port. Leave the Mode Pin on $\mathrm{IC}_{2}$ floating so that the part operates in dot mode instead of bar-graph mode.

At a 4.9 V input to the Signal-in Port, LEDs $\mathrm{D}_{1}$ through $\mathrm{D}_{4}$ illuminate, resulting in 2 V at the $\mathrm{R}_{\mathrm{LO}}$ input of $\mathrm{IC}_{2}$. The op amp adds 500 mV to that value and presents it to the $\mathrm{R}_{\mathrm{HII}}$ input of $\mathrm{IC}_{2}$ for a total of 2.5 V . The input to $\mathrm{IC}_{2}$ is 2.45 V , so the $\mathrm{D}_{9}$ output of $\mathrm{IC}_{2}$ lights $\mathrm{D}_{14}$, correctly indicating the LSB (leastsignificant bit) of the measurement as nine-tenths.EDN

[^0]
[^0]:    REFERENCE
    i- "LM3914 Dot/Bar Display Driver," National Semiconductor, February 2003, http://bit.ly/naDCRG.

