



Fig. 31-18. DVM adapter for a frequency counter. This circuit consists of 200K  $\Omega$ /V input low-pass active filter stage, a polarity detector, and a voltage-to-frequency converter. The output frequency is adjusted so that 50V will give a frequency of 5000 out. The three 25K pots are used for offset balancing. Any counter capable of readout to Hz is fine.