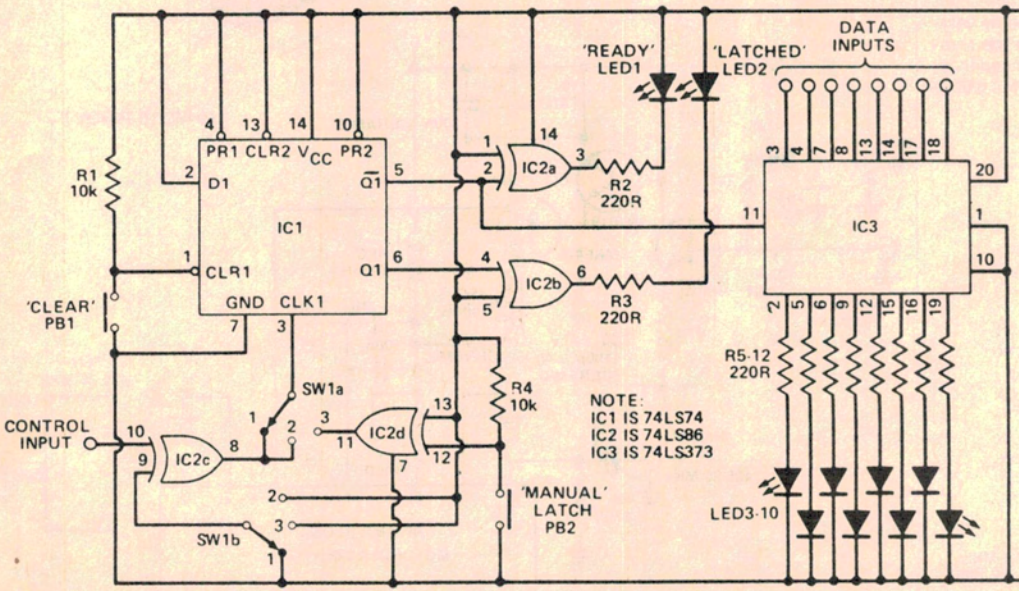


## Logic Analyser

When testing logic circuits it is often necessary to know the simultaneous state of several nodes. To do it, you need a bunch of logic probes or a logic state analyser. The 'professional' tool is all-singing-and-dancing, and expensive. This circuit is a simpler implentation. Eight input bits are latched and displayed, controlled by the CONTROL INPUT from the circuit being analysed. It has three triggering modes: with SW1 at 1, it latches on a rising edge on the control input; with SW1 on 2, it latches on the falling edge control input; with SW1 on 3 it latches when PB2 is pressed. PB1 clears the latch.