SYNTHESIZED PULSE GENERATOR

Here's a tip for readers who built the Synthesized Pulse Generator described in the October, 1980 issue of *Radio-Electronics.* Changing capacitor C6 from 4.7 μF to 1.0 μF may result in better performance, as some CD4046 IC's produce slight jitter using the old value. That will allow the project to give the premium performance that it was designed for, regardless of IC manufacturer.

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