



## Rising Edge Trigger

The diagram shows a method of triggering a conventional monostable on the rising edge of a short negative-going pulse. The additional transistor, TR1, provides good isolation between the output pulse and the triggering circuitry. The circuit shown gives a pulse of  $5\mu\text{sec}$  duration, but of course the usual design formula  $\tau = 0.65 RC$  can be used

to determine circuit values for other pulse widths.

One slight disadvantage of this circuit is that the collector of TR2 is held down by the triggering wave-form, so the switch-on of TR3 is not regenerative. For this reason the falling edge of the output pulse is not as fast as it might be, but is sufficient for most purposes.