

## Simple ring oscillator

This i.c. oscillator can be disabled by applying a 0 at either input, and will be retriggered when the low input is returned to a logic 1. Although under normal operation there are only two outputs, when the oscillator is disabled only one pair of outputs undergoes a further transition before oscillation stops. Therefore, this circuit is suitable for applications where it is necessary to have control of the stop and start point in the duty cycle.

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