



Notes:

- R46 is selected to trim input offset to <1mv.
- R15 is selected to bias input FETs at 4ma. each.
- R18 biases voltage gain stage at 4-6ma per leg.
- R25 sets output stage bias.
- R29 balances bias between drivers and outputs.
- Any transistor pairs or triples should have matched Hfe's

Great River OpAmp schematic

Drawn by Dan Kennedy

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