
A few errors crept into the the article "Build This Satellite-TV Descrambler" in the October 1986 issue of **Radio-Electronics**. In Fig. 2, the schematic diagram, pin 14 of IC's 2 and 3 should be tied to -5 volts, not $+5$ volts as shown. Resistor R29 should be tied to $+5$ volts. Transistor Q7 is misidentified: It should be a 2N3904. Finally, the positions of Q5 and Q6 have been reversed. The unit identified as Q5 should be Q6, a 2N3904 NPN transistor; the transistor identified as Q6 should be Q5, a 2N3906 PNP transistor.

In Fig. 3, the parts-placement diagram, two components are shown in the wrong location. Resistors R8 and R23 are shown vertically aligned; they should be mounted horizontally. Resistor R8 mounts between the top of what is shown as R8 and the top of what is shown as R23; resistor R23 mounts between the bottom of what is shown as R8 and the bottom of what is shown as R23.

Finally, a trace is missing from the PC pattern. It runs from the positive end of C5 to the center of jack J2. Replace that trace with a jumper.—*Editor.*
