

## Editorial Distortion

SIR:

Figure 11 of "Distortion in Voltage Amplifiers" in the February issue is quite interesting. The "Horrible Example" shows about 0.1 per cent intermodulation distortion at 1 volt output, while the improved circuit shows 10 per cent. A circuit diagram illustrating the good engineering practice needed to achieve this result would be enlightening.

W. H. FROLICH, M. D.,  
Steptoe Valley Hospital,  
East Ely, Nevada.

SIR:

I have read W. B. Bernard's article "Distortion in Voltage Amplifiers," in the February issue, with great interest, and with some dismay, particularly in regard to the curve of Fig. 11, page 55. As I interpret the curve, the improved circuit gives very much more intermodulation distortion than the unimproved circuit. This is just the type of "improvement" that my little nephew makes in some of his experimental amplifiers.

It appears that the curve is either an extreme attempt at honesty, so that Diogenes can blow out his lantern and go back to his barrel; or that there is a drafting error somewhere. Which?

RONALD L. IVES,  
5415 Main St.,  
Williamsville 21, N. Y.

*(Well, we did it again. But this time we can't blame the draftsman or the author. We rechecked the author's curve, and found that we had made an erroneous notation before sending it to the draftsman. We bow our editorial head in shame until the next similar error occurs. Seriously, we have requested Mr. Bernard to supply us with the improved circuit to enlighten our readers and ourselves. E.D.)*