

Scope Sweep Generator

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Construction details on an inexpensive time-mark generator for calibrating the horizontal sweep of an oscilloscope.

MEASURING time or frequency accurately with an oscilloscope is impossible if the scope's horizontal sweep (trace) is uncalibrated. The horizontal sweep can be calibrated by a time-mark generator. Although a professional time-mark generator costs about \$300, the simple time-mark generator covered in this article can be built for substantially less than this amount.

This time-mark generator has outputs at known frequencies and when the frequency is accurately known, the period in microseconds along the scope trace can be calculated. Table 1 indicates the relationship between frequency and time for this generator.

A scope's horizontal sweep can be calibrated in one of three ways. The first and most useful is in microseconds-per-centimeter. Other, less-common markings, are microseconds-per-inch, or frequency, which is uncalibrated. With a known frequency signal any scope's horizontal sweep can be calibrated with great accuracy by merely adjusting the variable sweep speed until a known number of timing intervals occupies a specific distance on the screen. Any scope sweep non-linearity will also be displayed.

In this time-mark calibrator, thirteen transistors are used to provide accurate calibration points of 1, 5, 10, 100, 1000, and 10,000 microseconds duration.

As shown in Fig. 1, a 1-MHz crystal oscillator (Q1) furnishes an accurate 1-microsecond timing output *via* Q2 to point "A". A one-shot multivibrator (Q3-Q4), adjusted for a division of five, furnishes an accurate 5- μ sec output at point "B". Division-by-two is accomplished in flip-flop Q5-Q6 to produce a 10- μ sec output at point "C". Division-by-ten can now be accomplished with unijunction transistor stage Q7 to produce a 100- μ sec output at point "D". Two more divide-by-ten circuits, Q8 and Q9, have outputs of 1000 μ secs at point "E" and 10,000 μ secs at point "F", respectively.

Accuracy is inherent in this design. The crystal provides the primary accuracy and any error is too small to be observed on a scope. All of the other outputs are obtained by division from this stable source. A word of caution—division in the last three stages will be correct only if the loading of the unijunction emitters remains constant. For this reason, an emitter follower, Q12 and Q13, has been added.

If your scope does not have a 10-megohm vertical amplifier input impedance, then the emitter follower should be used as part of the testing procedure. The emitter follower is a Darlington configuration with a calculated input impedance of about 14 megohms. It exhibits no measurable loading to any of the circuits.

Parts required to build this generator are standard but a high-*beta*, high-*f*, transistor should be used for the higher frequency circuits. Placement of the parts is not critical although short leads should be used.

The addition of regulator R8 and Q10 and Q11 will result in greater stability. The emitter-base junctions of the 2N3638's are used as a low-cost zener diode. If a regulated supply or a mercury battery is used, the regulator would not be required, and could be omitted.

Waveforms

When power is applied, the waveform of Fig. 2A will be present at the collector of Q1. The purpose of buffer Q2 is to provide a sharp falling edge for triggering the following divide-by-five counter. The waveform of Fig. 2B will be present at the collector of Q2 and point "A". The trailing edges of this waveform are 1 μ sec apart. Transistors Q3 and Q4 make up a one-shot, divide-by-five circuit. Q3 is normally "on" ("on" is defined as the transistor in conduction while "off" is when it is not). A negative pulse coupled through C6 and D1 turns Q3 "off" causing Q4 to go "on". Since the voltage across capacitor C8 cannot change instantaneously, the base of Q3 is forced approximately 10 volts below the emitter. Capacitor C8 is charged by R11 and R12 in series and, when approximately +0.6 volt is reached, Q3 turns "on" removing the base drive from Q4. Control R12 is adjusted so that the time required for this complete cycle is five pulses of the input. Fig. 2C shows the collector waveform of Q4 at point "B", while Fig. 2D shows the waveform at the base of Q3. The waveform at the collector of Q4 is 5 μ sec per cycle.

Division-by-two is most easily accomplished with a flip-flop. Transistors Q5 and Q6 make a simple flip-flop, with D2 and D3, the triggering diodes. Since R16 and R17 form direct paths from opposite collectors to opposite bases, the flip-flop will remain stable until a change is triggered by the falling edge of the Q4 output waveform. The waveforms of this flip-flop will be affected by subsequent loading, so C13 and R22 should be connected before testing. Fig. 2E shows the waveform present at the Q6 collector and point "C". This waveform is 10 μ sec per cycle, or 100 kHz.

Division-by-ten can be accomplished at lower frequencies using a unijunction transistor. Capacitor C13 couples the pulses from Q6 into base-2 of Q7. The combination of C14 and R20 and R21 form a time constant equivalent to a fre-

Table 1. The relationship between frequency and time for the various output waveforms of the simple scope sweep calibrator.

TEST POINT	FREQUENCY	TIME
"A"	1 MHz	1 μ sec
"B"	200 kHz	5 μ secs
"C"	100 kHz	10 μ secs
"D"	10 kHz	100 μ secs
"E"	1 kHz	1000 μ secs
"F"	100 Hz	10,000 μ secs

