# Tektronix <br> COMMITTED TO EXCELLENCE 

## WARNING

THE FOLLOWING SERVICING INSTRUCTIONS ARE FOR USE BY QUALIFIED PERSONNEL ONLY. TO AVOID PERSONAL INJURY, DO NOT PERFORM ANY SERVICING UNLESS YOU ARE QUALIFIED TO DO SO.

PLEASE CHECK FOR CHANGE INFORMATION AT THE REAR OF THIS MANUAL.


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## INSTRUMENT SERIAL NUMBERS

Each instrument has a serial number on a panel insert, tag, or stamped on the chassis. The first number or letter designates the country of manufacture. The last five digits of the serial number are assigned sequentially and are unique to each instrument. Those manufactured in the United States have six unique digits. The country of manufacture is identified as follows:

| B000000 | Tektronix, Inc., Beaverton, Oregon, USA |
| :--- | :--- |
| 100000 | Tektronix Guernsey, Ltd., Channel Islands |
| 200000 | Tektronix United Kingdom, Ltd., London |
| 300000 | Sony/Tektronix, Japan |
| 700000 | Tektronix Holland, NV, Heerenveen, |
|  | The Netherlands |

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## OPERATORS SAFETY SUMMARY

The general safety information in this part of the summary is for both operating and servicing personnel. Specific warnings and cautions will be found throughout the manual where they apply and do not appear in this summary.

## Terms in This Manual

CAUTION statements identify conditions or practices that could result in damage to the equipment or other property.

WARNING statements identify conditions or practices that could result in personal injury or loss of life.

## Terms as Marked on Equipment

CAUTION indicates a personal injury hazard not immediately accessible as one reads the markings, or a hazard to property, including the equipment itself.

DANGER indicates a personal injury hazard immediately accessible as one reads the marking.

## Symbols in This Manual

This symbol indicates where applicable cautionary or other information is to be found. For maximum input voltage see Table 1-1.

## Symbols as Marked on Equipment



Protective ground (earth) terminal.
$\square$ ATTENTION - Refer to manual.

## Power Source

This product is intended to operate from a power source that does not apply more than 250 volts rms between the supply conductors or between either supply conductor and ground. A protective ground connection by way of the grounding conductor in the power cord is essential for safe operation.

## Grounding the Product

This product is grounded through the grounding conductor of the power cord. To avoid electrical shock, plug the power cord into a properly wired receptable before connecting to the product input or output terminals. A protective ground connection by way of the grounding conductor in the power cord is essential for safe operation.

## Danger Arising From Loss of Ground

Upon loss of the protective-ground connection, all accessible conductive parts (including knobs and controls that may appear to be insulating) can render an electric shock.

## Use the Proper Power Cord

Use only the power cord and connector specified for your product.

Use only a power cord that is in good condition.
For detailed information on power cords and connectors see Figure 2-1.

## Use the Proper Fuse

To avoid fire hazard, use only a fuse of the correct type, voltage rating and current rating as specified in the parts list for your product.

## Do Not Operate in Explosive Atmospheres

To avoid explosion, do not operate this product in an explosive atmosphere unless it has been specifically certified for such operation.

## Do Not Remove Covers or Panels

To avoid personal injury, do not remove the product covers or panels. Do not operate the product without the covers and panels properly installed.

# SERVICING SAFETY SUMMARY <br> FOR QUALIFIED SERVICE PERSONNEL ONLY 

Refer also to the preceding Operators Safety Summary.

## Do Not Service Alone

Do not perform internal service or adjustment of this product unless another person capable of rendering first aid and resuscitation is present.

## Use Care When Servicing With Power On

Dangerous voltages exist at several points in this product.
To avoid personal injury, do not touch exposed connections or components while power is on.

Disconnect power before removing protective panels, soldering, or replacing components.

## Power Source

This product is intended to operate from a power source that does not apply more than 250 volts rms between the supply conductors or between either supply conductor and ground. A protective ground connection by way of the grounding conductor in the power cord is essential for safe operation.


The 2215 Oscilloscope.

## SPECIFICATION

## INTRODUCTION

The TEKTRONIX 2215 Oscilloscope is a rugged, lightweight, dual-channel, $60-\mathrm{MHz}$ instrument that features a bright, sharply defined trace on an $80-$ by $100-\mathrm{mm}$ cathoderay tube (crt). Its vertical system provides calibrated deflection factors from 2 mV per division to 10 V per division. Trigger circuits enable stable triggering over the full bandwidth of the vertical system. The horizontal system provides calibrated sweep speeds from 0.5 s per division to 50 ns per division along with delayed-sweep features for accurate relative-time measurements. A $\times 10$ magnifier extends the maximum sweep speed to 5 ns per division.

## ACCESSORIES

The instrument is shipped with the following standard accessories:
1 Operators manual
2 Probe packages
1 Service manual
2 Probe grabber tips

For part numbers and further information about both standard and optional accessories, refer to the "Accessories" page at the back of this manual. Your Tektronix representative, your local Tektronix Field Office, or the Tektronix product catalog can also provide accessories information.

## PERFORMANCE CONDITIONS

The following electrical characteristics (Table 1-1) are valid for the 2215 when it has been adjusted at an ambient temperature between $+20^{\circ} \mathrm{C}$ and $+30^{\circ} \mathrm{C}$, has had a warm-up period of at least 20 minutes, and is operating at an ambient temperature between $0^{\circ} \mathrm{C}$ and $+50^{\circ} \mathrm{C}$ (unless otherwise noted).

Items listed in the "Performance Requirements" column are verifiable qualitative or quantitative limits, while items listed in the "Supplemental Information" column are either explanatory notes, calibration setup descriptions, performance characteristics for which no absolute limits are specified, or characteristics that are impractical to check.

Environmental characteristics are given in Table 1-2. The 2215 meets the requirements of MIL-T-28800B, Class 5 equipment, except where otherwise noted.

Physical characteristics of the instrument are listed in Table 1-3.

Table 1-1
Electrical Characteristics

| Characteristics | Performance Requirements | Supplemental Information |
| :---: | :---: | :---: |

## VERTICAL DEFLECTION SYSTEM

\begin{tabular}{|c|c|c|}
\hline \multirow[t]{2}{*}{Deflection Factor

Range} \& \& \multirow[t]{4}{*}{| 1X gain adjusted with VOLTS/DIV switch set to 20 mV per division. |
| :--- |
| 10X gain adjusted with VOLTS/DIV switch set to 2 mV per division. |} <br>

\hline \& 2 mV per division to 10 V per division in a $1-2-5$ sequence. \& <br>
\hline Accuracy

$$
+20^{\circ} \mathrm{C} \text { to }+30^{\circ} \mathrm{C}
$$ \& $\pm 3 \%$. \& <br>

\hline $0^{\circ} \mathrm{C}$ to $+50^{\circ} \mathrm{C}$ \& $\pm 4 \%{ }^{\text {a }}$ \& <br>
\hline Range of VOLTS/DIV Variable Control. \& Continuously variable between settings. Increases deflection factor by at least 2.5 to 1. \& <br>
\hline Step Response \& \& Measured with a vertically centered 5 -division reference signal from a $50-\Omega$ source driving a $50-\Omega$ coaxial cable that is terminated in $50 \Omega$ at the input connector, with the VOLTS/DIV Variable control in its CAL detent. <br>

\hline Rise Time \& \& | 5.8 ns or less. |
| :--- |
| Rise time is calculated from the formula: | <br>

\hline
\end{tabular}

formula:

Rise Time $=\frac{0.35}{B W(\text { in } \mathrm{MHz})}$


[^0]Table 1-1 (cont)

| Characteristics | Performance Requirements | Supplemental Information |
| :---: | :---: | :---: |
| VERTICAL DEFLECTION SYSTEM (cont) |  |  |
| Input Characteristics |  |  |
| Resistance | $1 \mathrm{M} \Omega \pm 2 \%{ }^{\text {a }}$ |  |
| Capacitance | $30 \mathrm{pF} \pm 3 \mathrm{pF} .^{\text {a }}$ |  |
| Maximum Safe Input Voltage DC Coupled | $400 \mathrm{~V}(\mathrm{dc}+$ peak ac) or 800 V p-p ac to 1 kHz or less. ${ }^{\text {a }}$ |  |
| AC Coupled | $400 \mathrm{~V}(\mathrm{dc}+$ peak ac) or 800 V p-p ac to 1 kHz or less. ${ }^{\text {a }}$ |  |
| Common-Mode Rejection Ratio (CMRR) | At least 10 to 1 at 10 MHz . | Checked at $\mathbf{2 0 ~ m V}$ per division for common-mode signals of 8 divisions or less, with VOLTS/DIV Variable control adjusted for best CMRR at 50 kHz . |

## TRIGGER SYSTEM

| A Trigger Sensitivity AUTO and NORM | 0.4 division internal or 50 mV external to 2 MHz , increasing to 1.5 divisions internal or 250 mV external at 60 MHz . | External trigger signal from a $50-\Omega$ source driving a $50-\Omega$ coaxial cable that is terminated in $50 \Omega$ at the input connector. <br> Will trigger on tv line sync components in NORM only $\geqslant 0.4$ division internal or 50 mV p-p external. |
| :---: | :---: | :---: |
| AUTO Lowest Usable Frequency | $20 \mathrm{~Hz} .^{\text {a }}$ |  |
| TV FIELD | 2.0 divisions of composite video or composite sync. ${ }^{\text {a }}$ |  |
| B Trigger Sensitivity Internal | 0.4 division to 2 MHz , increasing to 2.0 divisions at 60 MHz . |  |
| External input <br> Maximum Input Voltage | 400 V (dc + peak ac) or 800 V p-p ac at 1 kHz or less. ${ }^{\text {a }}$ |  |
| Input Resistance | $1 \mathrm{M} \Omega \pm 2 \%{ }^{\text {a }}$ |  |
| Input Capacitance | $30 \mathrm{pF} \pm 3 \mathrm{pF} .^{\text {a }}$ |  |
| AC Coupled | 10 Hz or less at lower -3 dB point. ${ }^{\text {a }}$ |  |

[^1]Table 1-1 (cont)

| Characteristics | Performance Requirements | Supplemental Information |
| :---: | :---: | :---: |
| TRIGGER SYSTEM (cont) |  |  |
| LEVEL Control Range |  |  |
| A Trìgger (NORM) |  |  |
| INT | On screen limits. ${ }^{\text {a }}$ |  |
| EXT and DC | At least $\pm 2 \vee(4 \vee \mathrm{p}-\mathrm{p})^{\text {a }}$. |  |
| EXT and DC $\div 10$ | At least $\pm 20 \vee(40 \vee p-p) .^{\text {a }}$ |  |
| B Trigger Internal | On screen limits. ${ }^{\text {a }}$ |  |
| VAR HOLDOFF Control Range | Increases the A Sweep holdoff time by at least a factor of four. ${ }^{\text {a }}$ |  |

## HORIZONTAL DEFLECTION SYSTEM



[^2]Table 1-1 (cont)

| Characteristics | Performance Requirements | Supplemental Information |  |
| :--- | :--- | :--- | :---: |
| HORIZONTAL DEFLECTION SYSTEM (cont) |  |  |  |
| Delay Time (cont) <br> Jitter | One part, or less, in $10,000(0.01 \%$ ) of <br> the maximum available delay time. |  |  |
| Dial Accuracy | $\pm 1.5 \%$ of full scale. |  |  |

## X-Y OPERATION (X1 MAGNIFICATION)

| Deflection Factors Range | Same as Vertical Deflection System, with both VOLTS/DIV Variable controls in CAL detent. |  |  |
| :---: | :---: | :---: | :---: |
| Accuracy | $X$-Axis | Y-Axis | Measured with a dc-coupled, 5 -division reference signal. |
| $+20^{\circ} \mathrm{C}$ to $+30^{\circ} \mathrm{C}$ | $\pm 5 \%$ | $\pm 3 \%$ |  |
| $0^{\circ} \mathrm{C}$ to $+50^{\circ} \mathrm{C}$ | $\pm 6 \%{ }^{\text {a }}$ | $\pm 4 \%^{\text {a }}$ |  |
| Bandwidth <br> X-Axis | Dc to at least 2 MHz . |  | Measured with a 5 -division reference signal. |
| Y-Axis | Same as Vertical Deflection System. |  |  |
| Phase Difference Between Xand $Y$-Axis Amplifiers | $\pm 3^{\circ}$ from dc to 50 kHz . ${ }^{\text {a }}$ |  | With de-coupled inputs. |

PROBE ADJUST

| Signal at PROBE ADJUST Jack <br> Voltage | $0.5 \mathrm{~V} \pm 20 \%$. |  |
| :--- | :--- | :--- |
| Repetition Rate | $1 \mathrm{kHz} \pm 20 \%{ }^{\mathrm{a}}$ |  |

Z-AXIS INPUT

| Sensitivity | 5 V causes noticeable modulation. <br> Positive-going input signal decreases <br> intensity. |  |
| :--- | :--- | :--- |
| Usable Frequency Range | Dc to $5 \mathrm{MHz} .^{\text {a }}$ |  |
| Maximum Safe Input Voltage | 30 V (dc + peak ac) or <br> 30 V p-p ac at 1 kHz or less. ${ }^{\text {a }}$ |  |
| Input Impedance | $10 \mathrm{k} \Omega \pm 10 \% .^{\text {a }}$ |  |

[^3]Table 1-1 (cont)

| Characteristics | Performance Requirements | Supplemental Information |
| :---: | :---: | :---: |
| POWER SOURCE |  |  |
| Line Voltage Range | $90 \vee$ to $250 \mathrm{~V}^{\text {a }}$ |  |
| Line Frequency Range | 48 Hz to $62 \mathrm{~Hz} .^{\text {a }}$ |  |
| Maximum Power Consumption | $50 \mathrm{~W}^{\text {a }}$ |  |
| Line Fuse | $2 \mathrm{~A}, 250 \mathrm{~V}$, fast. |  |
| CATHODE-RAY TUBE |  |  |
| Display Area | 80 by $100 \mathrm{~mm} .^{\text {a }}$ |  |
| Standard Phosphor | P31. ${ }^{\text {a }}$ |  |
| Nominal Accelerating Voltage | 10,000 V. ${ }^{\text {a }}$ |  |

Table 1-2
Environmental Characteristics

| Characteristics | Description |
| :--- | :--- |
| The instrument meets all of the following MIL-T-28800B require- <br> ments for Class 5 equipment. |  |
| Temperature <br> Operating | $0^{\circ} \mathrm{C}$ to $+50^{\circ} \mathrm{C}\left(+32^{\circ} \mathrm{F}\right.$ to $\left.+122^{\circ} \mathrm{F}\right)$. |

Table 1-3
Physical Characteristics

| Characteristics | Description |
| :---: | :---: |
| Weight |  |
| With Front-Panel Cover, Accessories, and Pouch | $7.6 \mathrm{~kg}(16.8 \mathrm{lb})$. |
| Without Front-Panel Cover, Accessories, and Pouch | $6.1 \mathrm{~kg}(13.5 \mathrm{lb})$. |
| Domestic Shipping | $8.2 \mathrm{~kg}(18.0 \mathrm{lb})$. |
| Height With Feet and Handle | 137 mm (5.4 in). |
| Width |  |
| With Handle | 361 mm ( 14.2 in ). |
| Without Handle | 328 mm (12.9 in). |
| Depth |  |
| With Front-Panel Cover | 445 mm ( 17.5 in ). |
| Without Front-Panel Cover | 439 mm (17.3 in). |
| With Handle Extended | 511 mm (20.1 in). |

# OPERATING INSTRUCTIONS 

## PREPARATION FOR USE

## SAFETY

Refer to the Safety Summaries at the front of this manual for power source, grounding, and other safety considerations pertaining to the use of the 2215. Before connecting the instrument to a power source, carefully read the following information about line voltages, power cords, and fuses; then verify that the proper power-input fuse is installed.

## LINE VOLTAGE

The instrument is capable of continuous operation using ac-power-input voltages that range from 90 V to 250 V nominal at frequencies from 48 Hz to 62 Hz .

## POWER CORD

For the 120-V North American customer, the 2215 is delivered with a three-wire power cord permanently attached. At the end of the cord is a three-contact plug for connection to the power source and to protective ground. The plug's protective-ground contact connects (through the protective-ground conductor) to the accessible metal parts of the instrument. For electrical-shock protection, insert this plug only into a power-source outlet that has a securely grounded protective-ground contact.

For the non-North American customer (and for the 240-V North American user), the appropriate power cord is supplied by an option that is specified when the instrument is ordered. The optional power cords available are illustrated in Figure 2-1.

## LINE FUSE

The instrument fuse holder is located on the rear panel (see Figure 2-2) and contains the line fuse. Verify that the proper fuse is installed by performing the following procedure:

1. Unplug the power cord from the power-input source (if applicable).
2. Press in and slightly rotate the fuse-holder cap counterclockwise to release it.
3. Pull out the cap from the fuse holder, with the fuse attached to the inside of the cap.
4. Note fuse values and verify proper size (2 A, 250 V , fast-blow).
5. Reinstall the fuse and fuse-holder cap.

Operating Instructions-2215 Service

| Plug <br> Configuration | Category | Power Cord <br> and Plug <br> Type | Factory <br> Installed <br> Instrument <br> Fuse |
| :---: | :---: | :---: | :---: | :---: | :---: |

Figure 2-1. Power-input-voltage configurations.


Figure 2-2. Line fuse and power cord.

## CONTROLS, CONNECTORS, AND INDICATORS

The following descriptions are intended to familiarize the operator with the location, operation, and function of the instrument's controls, connectors, and indicators.

## POWER, DISPLAY, AND PROBE ADJUST

Refer to Figure 2-3 for location of items 1 through 7.
Internal Graticule -Eliminates parallax viewing error between the trace and graticule lines. Rise-time amplitude and measurement points are indicated at the left edge of the graticule.

POWER Switch-Turns instrument power on and off. Press in for ON; press again for OFF.

AUTO FOCUS Control-Adjusts display for optimum definition. Once set, the focus of the crt display will


Figure 2-3. Power, display, and probe adjust controls, connector, and indicator.
be maintained as changes occur in the intensity level of the trace.

PROBE ADJUST Connector-Provides an approximately $0.5-\mathrm{V}$, negative-going, square-wave voltage (at approximately 1 kHz ) that permits the operator to compensate voltage probes and to check operation of the oscilloscope vertical system. It is not intended to verify the accuracy of the vertical gain or time-base calibration.

BEAM FIND Switch-When held in, compresses the display to within the graticule area and provides a visible viewing intensity to aid in locating offscreen displays.
(6) TRACE ROTATION Control-Screwdriver control used to align the crt trace with the horizontal graticule lines.
(7) AUTO INTENSITY Control-Adjusts brightness of the crt display. This control has no effect when the BEAM FIND switch is pressed in. Once the control is set, intensity is automatically maintained at approximately the same level between SEC/DIV switch settings from 0.5 ms per division to $0.05 \mu \mathrm{~s}$ per division.

## VERTICAL

Refer to Figure 2-4 for location of items 8 through 16.
(8) SERIAL and Mod Slots-The SERIAL slot is imprinted with the instrument's serial number. The Mod slot contains the option number that has been installed in the instrument.
(9) CH 1 OR X and $\mathrm{CH} 2 \mathrm{OR} Y$ Connectors-Provide for application of external signals to the inputs of the vertical deflection system or for an $X-Y$ display. In the $X-Y$ mode, the signal connected to the CH 1 OR $X$ connector provides horizontal deflection, and the signal connected to the $\mathrm{CH} 2 \mathrm{OR} Y$ connestor provides vertical deflection.
(10) GND Connector-Provides direct connection to instrument chassis ground.


Figure 2-4. Vertical controls and connectors.
(11)

Input Coupling (AC-GND-DC) Switches-Used to select the method of coupling input signals to the vertical deflection system.

AC--Input signal is capacitively coupled to the vertical amplifier. The dc component of the input signal is blocked. Low-frequency limit ( -3 dB point) is approximately 10 Hz .

GND-The input of the vertical amplifier is grounded to provide a zero (ground) referencevoltage display (does not ground the input signal). This switch position allows precharging the input coupling capacitor.

DC-All frequency components of the input signal are coupled to the vertical deflection system.

CH 1 VOLTS/DIV and CH 2 VOLTS/DIV SwitchesUsed to select the vertical deflection factor in a 1-2-5 sequence. To obtain a calibrated deflection factor, the VOLTS/DIV variable control must be in detent.

1X PROBE-Indicates the deflection factor selected when using either a 1 X probe or a coaxial cable.

10X PROBE-Indicates the deflection factor selected when using a 10X probe.

VOLTS/DIV Variable Controls-When rotated counterclockwise out of their detent positions, these controls provide continuously variable, uncalibrated deflection factors between the calibrated settings of the VOLTS/DIV switches. Extends maximum uncalibrated deflection factor to 25 volts per division with IX probe (a range of at least 2.5:1).

INVERT Switch --Inverts the Channel 2 display when button is pressed in. Push button must be pressed in a second time to release it and regain a noninverted display.
(15) VERTICAL MODE Switches-Two three-position switches are used to select the mode of operation for the vertical amplifier system.

## CH 1-Selects only the Channel 1 input signal for display.

BOTH-Selects both Channel 1 and Channel 2 input signals for display. The BOTH position must be selected for either ADD, ALT, or CHOP operation.

CH 2-Selects only the Channel 2 input signal for display.

ADD-Displays the algebraic sum of the Channel 1 and Channel 2 input signals.

ALT-Alternately displays Channel 1 and Channel 2 input signals. The alternation occurs during retrace at the end of each sweep. This mode is useful for viewing both input signals at sweep speeds from $0.05 \mu \mathrm{~s}$ per division to 0.2 ms per division.

CHOP-The display switches between the Channel 1 and Channel 2 input signals during the sweep. The switching rate is approximately 250 kHz . This mode is useful for viewing both Channel 1 and Channel 2 input signals at sweep speeds from 0.5 ms per division to 0.5 s per division.
(16) POSITION Controls-Used to vertically position the display on the crt. When the SEC/DIV switch is set to X-Y, the Channel 2 POSITION control moves the display vertically ( $Y$-axis), and the Horizontal POSITION control moves the display horizontally (X-axis).

## HORIZONTAL

Refer to Figure 2-5 for location of items 17 through 23.
(17) B DELAY TIME POSITION Control-Selects the amount of delay time between the start of the $A$ Sweep and the start of the B Sweep. Delay time is variable from 0.5 times to 10 times the A SEC/DIV switch setting.
(18)

A and B SEC/DIV Switches-Used to select the sweep speeds for the $A$ and $B$ Sweep generators in a 1-2-5 sequence. For calibrated sweep speeds, the $A$ and $B$ SEC/DIV Variable control must be in the calibrated detent (fully clockwise).

A SEC/DIV-The A Sweep speed is shown between the two black lines on the clear plastic skirt. This switch also selects the delay time for delayed-sweep operation (used in conjunction with the B DELAY TIME POSITION control).

B SEC/DIV-The B Sweep speed is set by pulling out the DLY'D SWEEP knob and rotating it clockwise to a setting shown by the white line scribed on the knob. The B Sweep circuit is used only for delayed-sweep operation.
(19) A and B SEC/DIV Variable Control-Provides continuously variable, uncalibrated A Sweep speeds to at least 2.5 times the calibrated setting. It extends the slowest sweep speed to at least 1.25 s per division.


Figure 2-5. Horizontal controls.
(20)

X10 Magnifier Switch-To increase displayed sweep speed by a factor of 10 , pull out the $A$ and $B$ SEC/ DIV Variable knob. The fastest sweep speed can be extended to 5 ns per division. Push in the $A$ and $B$ SEC/DIV Variable control knob to regain the X 1 sweep speed.
(21) HORIZONTAL MODE Switch-This three-position switch determines the mode of operation for the horizontal deflection system.

A-Horizontal deflection is provided by the A Sweep generator at a sweep speed determined by the A SEC/DIV switch setting.

B-Horizontal deflection is provided by the B Sweep generator at a sweep speed determined by the setting of the B SEC/DIV switch. The start of the B Sweep is delayed from the start of the A Sweep by a time determined by the settings of both the A SEC/DIV switch and the B DELAY TIME POSITION control.

ALT-Alternates the horizontal displays between the A Sweep (with an intensified zone) and the B Delayed Sweep. The A Sweep speed is determined by the setting of the A SEC/DIV switch. The length of the intensified zone on the A Sweep (the B Sweep speed) is determined by the setting of the B SEC/DIV switch.
(22) POSITION Control-Positions the display horizontally for the A Sweep and the B Sweep. In the $\mathrm{X}-\mathrm{Y}$ mode, horizontally positions the X -axis.

A/B SWP SEP Control-Vertically positions the B Sweep trace with respect to the A Sweep trace when ALT HORIZONTAL MODE is selected.

## TRIGGER

Refer to Figure 2-6 for Iocations of items 24 through 33.
(24) EXT INPUT Connector-mprovides a means of introducing external signals into the $A$ Trigger generator.
(25) A EXT COUPLING Switch-Determines the method used to couple external signals to the A Trigger circuit.

AC-Signals above 60 Hz are capacitively coupled to the input of the A Trigger circuit. Any dc components are blocked, and signals below 60 Hz are attenuated.

DC-All components of the signal are coupled to the A Trigger circuitry. This position is useful for displaying low-frequency or low-repetition-rate signals,


Figure 2-6. Trigger controls, connector, and indicator.
$D C \div 10$-External trigger signals are attenuated by a factor of 10 .
(26)

A SOURCE Switch-Determines the source of the trigger signal that is coupled to the input of the A Trigger circuit.

INT-Permits triggering on signals that are applied to the $\mathrm{CH} 1 \mathrm{OR} X$ and CH 2 OR $Y$ input connectors. The source of the internal signal is selected by the A \& B INT switch.

LINE-Provides a triggering signal from a sample of the ac-power-source waveform. This trigger source is useful when channel-input signals are time related (multiple or submultiple) to the frequency on the power-source-input voltage.

EXT-Permits triggering on signals applied to the EXT INPUT connector.
(27) A \& B INT Switch-Selects the source of the triggering signal when the A SOURCE switch is set to INT.

CH 1-The signal applied to the CH 1 OR X input connector is the source of the trigger signal.

VERT MODE-The internal trigger source is determined by the signals selected for display by the VERTICAL MODE switches.

CH 2-The signal applied to the CH 2 OR Y input connector is the source of the trigger signal.
(28) A TRIGGER LEVEL ControI-Selects the amplitude point on the trigger signal at which the sweep is triggered.
(29) TRIG'D Indicator-The light-emitting diode (LED) illuminates to indicate that the A Sweep is triggered.

SLOPE Switches-Used to select the slope of the signal that triggers the sweep (also refer to TV Signal Displays at the end of Section 2).
$\int$-Sweep is triggered on the positive-going portion of the trigger signal.
-Sweep is triggered on the negative-going portion of the trigger signal.

A TRIGGER MODE Switch-Determines the trigger mode for the A Sweep.

AUTO-Permits triggering on waveforms having repetition rates of at least 20 Hz . Sweep free-runs in the absence of an adquate trigger signal or when the repetition rate is below 20 Hz . The range of the A TRIGGER LEVEL control is automatically set to the peak-to-peak range of the trigger level.

NORM-Sweep is initiated when an adequate trigger signal is applied. In the absence of a trigger signal, no baseline trace will be present. Triggering on television lines is accomplished in this mode.

TV FIELD-Permits triggering on television field signals (refer to TV Signal Displays at the end of Section 2).

B TRIGGER LEVEL Control--Selects the amplitude point on the trigger signal at which the sweep is triggered. When fully clockwise (CW-RUN AFTER DLY), the B Sweep circuit runs immediately following the delay time selected by the A SEC/DIV switch and the B DELAY TIME POSITION control.
(33) VAR HOLDOFF Control-Provides continuous control of holdoff time between sweeps. Increases the holdoff time by at least a factor of four. This control improves the ability to trigger on aperiodic signals (such as complex digital waveforms).

## REAR PANEL

Refer to Figure 2-7 for location of item 34.
(34) EXT Z AXIS Connector-Provides a means of connecting external signals to the Z -axis amplifier to
intensity modulate the crt display. Applied signals do not affect display waveshape. Signals with fast rise times and fall times provide the most abrupt intensity change, and a 5-V p-p signal will produce noticeable modulation. The Z -axis signals must be time-related to the display to obtain a stable presentation on the crt.


Figure 2-7. Rear-panel connector.

## OPERATING CONSIDERATIONS

The following basic operating information and techniques should be considered before attempting any measurements.

## GRATICULE

The graticule is internally marked on the faceplate of the crt to enable accurate measurements without parallax error (see Figure 2-8). It is marked with eight vertical and ten horizontal major divisions. Each major division is divided into five subdivisions. The vertical deflection factors and horizontal timing are calibrated to the graticule so that accurate measurements can be made directly from the crt. Also, percentage markers for the measurement of rise and fall times are located on the left side of the graticule.

## GROUNDING

The most reliable signal measurements are made when the 2215 and the unit under test are connected by a common reference (ground lead), in addition to the signal lead or probe. The probe's ground lead provides the best grounding method for signal interconnection and ensures the maximum amount of signal-lead shielding in the probe cable. A separate ground lead can also be connected from the unit under test to the oscilloscope GND connector located on the front panel.


Figure 2-8. Graticule measurement markings.

## SIGNAL CONNECTIONS

Generally, probes offer the most convenient means of connecting an input signal to the instrument. They are shielded to prevent pickup of electromagnetic interference, and the supplied 10 X probe offers a high input impedance that minimizes circuit loading. This allows the circuit under test to operate with a minimum of change from its normal condition as measurements are being made.

Coaxial cables may also be used to connect signals to the input connectors, but they may have considerable effect on the accuracy of a displayed waveform. To maintain the original frequency characteristics of an applied signal, only high-quality, low-loss coaxial cables should be used. Coaxial cables should be terminated at both ends in their characteristic impedance. If this is not possible, use suitable impedance-matching devices.

## INPUT COUPLING CAPACITOR PRECHARGING

When the input coupling switch is set to GND, the input signal is connected to ground through the input coupling capacitor in series with a $1-\mathrm{M} \Omega$ resistor to form a precharging network. This network allows the input coupling capacitor to charge to the average dc-voltage level of the signal applied to the probe. Thus, any large voltage transients that may accidentally be generated will not be applied to the amplifier input when the input coupling switch is moved from GND to AC. The precharging network also provides a measure of protection to the external circuitry by reducing the current levels that can be drawn from the external circuitry during capacitor charging.

The following procedure should be used whenever the probe tip is connected to a signal source having a different de level than that previously applied, especially if the dclevel difference is more than 10 times the VOLTS/DIV switch setting:

1. Set the AC-GND-DC switch to GND before connecting the probe tip to a signal source.
2. Insert the probe tip into the oscilloscope GND connector.
3. Wait several seconds for the input coupling capacitor to discharge.
4. Connect the probe tip to the signal source.
5. Wait several seconds for the input coupling capacitor to charge.
6. Set the AC-GND-DC switch to AC. The display will remain on the screen, and the ac component of the signal can be measured in the normal manner.

## INSTRUMENT COOLING

To maintain adequate instrument cooling, the ventilation holes on both sides and rear panel of the equipment cabinet must remain free of obstructions.

## OSCILLOSCOPE DISPLAYS

## INTRODUCTION

The procedure in this section will allow you to set up and operate your instrument to obtain the most commonly used oscilloscope displays. Before starting this procedure, verify that the POWER switch is OFF (push button out), then plug the power cord into an approved ac-power-source outlet.

## BASELINE TRACE

First obtain a baseline trace.

1. Preset the instrument front-panel controls as follows:

| Display |  |
| :--- | :--- |
| AUTO INTENSITY | Fully counterclockwise <br> (minimum) <br> AUTO FOCUS |
| Midrange |  |

## Horizontal

$A$ and $B$ SEC/DIV
$A$ and $B$ SEC/DIV
Variable
HORIZONTAL MODE $\times 10$ Magnifier
POSITION
B DELAY TIME
POSITION
A/B SWP SEP

Fully counterclockwise (minimum)
Midrange

AC
CAL detent (fully clockwise)

Off (button out)
Midrange

Locked together at 0.5 ms
CAL detent
(fully clockwise)
A
Off (variable knob in)
Midrange
Fully counterclockwise Midrange

A Trigger

| VAR HOLDOFF | NORM (fully counter- <br> clockwise) |
| :--- | :--- |
| SLOPE | (lever up) |
| LEVEL | Midrange |
| MODE | AUTO |
| A EXT COUPLING | AC |
| A SOURCE | INT |
| A \& B INT | VERT MODE |

## B Trigger

SLOPE $\quad$ (lever up)
LEVEL Fully clockwise
2. Press in the POWER switch button (ON) and allow the instrument to warm up for 20 minutes.
3. Adjust the AUTO INTENSITY control for desired display brightness.
4. Adjust the Vertical and Horizontal POSITION controls to center the trace on the screen.

## SIGNAL DISPLAY

1. Obtain a baseline trace.

[^4]3. Adjust the AUTO INTENSITY control for desired display brightness. If the display is not visible with the AUTO INTENSITY control at midrange, press the BEAM FIND push button and hold it in while adjusting the appropriate VOLTS/DIV switch(es) to reduce the vertical display size. Center the compressed display within the graticule area using the Vertical and Horizontal POSITION controls, then release the BEAM FIND push button.
4. Adjust the A TRIGGER LEVEL control, if necessary, to obtain a stable display.
5. Set the appropriate VOLTS/DIV switch(es) and readjust the Vertical and Horizontal POSITION controls to center the display within the graticule area.
6. Set the A SEC/DIV switch for the desired number of cycles of the displayed signal. Then adjust the AUTO FOCUS control for the best-defined display.

## MAGNIFIED-SWEEP DISPLAY

1. Obtain a Signal Display (see preceding instructions).
2. Adjust the Horizontal POSITION control to move the trace area that is to be magnified to the center of the crt graticule ( 0.5 division on each side of the center vertical graticule line). Change the A SEC/DIV switch setting as required.
3. Pull out the $A$ and $B$ SEC/DIV Variable knob (X10) to obtain sweep magnification.
4. Adjust the Horizontal POSITION control for precise positioning of the magnified display.
5. To calculate the magnified sweep speed, divide the A SEC/DIV switch setting by 10.

## DELAYED-SWEEP DISPLAY

1. Obtain a Signal Display.
2. Select ALT HORIZONTAL MODE. Adjust the appropriate channel POSITION control and the A/B SWP SEP control to display the $A$ trace above the $B$ trace.
3. Adjust the AUTO INTENSITY control as needed to make the intensified zone distinguishable from the remainder of the display. Set the B SEC/DIV switch until the intensified zone is the desired length.
4. Adjust the B DELAY TIME POSITION control to move the intensified zone to cover that portion of the $A$ trace that is to be displayed on the B trace. The B HORIIZONTAL MODE may be used to display the intensified portion of the A Sweep.

## DELAYED-SWEEP MEASUREMENTS

1. Obtain a Signal Display.
2. Select ALT HORIZONTAL MODE. Adjust the appropriate channel POSITION control and the A/B SWP SEP control to display the A trace above the B trace.
3. Adjust the AUTO INTENSITY control as needed to make the intensified zone distinguishable from the remainder of the display. Set the B SEC/DIV switch until the intensified zone is the desired length.
4. Adjust the B DELAY TIME POSITION control to move the intensified zone to the leading edge of the first pulse of interest; then fine adjust until the rising portion is centered at any convenient vertical graticule line.
5. Record the B DELAY TIME POSITION control dial setting.
6. Adjust the B DELAY TIME POSITION control clockwise until the rising portion of the second pulse of interest is positioned to the same vertical reference line selected in step 4.
7. Record the B DELAY TIME POSITION control dial setting.
8. Use the following formula to calculate the time difference:
$\begin{gathered}\text { Time } \\ \text { Difference } \\ \text { (delayed sweep) }\end{gathered}=\left(\begin{array}{cc}\text { second } & \text { first } \\ \text { dial } & - \\ \text { dial } \\ \text { setting } & \text { setting }\end{array}\right)\left(\begin{array}{c}\text { A SEC/DIV } \\ \text { switch setting } \\ \text { (delay time) }\end{array}\right)$

## X-Y DISPLAY

1. Obtain a baseline trace.
2. Use equal-length coaxial cables, or the two 10 X probes supplied with the instrument, to apply the horizontal signal ( $X$-axis) to the CH 1 OR $X$ input connector and to apply the vertical signal ( Y -axis) to the CH 2 OR $Y$ input connector.
3. Select $X$ - $Y$ mode by switching the A SEC/DIV switch to its fully counterclockwise position.
4. Advance the AUTO INTENSITY control setting until two dots are displayed. The display can be positioned horizontally with the Horizontal POSITION control and vertically with the Channel 2 POSITION control.

## NOTE

The display obtained when sinusoidal signals are applied to the $X$ - and $Y$-axis is called a Lissajous figure. This display is commonly used to compare the frequency and phase relationships of two input signals. The frequency relationship of the two input signals determines the pattern seen. The pattern will be stable only if a common divisor exists between the two frequencies.

## TV SIGNAL DISPLAYS

Displaying a TV Line-rate Signal

1. Perform the steps and set the controls as outlined under Baseline Trace and Signal Display to obtain a basic display of the desired TV signal.
2. Set A SEC/DIV to $10 \mu \mathrm{~s}$, and A \& B INT to CH 1 or CH 2 as appropriate for applied signal.
3. Set A TRIGGER SLOPE for a positive-going signal (lever up) if the applied TV signal sync pulses are positivegoing, or for a negative-going signal (lever down) if the TV sync pulses are negative-going.
4. Adjust the A TRIGGER LEVER control for a stable display, and AUTO INTENSITY for desired display brightness. If necessary, adjust VERTICAL VOLTS/DIV control to obtain 5 divisions or greater amplitude for a stable display.

## Displaying a TV Field-rate Signal

1. Perform Step 1 under Displaying a TV Line-rate Signal.
2. Set A SEC/DIV to 2 ms , A TRIGGER MODE to TV FIELD and A \& B INT to CH 1 or CH 2 as appropriate for the applied signal.
3. Perform Step 3 and 4 under Displaying a TV Line-rate Signal.
4. To display either Field 1 or Field 2 individually at faster sweep rates (displays of less than one full field), set VERTICAL MODE to BOTH and ALT simulataneously. This synchronizes the Channel 1 display to one field and the Channel 2 display to the other field.

To change the field that is displayed, interrupt the triggering by repeatedly setting the AC GND DC switch to GND or disconnecting the signal from the applied signal input until the other field is displayed. To display both fields simultaneously, apply the input signal to both the CH 1 and CH 2 inputs via two probes, two cables, or through a dual-input coupler.

To examine either a TV Field-rate or Line-rate signal in more detail, either the X10 Magnifier or HORIZONTAL MODE functions may be employed as described for other signals elsewhere in this manual.
5. To display a selected horizontal line, first trigger the sweep on a vertical (field-rate) sync pulse, then use the delayed sweep to delay out to that line for close examination. This procedure is useful for examining VITS signals.

# THEORY OF OPERATION 

## INTRODUCTION

## SECTION ORGANIZATION

This section contains a functional description of the 2215 Oscilloscope circuitry. The discussion begins with a general summary of instrument functions followed by a detailed description of each major circuit. Functional block diagrams and schematic diagrams are used to show the interconnections between parts of the circuitry, to indicate circuit components, and to identify interrelationships with the front-panel controls.

Schematic diagrams and the overall block diagram are located in the tabbed "Diagrams" section at the back of this manual. The schematic diagram associated with each description is identified in the text and indicated on the tab of the appropriate foldout page by a numbered diamond symbol. For best understanding of the circuit being described, refer to both the appropriate schematic diagram and the functional block diagram.

## INTEGRATED CIRCUIT DESCRIPTIONS

## Digital Logic Conventions

Digital logic circuits perform many functions within the instrument. Functions and operation of the logic circuits are represented by logic symbology and terminology. Most logic functions are described using the positive-logic convention. Positive logic is a system of notation whereby the more positive of two levels is the TRUE (or 1) state; the more negative level is the FALSE (or 0 ) state. In this logic description the TRUE state is referred to as HI, and the FALSE state is referred to as LO. The specific voltages which constitute a HI or a LO state vary between specific devices. For specific device characteristics, refer to the manufacturer's data book.

## Linear Devices

The functioning of individual linear integrated circuit devices in this section use waveforms or other techniques such as voltage measurement and simplified diagrams to illustrate their operation.

## GENERAL DESCRIPTION

In the following overall functional description of the 2215 Oscilloscope, refer to the basic block diagram (Figure 3 -1) and to the detailed block diagram (Figure 9-4) located in the "Diagrams" section of this manual. In Figures 3-1 and 9-4, the numbered diamond symbol in each major block refers to the appropriate schematic diagram number.

Signals to be displayed on the crt are applied to either the CH 1 OR $X$ input connector or the $\mathrm{CH} 2 \mathrm{OR} Y$ input connector. The signals may be coupled to the attenuator circuit either directly ( $D C$ ) or through an input-coupling capacitor (AC). The input may also be disconnected and the input to the attenuators grounded when the GND position of the coupling switch is used. In the GND position, the ac-coupling capacitor is allowed to precharge to the dc level present at the input connector. This precharging prevents large trace shifts of the display when switching from GND to $A C$ coupling.

Each channel output signal from the Attenuator circuitry is applied to the Vertical Preamplifier circuitry for further amplification. The Channel 2 Preamplifier includes an Invert feature that allows the operator to invert the Channel 2 signal display on the cathode-ray tube (crt). Trigger Pickoff Amplifiers in each channel supply an internal trigger signal from either channel signal or from both channels to the Internal Trigger Amplifier in the Trigger circuitry.

Each channel signal is selected for display in turn by the Channel Switching Logic circuit under control of the front-panel VERTICAL MODE switches. The output signal from the Channel Switching Logic circuit is applied to a Diode Gate circuit. The Diode Gate circuit switches either channel signal (or both signals for ADD) to a Delay Line Driver stage that supplies the proper drive and impedance match to the Delay Line. The Delay Line produces approximately 100 ns of delay in the vertical signal to allow the Horizontal circuitry time to produce the necessary sweep to display the signal.

Final amplification of the vertical signal is supplied by the Vertical Output Amplifier. The Vertical Output Amplifier supplies the required signal levels necessary to produce vertical deflection of the electron beam in the crt.

The A/B Sweep Separation circuitry supplies a dc-offset current to the Vertical Output signal which is used to
vertically position the B trace with respect to the A trace when ALT HORIZONTAL MODE is selected.

The Trigger circuitry uses either the Internal Trigger signal derived from the input signal(s), an External Trigger signal, or a Line Trigger signal obtained from the ac-powersource input waveform to develop the triggering signal for the Sweep Generator. An Auto Trigger circuit ensures that the range of the A TRIGGER LEVEL control conforms approximately to the peak-to-peak amplitude of the trigger signal when either AUTO or TV FIELD TRIGGER MODE is selected. In NORM MODE, the A TRIGGER LEVEL control must be adjusted for the correct trigger signal level before a sweep can be generated.

A TV Field sync circuit provides stable triggering on television-signal vertical-sync pulses. Triggering at the television line rate is accomplished when either AUTO or NORM MODE is used.

The Sweep Logic circuit controls the generation of the sweep and the unblanking of the Z-Axis Amplifier for the A Sweep display. When the A TRIGGER MODE switch is set to either AUTO or TV FIELD and no trigger signal is present, the Auto Baseline circuit causes the Sweep Logic circuit to produce a sweep after a period of time. In the NORM position of the A TRIGGER MODE switch the Auto Baseline circuit is disabled, and a sweep will not be generated until a triggering signal is received.

A gate signal produced by the A Sweep Logic circuit is applied to the A Miller Sweep circuit. This circuit produces a linear sweep output with a run-up time that is controlled by the A SEC/DIV switch. The sweep signal is applied to the Horizontal Preamplifier for initial amplification. Final amplification of the sweep signal to drive the crt horizontal deflection plates is provided by the Horizontal Output Amplifier.

The Horizontal Preamplifier gain is increased by a factor of 10 when the X10 Magnifier feature is used. Horizontal positioning of the display is also accomplished in the Horizontal Preamplifier circuit.

In the $X \cdot Y$ Mode of operation the CH 1 signal, via the Internal Trigger circuitry, is applied to the XY Amplifier where it is amplified for application to the Horizontal Preamplifier. In this operating mode, the CH 1 Internal


Figure 3-1. Basic block diagram of the 2215 Oscilloscope.

Trigger signal supplies the horizontal deflection to the crt, and a sweep signal is not produced by the Miller Sweep circuit.

The ALT HORIZONTAL MODE and the B HORIZONTAL MODE displays are controlled by circuitry contained in the Alternate B Sweep circuit. The circuit includes the B Miller Sweep Generator and the B Sweep Logic circuitry. In addition to providing the B Sweep sawtooth waveform, control signals are generated to control the display switching between the $A$ display and the $B$ display and to control the B Z-Drive signal for the alternated A Intensified Sweep and the B Sweep.

The Z-Axis drive from both the A Sweep Logic circuit and the Alternate B Sweep circuit is applied to the Z-Axis amplifier. The output signal from the $Z$-Axis Amplifier circuit sets the crt intensity. A Chop Blanking signal from the Chop Oscillator circuit blanks the crt display during the transition between the vertical channels when using CHOP VERTICAL MODE.

The DC Restoration circuit raises the output level of the Z-Axis Amplifier to allow it to be coupled to the crt control grid. Direct coupling is not employed due to the amplitude of the voltage levels applied to the crt elements.

The $\bar{A}$ Duty and the $\bar{B}$ Duty signals from the $A$ Sweep Logic and Alternate B Sweep circuits are applied to the Auto Intensity circuit. The Auto Intensity circuit provides partial control of the intensity of the display when switching between different positions of the SEC/DIV switches.

The Power Supply provides all the necessary operating voltages for the instrument circuitry. Operating potentials are obtained from a circuit composed of the Preregulator, Inverter and Transformer, and Rectifiers and Filters. The Preregulator produces approximately +45 V from the ac-power-input source which is used to drive the $20-\mathrm{kHz}$ Inverter stage. The Transformer secondary windings provide various ac levels that are rectified and filtered to produce the operating voltages. A High-voltage Multiplier circuit produces the accelerating, focus, and cathode potentials required by the crt.

A front-panel PROBE ADJUST output is provided for use in adjusting probe compensation. The voltage at the PROBE ADJUST connector is a negative-going square wave that has a peak-to-peak amplitude of approximately 0.5 V and a repetition rate of approximately 1 kHz .

## DETAILED CIRCUIT DESCRIPTION

## VERTICAL ATTENUATORS

Both the Channel 1 and Channel 2 Attenuator circuits, shown in Diagram 1, are identical in operation. In the following discussion, only the Channel 1 Attenuator circuit is described. The matching components in the Channel 2 Attenuator circuit perform the same function.

The Attenuator circuit (see Figure 3-2) provides control of input coupling, vertical deflection factor, and variable volts-per-division balance. Input signals for crt vertical deflection may be connected to either or both the CH 1 OR $X$ and the CH 2 OR $Y$ input connectors. In the $X \cdot Y$ Mode of operation, the signal applied to the CH 1 OR $X$ connector provides horizontal (X-Axis) deflection for the display, and the signal applied to the CH 2 OR $Y$ connector provides the vertical (Y-Axis) deflection for the display.

## Input Coupling

The signal applied to the CH 1 OR X input connector can be ac-coupled, dc-coupled, or internally disconnected from the input of the High-Z Input Attenuator circuit. Signals applied to the CH 1 input connector are routed through resistor R101 to Input Coupling switch S101. When S101 is set for de coupling, the CH 1 signal is applied directly to the input of the High-Z Attenuator stage. When ac-coupled, the input signal passes through R100 and dc-blocking capacitor C102. The blocking capacitor prevents the dc component of the input signal from being applied to the Attenuator circuit. When S101 is set to GND, the direct signal path is opened and the input of the attenuator is connected to ground. This provides a ground reference without the need to disconnect the applied signal from the input connector. The coupling capacitor is allowed to precharge through R102, a high-resistance component, which is connected across Input Coupling switch S101 in the GND position.


Figure 3-2. Detailed block diagram of the Channel 1 attenuator and attenuator switching tables.

## High-Z Attenuator

The first section of attenuator switch S105A directs the input signal to one of three paths: directly through R103 (no attenuation); through a 10 X attenuator consisting of C105, C107, R105, R106, R107, and R108; or through a 100 X attenuator consisting of C111, C112, R110, R111, R112, R114, and R115. Medium-frequency normalization of the input capacitance is accomplished by C104 in the 10X attenuator and by C110 in the 100X attenuator. Switch S105B connects the appropriate attenuator output to the input of the Buffer Amplifier.

## Buffer Amplifier and Low-Z Attenuator

The Buffer Amplifier presents a high-impedance, lowcapacitance load to the input signal and delivers an accurate replica of that signal to a low-impedance buffer output circuit. The Low-Z output circuit is composed of a $250-\Omega$
voltage-divider network (R139F through R139J) and the Volts/Div Var circuit (R141, G141, and R143). Switch S105B selects the appropriate output from the voltage divider. The Buffer Amplifier contains two paths: a slow path consisting of R116, R117, U120, and R119 in parallel with C119; and a fast path through C121. The signals through both paths are applied to the gate of Q122.

In the slow-path portion, the input signal is divided by ten by the combination of R117 and R116 and is then applied to $U 120$ pin 3. The Buffer Amplifier output signal is also divided by ten by the combination of R139B, R139C, R139D, and R139N. Sufficient dc-gate bias for input FET Q122 is generated by the slow-path circuit to produce a null (zero difference) between pins 2 and 3 of U120. The closed-loop gain of the slow path is matched to the fast-path gain. If the average output voltage from the
fast path changes, transconductance amplifier U120 adjusts the dc gate bias on Q122 to keep U120 pin 2 and U120 pin 3 nulled. This action keeps the slow-path and the fast-path gains matched. Resistor R119 isolates the output impedance of U120 from the input of FET Q122. This isolation, in combination with the high input impedance of U120, prevents high-frequency loading of the input signal. Capacitor C119 compensates for the output capacitance of U120.

Step Balance potentiometer R138 (at pin 1 of R139) is adjusted to compensate for input offsets reaching U120 pins 2 and 3 when switching between VOLTS/DIV switch positions.

In the fast path, the input signal is ac-coupled to input FET Q122 through C121. The input FET is arranged in a source-follower configuration used to drive complementary emitter followers Q133 and Q134. The combination of Q125, R126, R131, R132, VR130, and R130 sets a constant current through R125 in the source lead of Q122. The voltage drop across R125 biases Q133 and Q134 for about a $10-\mathrm{mA}$ idle current.

A bootstrap circuit composed of Q139, VR122, and R122 connects the Q 122 drain to the Q 122 source. This circuit forces the bias voltage across $Q 122$ to remain constant, which in conjunction with the constant bias current supplied by R125, keeps 0122 operating at a constant power level to prevent distortion due to changing signal currents.

Complementary emitter followers Q133 and Q134 supply drive current to the $\div 1, \div 2.5$, and $\div 5$ voltage dividers and provide impedance matching between input FET Q122 and the divider network. The bias levels of Q133 and Q134 are stabilized by emitter resistors R139A and R139E respectively. Average voltage changes occurring in the output of Q133 and Q134 are sensed through R139B and R139D which are connected to the point of lowest impedance (the emitters of Q133 and Q134). Resistor R139C provides a path that completes the feedback loop to the slow-path portion of the Buffer Amplifier.

## Volts/Div Var Circuit and X1/X10 Amplifier

The appropriate voltage divider signal output $(\div 1, \div 2.5$, or $\div 5$ ) is selected by VOLTS/DIV switch S105B and routed to the Volts/Div Var circuit composed of C141, R141, and R143. Changes that occur in the Buffer Amplifier output impedance due to setting R141 or switching the divider output are sensed via R139M. These changes modify the slow-path feedback signal to cause U120 to again match the gain of both paths.

From the Volts/Div Var circuit, the signal is applied to the input of the $\mathrm{X} 1 / \mathrm{X} 10$ Switchable-gain Amplifier U145. Amplifier U145 produces a differential output signal from the single-ended input signal. The gain of the amplifier is controlled by the setting of VOLTS/DIV switch S105.

Amplifier gain is changed by switching between two pairs of transistor amplifiers contained in U145. Gain of the X10 amplifier pair is adjusted by R145 to obtain the correct deflection factor for the $2 \mathrm{~m}, 5 \mathrm{~m}$, and 10 m VOLTS/ DIV switch positions. Resistors R146, R147, and R148 act to balance any dc offsets between the X 1 and X 10 amplifiers. Trace shift occurring when the VOLTS/DIV Variable control is rotated is minimized by resistor R142 which stabilizes the input bias current to U145.

## VERTICAL PREAMPS

The Channel 1 and Channel 2 Preamp circuitry, shown in Diagram 2, includes the vertical preamplifiers, the internal trigger pickoff amplifiers, and a common-base output stage for each channel. Vertical positioning of the channel display is incorporated in the common-base amplifier stage.

## Channel 1 Vertical Preamplifier

The Channel 1 Vertical Preamplifier produces differential output signals to drive the Vertical Output Amplifier and internal trigger signals to drive the Trigger circuitry.

Differential signal current from the Attenuator circuitry is applied to common-base transistors Q157 and Q167 through cable-terminating resistors R151 and R161 respectively. The collector currents of Q157 and Q167 will flow through R158 and R168 to produce level-shifted signals which drive U170D and U170E. Balance potentiometer R154 is adjusted to balance the dc level of the Channel 1 output with the Channel 2 output by setting the bias levels of Q157 and Q167. Channel 1 frequency response is matched to Channel 2 response by adjusting C167.

Transistors U170D and U170E form a common-emitter amplifier. The gain of U170D and U170E is set by R180 (connected between the emitters), and the high-frequency response is compensated by C 180 . The emitters are also connected to the bases of U170C and U170B respectively to provide an internal trigger signal pickoff point. Vertical signal output current flows from the collectors of U170D and U170E to the emitters of common-base amplifiers Q177 and Q187. A shunt resistor gain network (R176 and R186) sets the gain of the common-base stage. Channel 1 POSITION control R190 supplies a variable offset current to the emitters of Q177 and Q187 which allows the trace
to be vertically positioned on the crt. The common-base amplifier stage converts the differential signal input current to a differential signal output voltage that is applied to the Diode Gate circuitry (Diagram 3).

## Channel 2 Vertical Preamplifier

The Channel 2 Vertical Preamplifier functions the same as the Channel 1 Vertical Preamplifier previously described, with the exception of an additional pair of transistors that performs the inverting function. In the Normal mode of operation, Q257 and Q267 are biased on and Q258 and Q268 biased off by INVERT switch S264 grounding one end of R263. In the Invert mode (INVERT switch pressed in), cross-wired transistors Q258 and Q268 are biased on and Q257 and Q267 biased off by grounding the junction of R256 and R266. Invert Bal potentiometer R264 is adjusted to correct for $d c$ offsets between the two switching-transistor pairs. When R264 is correctly adjusted, a baseline trace will maintain the same vertical position as the amplifier is switched between Invert and Normal.

## Internal Trigger Pickoff Amplifier

The Internal Trigger Pickoff Amplifier supplies trigger signals to the Internal Trigger Amplifier in the Trigger circuitry (Diagram 4). Internal trigger signals are provided by the vertical preamplifiers and are applied to the bases of U170B and U170C (for Channel 1) and U270B and U270C (for Channel 2). These transistor pairs are biased on, either individually or together, from the Internal Trigger Switching Logic circuit (Diagram 3).

When Channel 1 is the selected internal trigger source, Q173 and U170A (CH 1) will be biased on and Q273 (CH 2) biased off. Current flowing through R173, R183, and R197 will bias on U197A to keep U197E cut off. Emitter current is supplied to U170A by U197D. In turn, U170A then supplies emitter current to U170B and U170C to enable the Channel 1 internal trigger signals to pass to the Internal Trigger Amplifier.

When Channel 2 is selected as the internal trigger source, Q273 and U270A will be biased on and Q173 biased off. Transistor U197A will remain on, and current supplied by U197D will supply emitter current to U270A. Then U270A in turn supplies the emitter current to U270B and U270C and enables the Channel 2 internal trigger signals to pass to the Internal Trigger Amplifier.

The actual signal source selected when the A TRIGGER $A \& B$ INT switch is set to VERT MODE depends on the setting of the VERTICAL MODE switches. If either CH 1 or CH 2 VERTICAL MODE is selected, the preceding discussion on Channel 1 or Channel 2 internal trigger signals applies. When the VERTICAL MODE switch is set
to BOTH, the VERTICAL MODE ADD-ALT-CHOP switch setting determines the switching action for selecting the internal trigger source.

Selecting ADD VERTICAL MODE causes both internal trigger-select signals ( $\overline{\mathrm{CH} 1 \text { Trig and }} \overline{\mathrm{CH} 2 \text { Trig }}$ ) to be LO, and both Q173 and Q273 are biased off. Transistor U197A then becomes biased off causing U197E to saturate. With U197E saturated, emitter current is supplied to both Channel 1 and Channel 2 Trigger Pickoff Amplifiers (U170C and U170B for Channel 1 and U270B and U270C for Channel 2) via R196-CR196 and R296-CR296 respectively. When both pickoff amplifiers are enabled, the resulting trigger signal is the sum of the Channel 1 and Channel 2 internal trigger signals. The sum of the current supplied by U197E to both pickoff amplifiers is the same magnitude as the current from U197D when either CH 1 or CH 2 is selected individually. Therefore, the dc output to the Internal Trigger Amplifier will be the same for CH 1 , CH 2 , and $A D D$ VERTICAL MODE trigger signals.

When ALT VERTICAL MODE is selected with the previously established settings (VERTICAL MODE to BOTH, A \& B INT to VERT MODE, and A SOURCE to (NT), the internal trigger-select signals alternate between channels. On one sweep the Channel 1 internal trigger will be selected as previously described. On the alternate sweep, Channel 2 internal trigger will be selected, again as previously described.

Under the same switch-setting conditions, selecting CHOP VERTICAL MODE produces the same triggerselection conditions as described for ADD VERTICAL MODE. The sum of the Channel 1 and Channel 2 internal trigger signals will be passed to the Internal Trigger Amplifier. See the "Internal Trigger Switching Logic" discussion for a description of how the internal trigger selection signals are generated.

## CHANNEL SWITCH AND VERTICAL OUTPUT

The Channel Switch circuitry, shown on Diagram 3, selects the input signal or combination of input signals to be connected to the Vertical Output Amplifier. By setting the logic input into the Channel Switching Logic circuit, VERTICAL MODE switches S315 and S317 select the input signal combinations to be displayed. The internal trigger-select signals are also generated in the Channel Switch circuitry.

## Diode Gates

The Diode Gates, consisting of eight diodes, act as switches that are controlled by the Channel Switching

Logic circuitry. The Q - and $\overline{\mathrm{Q}}$-outputs of U317A (pins 5 and 6 respectively) control forward biasing of the diodes to turn the gates on and off.

CHANNEL 1 DISPLAY ONLY. To display only the Channel 1 signal, the CH 1 Enable signal (U317A pin 5) is HI and the CH 2 Enable signal (U317A pin 6) is LO.

With CH 1 Enable HI, gate diodes CR187 and CR177 are reverse biased (see Figure 3-3). Series gate diodes CR 188 and CR178 are forward biased, and the Channel 1 vertical signal is allowed to pass to the Delay Line Driver. A LO CH 2 Enable signal applied to the Channel 2 gate diodes forward biases CR287 and CR277, and the Channel 2 vertical-signal current is shunted away from series diodes CR288 and CR278. The Channel 2 series diodes are reverse biased, and the Channel 2 signal current is prevented from reaching the Delay Line Driver.

CHANNEL 2 DISPLAY ONLY. When CH 2 VERTICAL MODE is selected, the CH 1 Enable signal goes LO and the

CH 2 Enable signal goes HI. The Channel 1 signal is blocked, and the Channel 2 signal reaches the Delay Line Driver.

ADD DISPLAY. Both Diode Gates are biased on to pass the Channel 1 and Channel 2 vertical signals. The channel signal currents are summed at the input to the Delay Line Driver. The Add Enable signal supplies the extra current required to keep both Diode Gates forward biased and to maintain the proper dc level at the base of the Delay Line Driver input transistors (Q331 and Q341).

ALTERNATE AND CHOPPED DISPLAY. The Diode Gates are switched on and off by the Channel Enable signals from the Channel Switching Logic circuit. When ALT VERTICAL MODE is selected, the Diode Gates are switched at the end of each trace. For CHOP VERTICAL MODE, the gates are switched at a rate of about 250 kHz .

X-Y DISPLAY. Setting the A SEC/DIV switch to the $X-Y$ position activates the $X-Y$ display feature. The


Figure 3-3. Diode gate biasing for a Channel 1 display.

Channel 1 Diode Gate is held off, and the Channel 2 Diode Gate is biased on. The Channel 2 signal is passed to the Delay Line Driver and ultimately to the crt to provide the $Y$-Axis display deflection. The $X$-Axis deflection signal is supplied to the XY Amplifier (Diagram 7) from the Channel 1 signal via the Internal Trigger Amplifier (Diagram 4).

## Delay Line Driver

The Delay Line Driver converts the signal current from the Diode Gates into a signal voltage for application to the Delay Line. The Delay Line Driver is configured as a differential shunt feedback amplifier and is composed of Q331, Q335, Q341, and Q345. Input currents to commonemitter transistors Q331 and Q341 are converted to voltages at the bases of 0335 and 0345 respectively. Emitter-follower output transistors Q335 and Q345 then drive the Delay Line through reverse terminations R335C335 and R345-C345. Amplifier compensation is provided by R340 and C340, and shunt feedback is supplied by R336 and R345.

## Delay Line

Delay Line DL350 provides about 100 ns of delay in the vertical signal. When using internal triggering $(\mathrm{CH} 1$, CH 2 , or VERT MODE), the delay time allows the Sweep Generator sufficient time to produce a sweep before the vertical signal reaches the crt deflection plates. This feature permits viewing the leading edge of the internal signal that originates the trigger pulse.

## Vertical Output Amplifier

The Vertical Output Amplifier, also shown on Diagram 3, provides final amplification of the input signals for application to the deflection plates of the crt. Signals from the Delay Line are applied to a differential amplifier input stage composed of Q350 and Q360. The Delay Line is terminated in the proper impedance by resistors R338 and R348. Resistor R355 sets the gain of Q350 and Q360. Thermal compensation of the stage gain is provided by thermistor RT356, connected in series with R356 across R355. The RC networks connected across R355 provide both low- and high-frequency compensation of the stage.

The differential output is applied to output transistor pairs Q376-Q377 and Q386-0387. These transistors form a common-emitter shunt-feedback amplifier stage, with R376, R377, R386, and R387 serving as feedback elements. Capacitors C377 and C387, connected across R377 and R387 respectively, provide increasing negative feedback as the signal frequency rises to limit the amplifier bandwidth at the upper frequency limit. Output voltage from the amplifier is divided between the two transistors of each half. The signal voltage applied to the crt vertical deflection
plates is the sum of voltage drops across the pairs (O376Q377 and Q386-0387). The deflection voltage is proportional to the signal current driving the bases of Q 376 and Q386.

BEAM FIND switch S390 (Diagram 6) normally supplies -8.6 V directly to R 390 to set the stage bias. When the BEAM FIND button is pressed in and held, the direct voltage is removed and the $-8.6-\mathrm{V}$ bias is provided via series resistor R391. The output voltage swing is thereby reduced to hold the vertical trace deflection to within the graticule area.

## A/B Sweep Separation Circuit

The circuit composed of 0370, 0380, Q392, and associated components provides a means of vertically positioning the $B$ trace with respect to the $A$ trace during ALT HORIZONTAL MODE displays. The $\overline{\text { Sep }}$ signal, provided by the Alternate Display Switching circuitry (Diagram 10), supplies the biasing voltage for Q392. During the $B$ trace display portion of the Alternate Horizontal display, $\overline{\mathrm{Sep}}$ is LO and Q 392 is biased off. This action allows A/B SWP SEP potentiometer R395 to affect the bias on one side of a differential amplifier composed of Q380 and Q370. The differential amplifier supplies a dc offset current to the Vertical Output signal that changes the position of the $B$ trace on the crt face.

During the $A$ trace portion of the Alternate Horizontal display, $\overline{\mathrm{Sep}}$ is HI and Q 392 is biased on. The base voltage on O380 then equals the base voltage on O370. With equal base voltages, the differential amplifier supplies equal current to both sides of the Vertical Output signal and no offset to the A trace occurs.

## Channel Switching Logic Circuit

The Channel Switching Logic circuitry composed of U310A and U317A selects either Channel 1 or Channel 2 and various display modes for crt display via front-panel switches and the X-Y position of the A SEC/DIV switch.

When the instrument is not in the $X-Y$ Mode, signal line $X Y$ is grounded through contacts on the A SEC/DIV switch (Diagram 8). This action establishes LO logic levels on pins C, B, and G of front-panel switch S317 (CH 1-BOTH-CH 2) and on pins C and B of S305 (A \& B INT).

Switch S317 selects the vertical channel signal that drives the Delay Line Driver via the Channel Diode Gates. With S317 set to CH 1, a LO is applied to the Set input (pin 4) of U317A. Flip-flop U317A will then be set, and the Q output ( pin 5 ) will be HI. Pin 5 of U317A is the CH 1 Enable signal line, and when it is HI , the Channel 1 vertical
signal is gated to the Delay Line Driver. When S 317 is set to CH 2, the Reset input of U317A (pin 1) will be held LO through CR705. The CH 2 Enable signal (U317A, pin 5) is then set HI and the Channel 2 vertical signal is gated to the Delay Line Driver.

Setting S317 to the BOTH position removes the LO from both the Set and Reset inputs of U317A. This action allows the channel selected for display to be determined either by the logic level applied to the $D$ input (pin 2 ) and the clock applied to pin 3 or by the logic level applied to the Set and Reset inputs from the ADD-ALT-CHOP switch.

The ADD-ALT-CHOP switch ( $\$ 315$ ) is enabled by the LO placed on pins A, C, and F when the CH 1-BOTH-CH 2 switch is set to BOTH. When in ADD, S315 holds both the Set and Reset input of U317A LO through CR706 and CR701 respectively. The Q and $\overline{\mathrm{O}}$ outputs of U317A will then be HI , and both Channel 1 and Channel 2 vertical signals are gated to the Delay Line Driver. The signal current is summed at the input to the Delay Line Driver, and the resulting oscilloscope Add vertical display is the algebraic sum of the two vertical signals.

The Add Enable circuit, composed of Q316, U197C, and U315A, is activated when both Diode Gates are turned on for an Add vertical display. With the Q and $\overline{\mathrm{Q}}$ outputs of U317A HI, the output of U315A will be LO and transistor Q316 is biased on. The collector of Q316 rises toward +5 V and U197C is biased on. Transistor U197C supplies the additional current required to keep both Diode Gates forward biased and to supply the proper dc level to the Delay Line Driver input. Bypass capacitor C316 prevents switching transients from being introduced into the Delay Line Driver by the Add Enable circuit.

When S315 is set to ALT, a HI is placed on both the Set and Reset inputs of U317A. Flip-flop U317A will transfer the logic level on the $D$ input (pin 2) to the O output (pin 5) on each clock-pulse rising edge. Pin 1 of NAND-gate U310A is held HI by the Chop Oscillator output, and pin 2 follows the $\overline{\mathrm{Alt} \text { Sync }}$ signal produced by the Holdoff circuitry in the A Sweep Generator (Diagram 5). The output of U310A (pin 3) is therefore an inverted $\overline{\text { Alt Sync }}$ pulse. The signal on the $D$ input of U317A (pin 2) follows the logic level set by the $\overline{\mathrm{Q}}$ output pin. As each clock pulse occurs, the states of the Q and $\overline{\mathrm{Q}}$ outputs reverse (toggle), enabling Channel 1 and Channel 2 Diode Gates alternately with each sweep.

CHOP OSCILLATOR. Setting S315 to CHOP enables the Chop Oscillator and the Chop Blanking circuit. Pins C and D of S315 are connected to place a LO logic level on
the Set input (pin 10) of U317B. The Q output of U317B is set HI and the Chop Oscillator is allowed to run. A HI level is present on U310D pin 13 due to C308 being charged to the HI level on U310D pin 11. When pin 12 of U310D also goes HI, the output of U310D goes LO. Capacitor C308 now must discharge to the new dc level. As soon as the charge of C308 reaches the LO threshold level of U310D, the output at pin 11 switches HI again and C308 charges toward the HI logic level (see Figure 3-4).

When the HI switching threshold level is reached, the output of U301D changes states to LO again. This cycle continues at about 500 kHz to produce both the Chop Clock and the Chop Blank signals.

The Chop signal is gated through NAND-gate U310C and applied to U310A pin 1. The $\overline{\text { Alt Sync }}$ pulse on U310A pin 2 is HI (except during holdoff time) so the output of U310A pin 3 is the inverted Chop Oscillator signal on pin 1. This signal is applied to the Clock Input (pin 3) of U317A to drive the Channel Switching circuitry. Since flipflop U317A clocks with rising edges only, the frequency of the chopped channel switching is about 250 kHz .

The signal output from U310C pin 8 is also fed to the Chop Blanking circuit. Capacitor C311 and resistors R310 and R311 form a differentiating circuit that produces positive and negative short-duration pulses when the Chop Oscillator signal changes levels.

The dc level at U310B pins 4 and 5 is set slightly above the HI switching threshold logic by a voltage divider consisting of R310 and R311. Positive pulses from C311 continue to hold U310B above the threshold level, so the output remains LO. Negative pulses from C311 drop below the threshold level of U310B, and the output of U310B switches HI for a duration of about $0.4 \mu \mathrm{~s}$ (see Figure 3-4) to produce the positive Chop Blanking pulse. The Chop Blanking pulse is fed to the Z-Axis Amplifier and is used to prevent display of the transistions when switching between vertical channels.

## Internal Trigger Switching Logic

Internal trigger-selection signals to the Trigger Pickoff Amplifier (Diagram 2) are produced in a logic circuit composed of U305B, U305C, U305D, U315B, and U315C. The A \& B INT Trigger Source switch (S305), in conjunction with $\mathrm{CH} 1-\mathrm{BOTH}-\mathrm{CH} 2$ switch (S317), determines the internal trigger source selected. When either the CH 1 or CH 2 Internal Trigger signal is selected by S305, the selected channel will be the internal trigger source. When VERT MODE is selected as the internal trigger signal, the position of S317 determines the channel (s) selected as the internal trigger source.


Figure 3-4. CHOP VERTICAL MODE waveforms.

CHANNEL 1 SOURCE. The $X Y$ signal line from the $A$ SEC/DIV switch (S630B) applies a LO logic level to A \& B INT switch S 305 on pins B and C . In the CH 1 position, the LO is coupled from pin $C$ to pin D and applied to U305B pin 4.

The LO is gated through U305B and applied to the $\overline{\mathrm{CH} 1}$ $\overline{\text { Trig signal line in a wired AND connection. The LO from }}$ U305B is applied to Q273 in the Channel 2 Internal Trigger Pickoff Amplifier (Diagram 2) to bias it off, thus preventing the Channel 2 signal from being selected. Operation of the Internal Trigger Pickoff Amplifiers is discussed in the "Channel 1 and Channel 2 Preamps" circuit descriptions.

Concurrently, pins 9 and 10 of U305C are pulled HI through R304 and R300 respectively to place a HI at U 305 C pin 8. The HI from U305C to the wired-AND connection on the $\overline{\mathrm{CH}} 2$ Trig signal line enables the output of U315B to control the logic level of the $\overline{\mathrm{CH}} 2$ Trig signal. Control is accomplished by the logic levels on the inputs of U305D, pins 12 and 13.

The LO on U305B pin 4 (placed there by S305) also occurs on U305D pin 13. This ensures a LO at U305D pin 11, which is applied to U315C pin 9 and to U315B pin 5. The logic level applied to U315C pin 9 has no effect on the CH 1 Trig signal because a LO is already present at the wired-AND connection to the signal line. However, the

LO applied to U315B pin 5 ensures that the output of U 315 B is HI . When the $\overline{\mathrm{CH}} 2$ Trig signal is $\mathrm{HI}, \mathrm{Q} 173$ in the Channel 1 Internal Trigger Pickoff Amplifier is biased on and the Channel 1 signal is passed to the Internal Trigger Amplifier (Diagram 4).

CHANNEL 2 SOURCE. When S305 is set to CH 2, the LO logic level present on S305 pin B is coupled to pin A and applied to U305D pin 12 and to U305C pin 10. The output of U305C at pin 8 is a LO which is applied to the $\overline{\mathrm{CH}} 2$ Trig signal line by the wired-AND connection. When the $\overline{\mathrm{CH}} 2$ Trig signal is LO, the Channel 1 Internal Trigger Pickoff Amplifier is biased off to prevent the Channel 1 signal from reaching the Internal Trigger Amplifier.

The inputs to U305B, pins 4 and 5, are both pulled HI through R305 and R304 respectively, and the HI output from pin 6, applied to the wired-AND connection on the
 $\overline{\text { Trig }}$ signal logic level. As described in the preceding "Channel 1 Source" discussion, the logic levels at U305D pins 12 and 13 control the output of U315B. The LO on U305D pin 12 ensures a LO output at pin 11, which is applied to U315C at pin 9. This LO ensures a HI output at U315C pin 8 , the $\overline{\mathrm{CH} 1 \text { Trig signal line. }}$

With the $\overline{\mathrm{CH} 1 \text { Trig signal } \mathrm{HI}, \mathrm{O} 273 \text { in the Channel } 2}$ Trigger Pickoff Amplifier is biased on and the Channel 2 signal is passed on to the Internal Trigger Amplifier.

VERT MODE SOURCE. Additional switch settings are involved in determining the internal trigger signal selection when VERT MODE Trigger Source is selected. Both the CH 1-BOTH-CH 2 and the ADD-ALT-CHOP VERTICAL MODE switches establish the vertical signal display and, as such, must also be used to obtain the internal vertical mode trigger signal.

When S305 is set to VERT MODE, the LO logic level on the XY signal line is removed from both U305B pin 4 and from U305D pins 12 and 13, pulling these inputs HI . In either ADD or ALT VERTICAL MODE, U305C pin 9 and U305B pin 5 are also pulled HI whenever a LO is not being applied from S315.

The input conditions just described for U305B, U305D, and U305C allow the logic levels on U315C pin 10 and U315B pin 4 to control the states of the $\overline{\mathrm{CH} 1 \text { Trig and }}$ $\overline{\mathrm{CH} 2 \text { Trig trigger-selection signals. Input signals to pins } 10}$ and 4 are obtained from the Channel Enable signals present at pins 5 and 6 of Channel Switch U317A.

When CH 1 Enable is HI (selecting the Channel 1 signal for display), U315C pin 10 is also HI and U315C pin 8 is LO to disable the Channel 2 Trigger Pickoff Amplifier. Concurrently U317A pin 6 applies a LO to U315B pin 4, and the HI output obtained from U315B pin 6 as a result enables the Channel 1 Trigger Pickoff Amplifier.

For ALT VERTICAL MODE displays, the output states of Channel Switch S317A are switched alternately, at the end of each sweep, in synchronization with the $\overline{\text { Alt Sync }}$ signal. Therefore, on alternate sweeps, the logic levels on U315C pin 10 and on U315B pin 4 also change states.

When the Channel 1 signal is being displayed, the Channel 1 Trigger signal is selected as the internal source. For Channel 2 signal displays, the Channel 2 Trigger signal is selected.

An ADD VERTICAL MODE display causes both pin 5 and pin 6 of U317A to be HI (see "Channel Switching Logic" discussion for a description of the circuit operation). The sum of the two channel vertical signals is displayed, and the sum of the two channel trigger signals is used as the internal trigger signal.

Summation is accomplished by the HI logic levels from
 Trig signals to go LO. With the input transistors to both Trigger Pickoff Amplifiers biased off, additional circuitry within the Trigger Pickoff amplifiers biases on the pickoff transistors for both Channel 1 and Channel 2 (see the Channel 1 and Channel 2 Preamplifier circuit descriptions.

A CHOP VERTICAL MODE display also uses the sum of the two internal trigger signals, but the switching logic involved is different from the ADD VERTICAL MODE display. With S315 set to CHOP, a LO logic level is applied to U305B pin 5 and to U305C pin 9 from the $X Y$ signal line via contacts on S315, S317, and S305. The outputs of both U305C and U305B are LO and are applied to the wired-AND connection on the $\overline{\mathrm{CH} 1 \text { Trig and } \overline{\mathrm{CH}} 2 \text { Trig }}$ signal lines. These LO signals override the outputs from U315C and U315B to hold the input transistors of both Channel 1 and Channel 2 Trigger Pickoff Amplifiers biased off. Channel 1 and Channel 2 Trigger signals are summed as described previously for the ADD VERTICAL MODE display.

X-Y MODE. When the A SEC/DIV switch is set to $X-Y$, the Channel 2 signal is selected as the input to the Vertical Output Amplifier to provide the X -Axis deflection. The Channel 1 Trigger signal provides the $X$-Axis signal to the XY Amplifier (Diagram 7) via the Internal Trigger

Amplifier. Therefore, the Trigger Switching Logic circuit must have inputs that enable the Channel 1 Trigger Pickoff Amplifier.

The LO logic level signal supplied by the $X Y$ signal line to S305 and S317 is removed by switching contacts on the A SEC/DIV switch. Concurrently, a LO logic level is placed on the $\overline{X Y}$ signal line by contacts on the A SEC/DIV switch. The LO on the $\overline{X Y}$ line is applied to the Reset input of U317A to select the Channel 2 signal for display. This LO is also applied to U305B pin 4 and to U305D pin 13 via U305A to set up the Trigger Switching Logic that enables the Channel 1 Trigger Pickoff Amplifier.

A LO on U305B pin 4 ensures that the output of U305B pin 6 is a LO, which is applied to the $\overline{\mathrm{CH}} 1$ Trig signal line to disable the Channel 2 Trigger Pickoff Amplifier. The LO on U305D pin 13 is gated to U315B pin 5. With U315B pin 5 LO , the output of U315B will be a HI that, when ANDed with the HI present from U305C pin 8, enables the Channel 1 Trigger Pickoff Amplifier.

## TRIGGER

The Trigger circuit, shown on Diagram 4, is composed of the Internal and External Trigger Amplifiers, Sourceswitching circuit, and Trigger Generator circuit. Included in the Trigger Generator circuit is the Auto Trigger and Auto Baseline circuitry and the TV Triggering circuitry.

## Internal Trigger Amplifier

The Internal Trigger Amplifier converts the differential current input from the Trigger Pickoff circuit to a zeroreferenced, single-ended output for use by the $A$ and B Trigger Level Comparators. Differential signals from the Pickoff Amplifier circuit are connected via R421 and R422 to common-base transistors U421E and U421D respectively. Transistor U421C and R428 constitute an invertingfeedback amplifier that converts U421D collector current to a voltage at the collector of U421C. This voltage is added in phase with the voltage drop across R 427 produced by the signal current of U421E. The resulting sum is a singleended voltage signal that is applied to the base of emitterfollower U421A. The emitter-follower stage provides a low-output-impedance signal source that drives both the XY Amplifier (through R701) and the emitter-follower (U421B) and supplies the trigger signal to the Alternate B Sweep circuitry. The output signal from U421B is applied to the Trigger Source Switching Diode circuit at the cathode of CR440 where it is available for selection as the triggering signal.

## Trigger Source-Switching Circuit

Trigger signal selection is accomplished by using the A SOURCE switch ( $S 440$ ) to enable one of three triggering signal paths (internal, external, or line) to the Trigger Level Comparator circuit. With S440 set to INT, the inhibiting voltage is removed from R438, causing both U421B and diode CR440 to be biased on. The internal trigger signal is then passed from the emitter of U421B through diode CR440 to the Trigger Level Comparator and Auto Trigger circuits. The A SOURCE switch prevents the line and external triggering signals from reaching the Trigger Level Comparator by reverse biasing diodes CR444 and CR448 and also by reverse biasing Q414 and CR418 through R417.

When S440 is set to LINE, U421B and CR4 18 are biased off through R438 and R417 respectively, while CR444 is enabled by removal of the inhibiting voltage from R446. Similarly, with S440 set to EXT, the external trigger signal is selected by biasing off CR444 and U421B through resistors R444 and R438 respectively and by enabling Q414 through the removal of the inhibiting voltage from R417.

## A External Trigger Amplifier

The A External Trigger Amplifier provides a means of triggering the instrument from an externally supplied signal that can be applied to the EXT INPUT connector. Input coupling to the Amplifier is selectable by the three-position A EXT COUPLING switch, S401. In the AC position, the dc component of the external trigger signal is blocked by coupling the signal through C402. In the DC position, all components of the signal are coupled directly to the gate of Q411A through an input divider composed of R404 and R408. Resistors R402 and R403 form a voltage-divider network that attenuates the signal by a factor of 10 whenever S 401 is set to $\mathrm{DC} \div 10$.

Field-effect transistors Q411A and Q411B are a matched pair. Source-follower Q411A provides a high input impedance for the external trigger signal. Current-source transistor Q411B causes Q411A to operate at zero gate-tosource bias, so the device functions with no dc offset between the input and output signals. The output signal from the source of Q411A drives the base of emitter follower Q414. The emitter-follower stage lowers the output impedance of the Trigger Amplifier and functions as part of the Trigger Source Switching circuitry.

## Auto Trigger Circuit

When either AUTO or TV FIELD triggering is selected, the Auto Trigger circuit detects positive and negative peaks of the input trigger signal and produces output voltages that set the A TRIGGER LEVEL control range to within the peak-to-peak amplitude of the triggering signal. The peak detectors are disabled when S611 is set to NORM, and
fixed voltage levels are applied to both ends of $A$ TRIGGER LEVEL potentiometer R455.

In either AUTO or TV FIELD, the A TRIGGER MODE switch (S611) opens the Auto Disable signal line to allow CR503 and CR504 to become reverse biased. This action isolates the voltage divider network (composed of R525, R527, R528, R526, and Q519) from the + inputs of U507A and U507B. The peak detectors (composed of 0503 for the positive peak and O504 for the negative peak) become enabled when the Auto Disable signal is removed.

The trigger signal is applied to the bases of 0503 and Q504 via R444. Positive trigger signal peaks bias Q503 into conduction, causing its emitter voltage level to rise to the peak level of the trigger amplitude minus the base-toemitter voltage drop.

Capacitor C503 charges up to the positive emitter voltage level. The charge is retained between trigger pulses due to the long RC time constant of R505 and C503. The comparator voltage is applied to U507A pin 3 which is a voltage follower and level shifter that sets the voltage at one end of the A TRIGGER LEVEL potentiometer (R455). Transistor Q507 provides the feedback path for U507A and thermally compensates for $\mathbf{Q} 503$. The base-to-emitter drop of Q507 corrects for the dc offset introduced by Q503, and potentiometer R511 is adjusted to balance out dc offsets introduced from the trigger circuitry.

The negative peak detector operates in the same manner as the positive peak detector, with corresponding components performing the identical circuit function on the trigger-signal negative peaks.

When S611 is set to NORM, +8.6 V is applied through the switch to R525 and R517. Transistor Q519 is biased into saturation by the positive voltage, and both CR503 and CR504 become forward biased. This action reverse biases peak detector transistors Q503 and Q504 to prevent the trigger signal from affecting the A TRIGGER LEVEL control range.

With CR503 and CR504 forward biased, the voltage divider network (R525, R527, R526, and R528) sets the input voltage to U507A pin 3 and U507B pin 5. A fixed positive output voltage from U507A pin 1 is applied to one end of R455, and a fixed negative output voltage from U507B pin 7 is applied to the other end of R455.

## Trigger Level Comparator

The Trigger Level Comparator circuit determines both the trigger level and slope at which a triggering signal is produced. Transistors U460E and U460B form a comparator circuit. It compares the trigger signal level applied to the base of U460E with the reference dc level set by the A TRIGGER LEVEL potentiometer (R455) and applied to the base of U460B. Slope switching is accomplished by controlling the biasing of transistor pairs U460A-U460D and U460C-U460F.

When AUTO or TV FIELD triggering is selected, the Auto Trigger circuit maintains a dc level range at the base of $U 460 B$ that is dependent upon the amplitude of the trigger input signal. In this instance, the Comparator ( $U 460 E$ and U460B) determines the point on the input trigger waveform at which the Schmitt Trigger circuit will produce an output.

When NORM triggering is selected, the A TRIGGER LEVEL potentiometer (R455) is set manually to a dc level that will produce a trigger signal at the output of the Comparator. If the trigger signal amplitude at the base of U460E is below the reference level, the Schmitt Trigger circuit will never switch. If the trigger signal is above the reference level, the Schmitt Trigger circuit output will switch HI and remain HI until either the trigger signal is decreased or the reference dc level is increased.

The A TRIGGER SLOPE switch (S464) controls the bias on U460C and U460F. When set to the positive slope position, the ground is removed from the bottom end of R464, and the forward bias is then determined by the voltage divider formed by R462 and R463. Both U460C and U460F are biased into conduction and carry the signal current from the Comparator transistors. Moving the SLOPE switch to the negative slope position grounds the bottom of R464 and reduces the bias level of U460C and U460F. The fixed bias level on the bases of U460A and U460D is now higher than the bias on U460C and U460F so that U460A and U460D carry the signal current from the Comparator transitors. The collectors of U460A and U460D are cross connected to the collectors of U460F and U460C, so the resulting trigger signal output is inverted.

## Inverting Amplifier and TV Trigger Circuit

Current from one transistor of the conducting pair of transistors chosen by SLOPE switch S464 is applied to U480C pin 10. Current from the other side of the Comparator is applied to pin 14 at the output side of U 480 C through R468. Pin 11 of U480C is at a LO logic level except when TV FIELD triggering is enabled. This LO does not affect circuit operation in either AUTO or NORM triggering.

NOR-gate U480C is an emitter-coupled logic (ECL) device that is operated in the linear region. In the linear region, $U 480 \mathrm{C}$ acts as a high-speed inverting amplifier. Common-mode signals such as noise or thermal drift in the Comparator output signal are cancelled by U480C and associated circuitry. These types of offsets equally affect the outputs from both sides of the Comparator. Changing current to pin 10 of U480C causes a corresponding voltage change at U480C pin 14. The voltage change at one end of R468 is equal in amount and opposite in direction to the voltage change at the other end since the same common-mode signal from the other half of the Comparator is applied to the other end of R468.

When the A TRIGGER MODE switch is set to TV FIELD, +8.6 V is applied to the TV Trig Enable signal line. Transistors Q474 and Q476 are biased on via R474, and U480C pin 11 is set HI , causing the output of U480C at pin 14 to be LO .

Current flowing through R466 from either U460C or U460D causes a voltage drop that establishes the bias voltage on the base of Q474. Current flowing through R473 and R472 produces a voltage drop across R473 that establishes the bias voltage on the base of O 476 . The circuit components are selected such that when the Comparator output voltages from both halves are equal, the base voltages to both O 474 and O 476 will be the same. With equal base voltages, each transistor will conduct an equal amount of current.

When the Comparator output becomes unbalanced, due to an input trigger signal, unequal biasing of Q474 and 0476 occurs. In response to a changing bias condition, the collector currents vary proportionally.

The collector current changes from 0474 are filtered by a network composed of C476, C477, R477, and R478. The filter network rejects TV video information and averages the TV horizontal-sync pulses. Setting the triggerlevel threshold at near the center of the horizontal-syncpulse swing establishes the untriggered level. When the TV vertical-sync block occurs, the output of the filter rises to a level that will cause the Schmitt Trigger circuit to switch. Precise TV field synchronization is obtained as a result of the filtering action.

The output signal from the filter is applied to U480B pin 6. The Schmitt Trigger circuit responds only to the TV sync signal because pin 7 is held LO by the output of U480C.

## Schmitt Trigger Circuit

With a LO on U480B pin 7, the output at pin 3 goes LO as soon as the signal on U480B pin 6 reaches the switching threshold. The LO is applied to U480A pin 4 and, together with the fixed LO on pin 5, causes the output of U480B pin 6 via R480 to reinforce the switching action. As a result, the output signal at U480A pin 2 switches rapidly.

When the level from the filter network falls to the LO threshold level, the feedback supplied by R480 holds the Schmitt Trigger switched HI for a short time. The amount of time involved prevents noise occurring exactly at the threshold level from causing false triggering.

When either AUTO or NORM triggering is selected, input pin 6 of U480B is held LO, and the Comparator output signal on U480B pin 7 supplies the input to the Schmitt Trigger circuit.

The output of the Schmitt Trigger circuit is obtained from U480D pins 9 and 15. The differential output signal derived from U480D is applied to a two-transistor level shifting circuit composed of Q492 and Q493. The levelshifting circuit converts the ECL logic levels to TTL logic levels required for the Sweep Generator. A signal obtained from the collector of 0493 is used to drive the Auto Baseline circuit.

## Auto Baseline Circuit

The Auto Baseline circuit (composed of U640A, Q605, and associated components) is enabled in both AUTO and TV FIELD triggering modes. This circuit provides a signal to the Sweep Generator circuit (Diagram 5) that initiates a sweep if a triggering signal is not received by the Schmitt Trigger circuit within a period of about 100 ms . A second output from the circuit illuminates the TRIG‘D LED on the instrument front panel when the sweep is triggered.

When adequate triggering signals are being received, the output of Q493 is applied to pin 5 of monostable multivibrator U640A. The negative-going edge of the signal causes pin 6 of U640A to switch HI . The HI forward biases CR615, and Q605 is then biased into conduction. With O605 conducting, the Auto Baseline signal line is held LO to prevent the Sweep Generator circuit from free running.

The amount of time that pin 6 of U640A stays HI without receiving an input signal is determined by timing components R614 and C614. If a trigger signal is not received in about 100 ms , pin 6 of U640A will go LO and 0605 will be biased off. The Auto Baseline signal line then
goes HI through pull-up resistor R610, and the Sweep Generator free runs to produce the baseline trace.

In NORM triggering mode, the Auto Disable signal ( +8.6 V ) is applied to the base circuit of 0605 via CR611 and R611. The signal holds Q605 forward biased and prevents the Sweep Generator from free running.

The other function of the Auto Baseline circuit is to illuminate the TRIG'D LED when the sweep is properly triggered. As long as U640A pin 6 remains HI (triggering signals occurring with the proper time), TRIG'D LED DS618 will be illuminated. The trigger mode in use does not affect the operation of the TRIG'D LED.

## A SWEEP GENERATOR AND LOGIC

The Sweep Generator and Logic circuitry, shown on Diagram 5, produces a sawtooth voltage that is amplified by the Horizontal Amplifier to provide horizontal deflection on the crt. This sawtooth voltage (sweep) is produced on command from the Sweep Logic circuits. The Sweep Generator circuits also produce gate waveforms that are used by the Auto Intensity and Z-Axis circuits to establish the correct timing of the crt unblanking and intensity levels used for viewing the display. See Figure 3-5 for the A Sweep timing diagram.

The Sweep Logic circuitry controls the holdoff time, starts the sweep upon reception of a trigger signal, and terminates the sweep at the proper sweep level. When using AUTO or TV FIELD triggering, the Sweep Logic circuitry will cause the Sweep Generator to free run, producing a baseline trace if a trigger signal is not received within the predetermined time period.

## Miller Sweep Generator

The Miller Sweep circuit is composed of O630A, Q630B, Q631, and associated timing components. The circuit operates to hold the charging current to the timing capacitor at a constant value. When a capacitor is charged in this manner, the rise of voltage across the capacitor is linear rather than exponential.

Field-effect transistors Q630A and Q630B are matched devices. As such, the $I_{\text {DSS }}$ (drain current with gate-tosource shorted) characteristics of each are nearly identical. FET Q630B acts as a source-current supply for Q630A and holds the gate-to-source voltage of Q 630 B at zero volts.

Before a sweep starts, pin 6 of U620 (the A Sweep Logic Gate) is HI, and both disconnect diodes (CR626 and CR630) are forward biased. The charge on the selected timing capacitor will be zero volts. When U620 pin 6 goes LO, the disconnect diodes become reverse biased and the timing capacitor begins charging through the timing resistor to start the sweep.


Figure 3-5. A Sweep timing diagram.

The overall gain of the amplifier composed of sourcefollower Q630A and common-emitter amplifier Q631 is very high. As the timing capacitor charges, Q631 supplies feedback to the gate of Q630A to hold the gate voltage nearly constant. Voltage across the timing resistor is therefore constant, and the charging current to the timing capacitor is constant. The resulting voltage waveform produced at the collector of Q631 is a linear ramp.

When the sweep waveform amplitude reaches about +13 V, the A End-of-Sweep Comparator (O640) is biased on and the Sweep Logic circuit resets. Pin 6 of U620 goes HI to forward bias disconnect diode CR626, and the current through the timing capacitor reverses direction. The sweep output waveform drops rapidly until disconnect diode CR630 also becomes forward biased. At this point, the Sweep Generator is ready to start another sweep.

## Sweep Logic

Following the sweep completion, a finite time is required to discharge the timing capacitor. The Sweep Logic circuit is prevented from responding to a trigger signal during this time by the Holdoff circuit. The end of sweep (and start of the holdoff period) is determined by the A End-of-Sweep Comparator ( O 640 ).

The A Sweep ramp waveform is applied to the base of Q640 through both a voltage divider and a biasing network composed of R637, R638, and C637. When the ramp amplitude reaches the threshold level of Q640, the collector of Q640 goes LO, and a LO is placed on both U640B pin 11 and U607C pin 10. The output of U607C goes HI , and the positive feedback supplied to the base of 0640 through R639 speeds up the change of state of Q640. By reinforcing the switching action of Q640 in this manner, noise occurring at the threshold level of Q640 is overridden.

The sweep holdoff period commences when the LO from Q640 is applied to pin 11 of monostable multivibrator U640B. The $\bar{O}$ output on pin 9 goes LO and remains LO for a length of time determined by the RC timing components connected between pins 14 and 15 of U640B.

Holdoff time can be varied from the normal period by using VAR HOLDOFF control R647. Potentiometer R647 and a voltage divider composed of R645 and R646 establish the charging voltage of holdoff timing capacitors C645, C646, and C647. The capacitor (or combination of capacitors) used is switched into the holdoff circuit by contacts on S630B, the A SEC/DIV timing switch.

During holdoff time, while U6408 pin 9 remains LO, the output of U607C will be HI. Inverter U607B will invert the HI to a LO logic level that is then applied to the Reset inputs of both U603A and U603B at pins 1 and 13 respectively. The LO at these inputs holds both flip-flops in the reset state, with the $\overline{\mathrm{O}}$ outputs HI and Q outputs LO. In the reset state, flip-flops U603A and U603B will not respond to input trigger signals. The Set input of U603B is held HI by the output of U607A and does not affect flipflop operation. (With AUTO trigger mode selected, a different condition at the Set input of U603B occurs when triggeringsignals are not received, see Auto Baseline Sweep.)

As long as the Reset input of U603B is held LO, the $Q$ output at U603B pin 9 stays LO. The LO is applied to one of the inputs of all four AND-gates contained in Sweep Logic Gate U620, and output pins 6 and 8 of U620 will be held HI . As previously described, a HI on U620 pin 6 resets the Miller Sweep Generator.

When the timing capacitor is charged up to the reset threshold of U640B, the holdoff time elapses, and U640B switches back to the stable state to place a HI on the $\overline{\mathrm{Q}}$ output (pin 9). The A End-of-Sweep Comparator output on U607C previously became HI when the Miller Sweep Generator finished resetting. With both inputs of U607C now HI, the output on pin 8 is LO. This LO is inverted to a HI by U607B and applied to both U603A and U603B to remove the reset condition. The Q output of U603B at pin 9 will remain LO when the reset is removed, while the Q output on U603A (pin 5) will depend on the state of the Set input when the reset is removed.

If the Set input to U603A is HI when the reset is removed, the Q output will be LO. However, if the Set input is LO, the Q output on U603A will be HI prior to the reset removal, and it will remain HI after the reset is removed. If the Set input of U603A was HI when the reset was removed, the triggering signal will make a negative transistion to set U603A before U603B is clocked, since U603B clocks only on positive transitions.

In either case (with the Set input either HI or LO when the holdoff period ends), the Q output of U603A will be HI as U603B is clocked by the first positive transition of the trigger signal after holdoff ends. The HI output present on the D input of U603B (pin 12) is then transferred to the Q output (pin 9), where it is applied to one input of each AND-gate contained in Sweep Logic Gate U620.

The HI is ANDed with the fixed HI supplied by pull-up resistor R608 on U620 pin 4 and inverted by a NOR-gate to produce a LO output on U620 pin 6. As previously described, this LO output reverse biases disconnect diodes CR630 and CR626 to allow the A Sweep to begin.

Gating in the lower half of U620 is concerned with unblanking the display for the A Sweep, as discussed in the following $A Z$-Axis Switching description.

A Z-AXIS SWITCHING. The Z-Drive signal is a combination of input currents that are applied to the $Z$-axis amplifier (Diagram 6) to establish the display intensity. Switching of the Z-axis drive for the A Sweep is controlled by the A Gate and A Disp input signals to the A Sweep Logic Gate (U620). The A Gate signal is HI during each A Sweep period, but A Disp is HI only during the time the A trace is to be displayed. During the B Sweeps that occur in both ALT and B HORIZONTAL MODE, the A Disp signal is held LO.

When the A Sweep is to be displayed, the signals at U620 pins 9 and 10 are both HI and U620 pin 8 is LO. The LO reverse biases CR620, and the Intens Level current from the Auto Intensity circuit (Diagram 6) passes through CR622 as the A Z-Drive signal. During B Sweep displays, the A Disp signal on U620 pin 10 is held LO and the signal on U620 pin 8 is HI. Diode CR620 becomes forward biased, reverse biasing CR622, and the Intens Level current is prevented from flowing through CR622 to the Z-Drive signal line. With the A Z-Drive signal shut off, the A Sweep display is blanked, and Z-Drive current is supplied by the B Z-Axis Logic circuit (Diagram 10).

AUTO BASELINE SWEEP. This feature causes an automatic sweep to be generated after about 100 ms if no trigger signals are received. Generation of the Auto Baseline signal was discussed previously in this section. The Auto Baseline signal is LO either when trigger signals are being received or when the circuit is disabled by using NORM triggering.

The Auto Baseline signal is applied to pin 1 of NANDgate U607A, while the Holdoff Gate signal is applied to U607A pin 2. As long as the Auto Baseline signal remains LO, the output of U607A on pin 3 will be HI and will not affect the Set input of U603B. When the Auto Baseline signal goes HI in the absence of triggers (using either AUTO or TV FIELD triggering), the output of U607A is an inverted Holdoff Gate signal.

During holdoff, the output of the Holdoff Gate is a LO and places a reset on both U603A and U603B. The reset causes the Q output of U603B to be LO. At the end of the
holdoff period, pin 2 of U607A goes HI , and the reset is removed from U603A and U603B. With both pins 1 and 2 of U607A HI, the output on pin 3 goes LO, and U603B becomes set. Pin 9 of U603B becomes HI and U620 pin 6 goes LO to initiate the A Sweep. As long as no trigger signal is received, U603B will continue to free run in the manner just described to produce a sweep at the end of each holdoff period.

X-Y DISPLAY. Switching the A SEC/DIV switch to the X-Y position applies a LO logic level to U640B pin 11 and U607C pin 10 via CR640 and to U607A pin 1 via CR610. The LO applied to U640B pin 11 prevents the Holdoff monostable multivibrator from being triggered. The LO applied to U607C pin 10 and to U607A pin 1 ensures that both U603A and U603B are held in the reset condition and do not respond to input trigger signals.


#### Abstract

ALT SYNC PULSE. A shaping network connected to U640B pin 9 converts the leading edge of the negative-going holdoff transitions into a narrow pulse suitable for use as a synchronization signal. Zener diode VR644 holds the voltage at one end of C 644 at about 3 V , while the $\overline{\mathrm{Q}}$ output of U640B at pin 9 is HI . When the $\overline{\mathrm{Q}}$ output of U640B goes LO at the start of the holdoff period, C644 couples the negative-going edge of the pulse to the $\overline{\text { Alt Sync }}$ signal line.

Capacitor C644 charges rapidly to the new voltage difference through R642 to produce a very narrow pulse output across R642. When the holdoff period ends, the $\overline{\mathrm{Q}}$ output of U640B goes HI again and C 644 charges in the opposite direction through VR644. The positive-going edge of the differentiated holdoff pulse is very small in amplitude and does not affect the circuitry to which the $\overline{\text { Alt Sync }}$ signal is applied.


The $\overline{\text { Alt Sync }}$ signal is fed to two places: the Alternate Sweep circuit and the Channel Switching circuit. It is used to synchronize the horizontal display with channel switching transitions when using ALT VERTICAL MODE and to alternately switch between the A and B Sweeps when using ALT HORIZONTAL MODE.

## ALTERNATE B SWEEP

The Alternate B Sweep circuitry, shown on Diagram 10, produces the $B$ sawtooth voltage that is amplified by the Horizontal Amplifier to provide the B Sweep horizontal deflection on the crt. The Alternate B Sweep circuitry also produces the sweep-switching signals, that control the display of the $A$ and $B$ Sweeps, and the gate waveforms used by the Auto Intensity and Z-Axis circuits to establish
the crt unblanking and intensity levels needed for viewing both the A Intensified and B Sweep displays.

The B Sweep sawtooth voltage is produced on command from the B Sweep Logic circuit either immediately after the end of the established delay time (Run After Delay) or upon receipt of the first trigger signal after the delay time has elapsed. The delay time is established by the B Delay Time Position Comparator circuit.

## Run After Delay

The Run After Delay circuit allows the B Sweep Logic to generate a B Sweep independently of any B Trigger signals. In the RUN AFTER DLY mode, the B TRIGGER LEVEL. control (R557) is rotated fully clockwise. This biases off 0573 and places a LO logic level on its collector. Inverter U690A will then have a HI output. Resistor R574 provides positive feedback to hold the output HI. The output of U690A is applied to U665C pin 10 and is also inverted through U690B to hold U696A reset.

If the B TRIGGER LEVEL control is not fully clockwise, 0573 is biased on, and the output of U690A is LO. Pin 10 of U665C will then be LO and, with the inverting by U690B, U696A will not be held reset. Operation of the B Sweep Logic circuitry under both of these input conditions is described in the "B Sweep Logic" discussion.

## B Delay Time Position Comparator

The B Delay Time Position Comparator circuit compares the amplitude of the A Sweep sawtooth voltage waveform with the dc voltage level set by the B DELAY TIME POSITION potentiometer (R658). The output of the comparator is used to initiate a B Sweep and to control the B Z-Axis Logic circuit switching.

Transistors U648A and U648B form the Comparator, and U648C acts as a current source for the Comparator. Wiper voltage from the B DELAY TIME POSITION potentiometer is applied to one input of the Comparator at the base of U648A (pin 1). The A Sweep sawtooth voltage is applied to the other Comparator input through a voltage divider composed of R653, R654, and R655. The divider establishes the portion of the sawtooth voltage amplitude that is applied to the base of U648B at pin 5. Delay Dial Gain potentiometer R654 is adjusted in conjunction with Delay Dial Start potentiometer R659 to set the B DELAY TIME POSITION dial accuracy.

Normally U648A in the Comparator is biased on by the dc level set by potentiometer R658, and U648B is biased off. When the sawtooth voltage amplitude at the base of U648B reaches the dc voltage level set by R658 on the base
of U648A, the biasing conditions are reversed, and U648B becomes forward biased while U648A is biased off.

The Comparator output signal from the collector of U648A is applied to the base of U648D at pin 11. Transistors U648D and U648E form a differential amplifier circuit that will either pass the Delay Time signal or block it, depending on the state of the $\overline{\mathrm{A} \text { Only }}$ signal. If $\overline{\mathrm{A} \text { Only }}$ is $\mathrm{HI}, \mathrm{O} 622$ is biased into saturation and CR662 becomes reverse biased. With CR662 reverse biased, the base bias level of U648E enables the Comparator output signal to turn O664 off and on. In this biasing state, the changes in collector voltage of 0664 are coupled through C664 to U665D pin 13.

When $\bar{A}$ Only is LO, the B trace will not be displayed. Transistor O662 is biased off, and the bias level on U648E is established at a level that prevents the Comparator output from turning on U648D. Therefore, U648E remains on with O664 saturated, and no Delay Time Comparator output signal is obtained.

## B Sweep Logic

The B Sweep Logic circuitry utilizes inputs from the associated B Sweep circuitry to generate a signal controlling both the B Miller Sweep and the B Z-Axis Switching Logic circuits.

In the RUN AFTER DELAY mode (R557 fully clockwise), U696A is held reset by U690B to place a HI on U665B pin 5, and U665C pin 10 is HI . The output of U665B, when LO, will enable the B Miller Sweep, and when HI, will disable the B Miller Sweep. The flip-flop composed of U665A and U665D will determine the output level of U665B through U665C. Input signals to the flip-flop come from the Delay Time Position circuitry (at U665D, pin 13), and from the ANDed output of the Alt Sync signal and the B End-of-Sweep Comparator circuitry (at U665A, pin 1). As long as the input to U665D pin 13 is HI , a B Sweep will not be generated. When U665D pin 13 goes LO, the output at pin 11 will go HI . If $\overline{\mathrm{Alt} \mathrm{Sync}}$ (applied to U693A pin 2) is also HI, U665A pin 3 will go LO and initiate a B Sweep through U665C and U665B. The sweep will run until either Alt Sync goes LO or the sweep output biases on the B End-of-Sweep Comparator transistor (O690). In either case, the output of U693A will go LO, resetting the flip-flop and disabling the B Miller Sweep by setting the $\overline{B \text { Gate }}$ signal at U 665 B pin 6 HI .

When not in the RUN AFTER DELAY mode, U696A is not held reset ( pin 1 is HI ), and U665C pin 10 is LO. The output of U665D is LO, holding U696A in the set state to place a LO on U665B pin 5 . The B Sweep is initiated on the
first positive pulse from the B Trigger Generator circuitry that occurs after the $\overline{\text { Delay Gate }}$ signal goes LO. Delay Gate going LO will release U696A from the set condition by causing U665D pin 11 to go HI . This HI on pin 11 will also cause U665A pin 3 to go LO, and a LO will be placed on the $D$ input of U696A (pin 2). A positive transition from the B Trigger circuitry will then clock U696A, causing a HI on pin 6 which will make U665B pin 6 LO. The B Miller Sweep will then run until either $\overline{\text { Alt Sync }}$ goes $L O$ or the sweep output biases on the B End-of-Sweep Comparator transistor (0690) to end the sweep.

## Alternate Display Switching Logic

The Alternate Display Switching Logic circuitry controls both the Horizontal Amplifier sweep switching and the B Z-Axis Logic switching.

HORIZONTAL MODE switch S650 selects the input logic levels that are applied to the circuitry. In A HORIZONTAL MODE, U696B pin 10 is LO and pin 13 is HI. This holds U696B set ( O output HI and $\overline{\mathrm{Q}}$ output LO), allowing only the $A$ Sweep to be passed to the Horizontal Amplifier. In B HORIZONTAL MODE, U696B pin 10 is HI and pin 13 is LO, holding U696B reset and allowing only the B Sweep to go to the Horizontal Amplifier.

With S650 set to ALT and the CH 1-BOTH-CH 2 VERTICAL MODE switch set to CH 1 , all of the following pins are HI : U670D pin 13, U690D pin 9, U690E pin 11, and U670A pin 2. The resulting LOs applied to the inputs of U693D from the outputs of U690D and U690E cause the output of U693D (pin 11) to be L.O. This LO is inverted by U690F, causing pin 10 of U693C to be HI. Since U696B is not held either set or reset (pins 10 and 13 are both HI), the output state will reverse (toggle) whenever a clock pulse is received on pin 11. Negative-going transistions of the $\overline{\mathrm{A} \mid t}$ Sync signal will cause the output of U670D to go HI , which transfers through U693C, clocking U696B. With each $\overline{\mathrm{Alr}}$ Sync pulse, the outputs of U696B will toggle to alternately enable the A and B Sweeps to reach the Horizontal Amplifier. For the CH 2 position of the VERTICAL MODE switch, circuit operation is the same except that U690E pin 11 is LO. Whenever the B Sweep is selected for the Horizontal Amplifier, U696B pin 8 will be HI . This HI is applied to U670A pin 1, and since pin 2 is also HI , output pin 3 (Sep) will go LO to enable the $A / B$ Sweep Separation circuitry (Diagram 3).

When the VERTICAL MODE CH 1-BOTH-CH 2 switch is set to BOTH, the ADD-ALT-CHOP switch becomes functional. In the VERTICAL MODE ALT position, the following conditions are present: the $\overline{\text { Valt }}$ signal is LO, the Halt signal is HI , and the CH 1 Sel signal is a TTL square wave that switches states at the end of the A Sweep.

The output of U670D will be HI to enable the output of U693C to change with level changes of the CH 1 Sel signal that is gated through U690E, U693D, and U690F. Since only positive transitions on the clock input of U696B will cause U696B to change states, two A Sweeps are required to cause U696B output levels to switch. With this switching arrangement, the crt will first display the two A Intensified Sweeps and then the two alternate B Sweeps.

In the VERTICAL MODE CHOP position, the CH 1 Sel signal is HI and the $\overline{\mathrm{Valt}}$ signal is LO. Input pin 10 of U693C will always be HI , and pin 9 will receive the $\overline{\mathrm{Alt}}$ Sync signal gated through U670D. The outputs of U696B will therefore toggle whenever its clock input receives a positive transition. The Horizontal Amplifier will alternately receive first $A$ and then $B$ information.

For the VERTICAL MODE ADD position, the CH 1 Sel signal is LO. The outputs of U 696 B will change states with the $\overline{\text { Alt Sync }}$ signal which is gated through U670D and U693C.

## B Z-Axis Logic

The B Z-Axis Switching Logic circuitry switches the B Z-Drive signal to supply current to the Z-Axis Amplifier for both the B and the A Intensified Sweep displays. The current supplied is summed with the other signal inputs on the $Z$-Drive line to produce the complete display intensity level. Figure $3-6$ is a simplified diagram of the Z-Axis Switching Logic that includes the A Z-Axis Switching Logic circuit.

When HORIZONTAL MODE switch S650 is in the ALT position, pin 5 of U693B is HI . If the outputs of U696B are set for an $A$ display ( $\mathrm{Q} \mathbf{H I}$ and $\overline{\mathrm{O}} \mathrm{LO}$ ), then the outputs of U693B and U670B will both be HI. The $\overline{\mathrm{B} \text { Duty }}$ signal will therefore be HI , and the B Z-Drive current through R671 (Intens Level from the Auto Intensity circuit, Diagram 6) will be switched off of the Z-Drive line by reverse-biased diode CR671. Z-Drive current will be supplied by the A Z-Axis Logic circuit during this time. When the output of the B Sweep Logic circuit is currently enabling a B Sweep, then the output of U670C will be LO and CR672 will be forward biased. This will enable current from R672 to reach the Z-Drive line where it adds to the $A$ Z-Drive current to produce an intensified A Sweep display. Should a B Sweep not be running, then the output of U670C will be HI , and current from R 672 will be prevented from reaching the Z-Drive line by reverse biasing CR672.

If the outputs of U696B are set for a B display ( O LO and $\overline{\mathrm{Q}} \mathrm{HI}$ ), then the output of U693B will be LO, causing the output of U670C to be HI. This will forward bias CR669 and reverse bias CR672 to prevent• the B Z-Drive


Figure 3-6. Simplified diagram of the Z-Axis Switching Logic circuit.
current (for intensifying the A Sweep) from reaching the Z-Drive line. While a B Sweep is not running, pin 5 of U670B will be LO. Output pin 6 will then be HI , forward biasing CR670 and reverse biasing CR671 to switch off B Z-Drive current from R671 to the Z-Drive line. However, when a B Sweep is enabled, the output of U670B will be LO, thereby forward biasing CR671 to pass current from R671 to the Z-Drive line for a B Sweep display.

## AUTO INTENSITY AND Z-AXIS AMPLIFIER

## Auto Intensity

The purpose of the Auto Intensity circuit, shown in Diagram 6, is to keep the intensity of the trace on the crt at a constant level with changing sweep speeds and trigger
signal repetition rates. In conventional oscilloscopes, as the duty cycle of the displayed trace changes, the intensity will vary. The Auto Intensity circuit compensates for this effect by increasing the $Z$-Axis Drive voltage for low $A$ Sweep duty factors. The elements of the Auto Intensity circuit consist of four blocks: the duty-cycle averager, the boost-factor converter, the intensity-control multiplier, and the crt triode compensation circuit. The duty-cycle averager consists of an electronic switching circuit composed of U825A, U825B, and U825C. The A Duty signal that is applied to U825B pin 11 causes the output voltage at pin 14 to be switched between ground and +5 V . The output voltage is averaged by R821 and C821. The B Sweep duty-cycle averager operates in an identical manner as the A Sweep duty-cycle averager. The $\bar{B}$ Duty signal is connected to U825 pin 10 and is averaged by R825 and C825. Bilateral switch U825C, under control of the A Disp signal from the Alternate Display Switching Logic circuit
(Diagram 10), selects which of the two averaged voltages will be connected to the input of U835A.

As the sweep duty factor decreases, the crt beam current must be increased to maintain a constant intensity. To accomplish the task, the boost-factor converter increases the drive in inverse proportion to the duty factor of the trace being displayed.

Amplifier U835A is a high-impedance voltage follower. For $100 \%$ duty factor, the output voltage will be approximately zero. Decreasing the duty factor to $10 \%$ results in approximately 4.5 V output, and when no sweep occurs ( $0 \%$ duty factor) the output will be 5 V . The output of U835A is applied to a network consisting of CR828, CR830, and resistors R827, R828, R829, R830, and R831. This network produces an output current which is a nonlinear function of the duty-factor voltage. For $10 \%$ duty factor, the output current is 10 times greater than the current at $100 \%$ duty factor. Maximum available boost limits at a factor of about 25:1.

The nonlinear current is connected to the emitters of the differential amplifier composed of 0811 and 0812. The emitters of the two amplifier transistors are held at a constant voltage by the action of Q813. AUTO INTENSITY control R807 is connected to the base of Q811 via R811. It controls the portion of the boost current that goes to the summing junction of U835B. Boost current is proportional to the true beam current required at the faceplate of the crt.

The crt triode compensation circuit is an inverting operational amplifier with nonlinear feedback. It is composed of U835B, R834, R835, C834, and CR834. Output voltage of the circuit changes in response to the input current in a manner that complements the nonlinear triode characteristics of the crt. This output voltage is applied both to the Intens Level signal line and to the $Z$-Axis Amplifier via the A and B Z-Axis Logic Switching circuits. The Intens Level signal is also applied to the Focus circuit (Diagram 9) for use in focus tracking of the intensity level changes.

The intensity of the display is allowed to reduce to zero through the action of CR809, VR809, and R809. Without this circuit, the Auto Intensity circuit would not allow the intensity to go to zero when the AUTO INTENSITY control is set to minimum intensity.

## Z-Axis Amplifier

The Z-Axis Amplifier controls the crt intensity level via several input-signal sources. The effect of these input
signals is either to increase or decrease trace intensity or to completely blank portions of the display. The A and B $Z$-Drive signal current and the input current from the Z-AXIS INPUT connector (if in use) are summed at the emitter of common-base amplifier transistor 0841. The algebraic sum of these signals determines the collector current of Q841. Input transistor Q841 provides a lowimpedance termination for the input signals and isolates the signal sources from following stages of the Z-Axis Amplifier.

Signal current from Q841 flows through CR844 and develops a signal voltage drop across R844. Increasing current through 0841 reduces the forward bias of Q844, thereby reducing the current through $\mathbf{Q 8 4 4}$. This action causes the collector voltage of 0844 to go more negative (toward the -8.6 V supply) and increases the forward bias on emitter-follower Q845. As emitter current of Q845 increases, negative-going voltage developed across R847 is applied to the bases of complementary-pair output transistors Q847 and Q850. Positive transistions of the Z-Axis signal are coupled to the base of Q850 via C852. The fastrise transitions are amplified by $Q 850$ to speed up the response time. For negative transitions of the Z -Axis signal, as well as for dc and low-frequency signal components, Q847 acts as the amplifier, with $\mathbf{Q 8 5 0}$ supplying the current.

Diode CR856 prevents the Z-Axis output signal from going negative, and neon lamps DS854 and DS856 provide protection to the Z-Axis Amplifier in the event of highvoltage arcing in the crt.

The amplifier gain with respect to the A or B Z-Drive current is set to about 10 by the negative feedback supplied from the collectors of Q847 and Q850 to the base of Q845 via feedback resistor R 846 . The gain with respect to the external $Z$-Axis Input signal is held to about three by R801, R802, and R803 in series with the external input signal. Diodes CR801 and CR802 provide protection for the Z-Axis Amplifier in case of an accidental application of excessive signal amplitude to the Z-AXIS INPUT connector.

When CHOP VERTICAL MODE is selected, the Chop Blank signal is applied to the collector of 0841 during the display switching time. Signal current is shunted away from CR844, and the forward bias of 0844 increases to the blanking level. When blanked, the output of the Z-Axis Amplifier drops to about +10 V , and the crt beam current is reduced to below viewing intensity to eliminate chop switching transients from the display.

For an X-Y displav, the A Z-Drive and B Z-Drive signal currents are switched off. When the $X Y$ signal is LO, CR837 is forward biased and Intens Level current flows
through R837 to Z-Axis Amplifier transistor Q841 to establish the display intensity.

The last input to the Z-Axis Amplifier is the Beam Find current. Normally, BEAM FIND switch S390 is closed, and -8.6 V is supplied to the base bias network of 0841 and Q844. When the BEAM FIND switch is opened, the -8.6 V is removed, and the bias voltage becomes more positive. Transistor Q841 becomes more forward biased while Q844 becomes much less forward biased. The current through Q844 is reduced, and the base bias voltage of 0845 is thereby increased. The output of 0845 then goes to a level that produces a fixed, predetermined $Z$-Axis output signal level. Thus neither the AUTO INTENSITY control nor the Z-Drive signal have any control over the intensity level of the crt display whenever the BEAM FIND push button is pressed in, and a bright trace (or dot if no sweep is present) will be displayed.

## HORIZONTAL

The Horizontal Amplifier circuit, shown on Diagram 7, provides the output signals that drive the horizontal crt deflection plates. Signals applied to the Horizontal Preamplifier can come from either the A or the B Miller Sweep Generator (for sweep deflection) or from the XY Amplifier (when X-Y display mode is selected). Sweep switching is under control of the Alternate Display Switching Logic circuit (Diagram 10). See Figure 3-7 for a detailed block diagram of the Horizontal Amplifier circuit

The Horizontal POSITION control, $\times 10$ magnifier circuitry, and the horizontal portion of the beam finder circuitry are also contained in the Horizontal Amplifier circuit.


Figure 3-7. Detailed block diagram of the Horizontal Amplifier.

## Sweep Switching

The Sweep Switching circuit is composed of two transistors, Q634 and Q684, acting as switches under control of the Alternate Sweep Switching Logic circuit. Either the A Disp or the $B$ Disp signal is applied to the base of the associated transistor (A Disp to Q684 and B Disp to Q634), and the sweep signals are applied to the collectors of the switching transistors. The A Disp and B Disp signals are complementary (when one is HI the other is LO) so only one sweep signal at a time will be applied to the Horizontal Preamplifier.

A SWEEP DISPLAY. To pass the A Sweep to the Horizontal Preamplifier, the A Disp signal is HI. Transistor switch Q684 is biased on, and the B Sweep signal is shunted to ground through the transistor. Since $Q 634$ is biased off, the A Sweep signal is allowed to pass to the preamplifier summing junction at the base of $\mathbf{Q 7 3 0}$. Sweep signal current is summed with the horizontal positioning current supplied by Horizontal POSITION control R726.

B SWEEP DISPLAY. The A Disp signal becomes LO and the B Disp signal applied to the base of 0634 becomes HI . Switching transistor Q634 is biased on, and the A Sweep current is shunted to ground. The B Sweep current passes to the input summing junction to be added to the horizontal positioning current. The B Gain potentiometer (R682) is adjusted to provide the same gain for the $B$ Sweep signal as for the A Sweep signal.

ALT HORIZONTAL DISPLAY. The A Disp and B Disp signals are switched at the alternate sweep rate by the Alternate Sweep Switching Logic circuit. When both vertical channels are being viewed simultaneously, the intensified traces of both Channel 1 and Channel 2 are first displayed, then both alternate $B$ traces are displayed.

## Horizontal Preamplifier

The sum of the sweep and positioning current is applied to the input of one side of a differential amplifier composed of Q730 and Q731. For all conditions other than the X-Y Mode, XY Switch transistor Q720 is biased on to provide a ground reference at the other input of the differential amplifier (at the base of Q731). The output of the differential amplifier, taken from the collector of Q731, is amplified by Q736.

A feedback network connected between the output of Q736 and the base of Q730 provides the circuitry required for the $\times 10$ magnification feature. In the unmagnified mode, X 10 Magnifier switch S 734 is closed and the feedback is provided by the paralleled combination of R732 and C732. Resistor R732 sets the unmagnified amplifier gain and C732 provides the HF compensation.

When the $\times 10$ Magnifier push button is pressed in, S734 opens and additional components are added to the feedback network. With the feedback reduced, the amplifier gain is increased by a factor of 10 . The $\times 10$ Gain potentiometer (R733) is adjusted to produce the exact gain required. High-speed linearity compensation of the feedback network is provided by adjustable capacitor C734.

## XY Amplifier

When the $X-Y$ display mode is selected using the A SEC/ DIV switch, the $\overline{X Y}$ signal line goes LO and $X Y$ Switch transistor $Q 720$ is biased off. The $\overline{X Y}$ signal is also applied to FET 0714 (used as a switch to prevent crosstalk) in the $X Y$ Amplifier to bias it on. With this action, the XY Amplifier is enabled to pass $X$-Axis signals on to the Horizontal Preamplifier. Another function of the $\overline{X Y}$ signal is to disable the A Sweep Generator to prevent the $A$ and B Sweep signals from being applied to the Horizontal Preamplifier.

The X -Axis signal is derived from the Channel 1 internal trigger signal and applied to the base of Q703. Transistor Q703 is one-half of a differential amplifier composed of 0703 and 0706 . The base of 0706 is referenced to ground through R706. Transistor Q708 amplifies the output signal from the collector of 0706 and applies it to the drain of FET Q714. A feedback network composed of R709, R708, and C708 is connected between the collector of 0708 and the base of Q703. The feedback network sets the overall gain of the XY Amplifier, with X-Gain potentiometer R709 adjustable to obtain the exact gain required.

The X-Axis signal passes through FET Q714 and is applied to the base of Q731 in the Horizontal Preamplifier. Horizontal positioning current on the base of Q730 is added to the X -Axis signal by the action of the differential amplifier. Then the sum of these two currents is amplified by Q736 and applied to the input of the Horizontal Output Amplifier.

## Horizontal Output Amplifier

The Horizontal Output Amplifier converts the singleended output of the Preamplifier into the differential output required to drive the crt horizontal deflection plates. The output stage consists of an input paraphase amplifier and an output complementary amplifier.

Horizontal signal voltage from Q736 is applied to the base of Q763. The base of the other transistor ( Q 753 ) in the paraphase amplifier, is biased through a voltage divider composed of R758, R757, and R756. Horizontal centering between the X 1 and $\times 10$ Magnified sweeps is accomplished by adjusting Mag Registration potentiometer R758.

Gain of the paraphase amplifier is determined by components connected between the emitter leads of Q763 and 0753. The exact gain is adjusted by Horiz Gain potentiometer R752.

Transistor 0747 supplies the emitter current to both 0763 and 0753. The horizontal portion of the Beam Find circuitry affects the available current to 0747. Normally, -8.6 V is applied to the emitter of Q 747 from the BEAM FIND switch via CR745 and R746. When the BEAM FIND push button is pressed in, the direct -8.6 V is removed. In this condition, -8.6 V is supplied via R 745 which reduces the current available, thereby reducing the output voltage swing capability of 0763 and 0753 . Diodes CR772, CR782, CR783, and CR773 prevent the paraphase amplifier from overdriving the output amplifier stage when the $\times 10$ Magnification feature is in use.

Final amplification of the horizontal deflection signal is provided by the complementary-pair output stage. Both sides of the differential output amplifier are identical in function, so only one side is discussed in detail.

Transistors 0780 and 0785 form a cascode feedback amplifier. Gain of the stage is set by feedback resistor R785, and high-speed compensation is provided by C783 and adjustable capacitor C784. For dc and low-frequency components of the horizontal deflection signal, 0789 acts as a current source for 0785 . High-frequency components of the signal are coupled through C789 to the emitter of 0789 to speed up the output response time.

Emitter voltage for both 0780 and 0770 is supplied by a circuit composed of 0765 and associated components. The emitter voltage is maintained at a level that provides proper biasing for 0763 and Q753. Diodes CR770 and CR780 set up an emitter-bias difference between 0780 and Q770, causing the base voltage of both transistors to be equal.

## POWER SUPPLY

The Power Supply circuits provide all the low and high voltages required for operation of the instrument. The circuitry shown in Diagram 9 converts the ac-source voltage to the required levels through the action of a switching power supply. It does not have a primary power transformer.

## Power Input

The Power switch (S901) connects the line voltage to the instrument through line fuse F901 and transient suppressor VR901. Suppressor VR901 protects the instrument
from large voltage transients. High-frequency line noise is attenuated by C901.

## Preregulator

The Preregulator circuit converts the ac-power-source input voltage to a regulated dc voltage. A triac is used as a switch to conduct current during a controlled period of the input-line-voltage cycle so that energy to be used by the Inverter circuit is stored in capacitor C937.

Current from one side of the ac-power-source input will go through L.925 (a current-limiting impedance) and triac 0925. Diodes CR931 and CR933 (on the Main board) and CR932 and CR934 (on the Current Limit board) form a full-wave bridge rectifier circuit. The rectifier converts the ac-input voltage into dc pulses that charge C937. Surge arrestor VR938, connected in parallel with C937, conducts to protect the following circuitry should the Preregulator output voltage become too high.

The two-transistor circuit composed of 0933, 0938, and associated components provides overcurrent protection in the event of triac misfiring or ac-power-source transients. Transistor 0938 is an insulated-gate FET used as a switch in the charging path of C937. Transistor 0933 controls the FET bias to limit the current under abnormal firing conditions of Q925. In normal power-supply operation, the voltage developed across R937 is not sufficient to bias 0933 into conduction. The gate-to-source voltage of 0938 is set to 10 V by VR934 and R938, so the FET presents a low resistance to the charging current to C937. If triac 0925 should misfire to cause excessive current, Q933 becomes forward biased and 0938 is switched off to reduce the current. When 0938 switches off, the current that was flowing through Q938 flows through R939. The voltage drop developed across R939 causes current to flow through VR933 and R933, which holds Q933 on for most of the remainder of the ac-power-source input cycle. Resistor R939 limits the rate of collapse of the field around L.925 to prevent damage to Q938. Thermistor RT935 adjusts the bias of Q933 over varying ambient temperature.

PREREGULATOR CONTROL. The ac-source voltage is full-wave rectified by CR903 through CR906 and applied to a voltage divider composed of R911, R912, and R915. Output from this divider serves as a reference voltage for a ramp-and-pedestal comparator utilizing a programmable unijunction transistor (PUT), O921. Capacitor C912 filters the line noise to prevent false triggering of the PUT. Voltage-dropping resistor R914 provides current for zener diodes VR914 and VR915 to produce constant voltages during each half of the ac-power-source cycle.

When the instrument is first turned on, C917 is not charged. Capacitor C915 charges through CR917 to the
voltage of VR915 minus the diode drop of CR917. When the anode voltage of Q921 is greater than the gate voltage, Q921 will fire and C915 will discharge through the primary of T925. This event will happen after the peak of the voltage waveform. Pulse transformer T925 is connected to the gate of O925, and the discharge of C915 through the T925 primary winding is coupled to the secondary to cause triac Q925 to conduct. After firing, the triac will turn off again when the sinusoidal source voltage crosses through zero. As C917 charges through R917, 0918 current increases proportionally to charge C915 more rapidly. When C915 charges at a faster rate, the anode voltage of Q921 rises above the gate voltage earlier in the ac-source cycle and thereby causes 0925 to conduct for a longer period of time. The portion of the cycle preceding the zerocrossing point over which the triac is conducting is called the conduction angle. The conduction angle will increase from nearly zero (at turn on) to an angle sufficient to supply the energy needed by the inverter. Feedback from the inverter through optical isolator $\mathbf{U} 931$ holds the correct conduction angle by shunting current from R917. This shunting action controls the voltage on C917, thereby controlling the increase in base voltage on Q918. This action controls the charging rate of C915 and therefore the conduction angle of 0925 .

The Preregulator circuit can handle a wide range of input voltages by changing the conduction angle of the triac as the input voltage changes. As the input voltage increases, the conduction angle will decrease to maintain the Preregulator output voltage at a constant level. The voltage divider composed of R911, R912, and R915 produces an output voltage proportional to the input line voltage that is applied to the gate of Q921. Since VR914 and VR915 hold bias levels on Q918 constant regardless of input voltage, the point on the cycle at which Q921 fires will vary with changes in the ac-source voltage. This feedforward, together with the feedback from the Inverter through optical isolator U931, ensures a constant Preregulator output to the Inverter.

## Inverter

The Inverter circuit changes the dc voltage from the Preregulator to ac for use by the supplies that are connected to the secondaries of T940.

The output of the Preregulator circuit is applied to the center tap of T940. Power-switching transistors Q940 and Q942 alternate conducting current through R941 from the primary circuit common to the Preregulator output line. The transistor switching action is controlled by T942, a saturating base-drive transformer.

When the instrument is first turned on, one of the switching transistors will start to conduct and the collector
voltage will drop toward the common voltage level. This will induce a positive voltage from the lead of T942 which is connected to the base of the conducting transistor to reinforce conduction. Eventually T942 will saturate; and as the voltage across T942 (and T940) begins to reverse, the conducting transistor cuts off because of the drop in base drive. The other transistor will not start conduction until the voltage on the leads of T942 reverse enough to bias it on. This process will continue, and the saturation time of T942 plus the transistor-switching time will determine the frequency of Inverter operation (typically 20 kHz ). After the initial Inverter start up, the switching transistors do not saturate; they remain in the active region during switching.

Diodes CR940 and CR942 serve as a negative-peak detector to generate a voltage for controlling the outputs of both the Preregulator and the error amplifier. Capacitor C951 will charge to the peak amplitude of the collector voltage of Q940 and Q942. This voltage level is applied to the divider composed of R945, R946, and R947. The error amplifier, composed of Q948 and Q954, is a differential amplifier that compares the reference voltage of VR951 with the voltage on the wiper of potentiometer R946. The current through 0954 will set the base drive of Q956 and thereby control the voltage on C957. This voltage will bias Q940 and Q942 to a level that will maintain the peak-to-peak input voltage of T940. The amplitude of the voltage across the transformer primary winding and thus, that of the secondary voltages of T940, is set by adjusting -8.6 V Adj potentiometer R946.

At turn on, Q948 is biased off and 0954 is biased on. All the current of the error amplifier will therefore go through Q954 to bias on Q956. Diode CR956 allows the base of Q956 to go positive enough to initially turn on Q940 or Q942. The current through Q956 controls the base drive for Q940 and Q942. Base current provided by basedrive transformer T 942 will charge C957 negative with respect to the Inverter circuit floating ground (common) level.

Voltage from CR940 and CR942 also provides a measurement of the minimum collector voltage of Q940 and Q942 with respect to the Inverter circuit floating ground. This voltage is fed back to the Preregulator through optical isolator U931 to control the output voltage from the Preregulator circuit. As the negative peak voltage at the collectors of the switching transistors is regulated by the error amplifier with respect to the ouput of the Preregulator, control of the dc level from the Preregulator will control the minimum voltage with respect to the floating ground. Potentiometer R952 (Head Room Voltage Adjust) is used to set this minimum voltage level to a point that prevents saturation and excessive power dissipation of the Inverter switching transistors.

## CRT Supply

High-voltage multiplier U990 utilizes the 2-kV winding of T940 to generate 8 kV at one output to drive the crt anode. It also uses an internal half-wave rectifier diode to produce -2 kV for the crt cathode. The -2 kV supply is filtered by a three-stage low-pass filter composed of C990, R992, R990, C992, R994, C995, and R995. Neon lamp DS870 protects against excessive voltage between the crt heater and crt cathode by conducting if the voltage exceeds approximately 75 V .

## Auto Focus Circuit

Focus voltage is also developed from the -2 kV supply via a voltage divider composed of R884, R882, AUTO FOCUS potentiometer R883, R881, R880, R879, R878, R872, Auto Focus Adjust potentiomter R875, and 0877. The focus voltage tracks the intensity level through the action of Q877. The Intens Level signal from the Auto Intensity circuit (Diagram 6) is applied to the emitter of 0877 through R877. When the Intens Level signal changes due to a changing display intensity, the current through the divider resistors changes proportionally. Auto Focus Adjust potentiometer R875 is adjusted to produce the best focus tracking.

## Low-Voltage Supplies

The low-voltage supplies utilize the secondary windings of T940 and are all full-wave, center-tapped bridges. The +100 V supply uses CR961 and CR963 for rectification and uses C961 for filtering. Diodes CR965 and CR967 rectify ac from taps on the $100-\mathrm{V}$ winding, and C965 filters the output to produce +30 V dc. The diode bridge consisting of CR971 through CR974 produces the +8.6 V and -8.6 V supplies. Filtering of the +8.6 V is accomplished by C971, C975, and L971; while filtering of the -8.6 V is done by C972, C976, and L972. Voltage regulator U985 uses the rectified $+8.6-\mathrm{V}$ supply to produce the $+5 \cdot \mathrm{~V}$ output. Diode CR985 protects the regulator by not allowing the output voltage to go more positive than the +8.6 V input voltage.

## DC Restorer

The DC Restorer circuit produces the crt control-grid bias and couples both dc and low-frequency components of the Z-Axis Amplifier output to the crt control grid. Direct coupling of the $Z$-Axis Amplifier output to the crt control grid is not employed due to the high potential differences involved. Refer to Figure 3-8 during the following discussion.


Figure 3-8. Simplified diagram of the DC Restorer circuit.

## Theory of Operation-2215 Service

The ac drive to the DC Restorer circuit is obtained from pin 16 of T940. The drive voltage has a peak amplitude of about 150 V and a frequency of about 20 kHz . The sinusoidal drive voltage is coupled through C863 and R863 into the DC Restorer circuit at the junction of CR860, CR863, and R864. The cathode end of CR860 is held at about +85 V by the voltage applied from the wiper of Grid Bias potentiometer R860. When the positive peaks of the acdrive voltage reach a level that forward biases CR860, the voltage is clamped at that level.

The Z-Axis Amplifier output-signal voltage is applied to the DC Restorer at the anode end of CR863. The Z-Ax is signal voltage level varies between +10 V and +75 V , depending on the setting of the AUTO INTENSITY control. The ac-drive voltage will hold CR863 reverse biased until the voltage falls below the $Z$-Axis Amplifier output voltage level. At that point, CR863 becomes forward biased and clamps the junction of CR860, CR863, and R864 to the Z-Axis output level. Thus, the ac-drive voltage is clamped at two levels on the positive swing of the cycle to produce an approximate square-wave signal with a positive dc-offset level.

The DC Restorer is referenced to the $-2-\mathrm{kV}$ crt cathode voltage through R867 and CR867. Initially, both C865 and C864 will charge up to a level determined by the difference between the $Z$-Axis output voltage and the cathode voltage. Capacitor C865 charges from the crt cathode through R867, CR867, CR868, and R865 to the Z-Axis output. Capacitor C864 charges through R867, CR867, R864, and CR863 to the Z-Axis output.

When the ac-drive voltage starts its positive transition from the lower clamped level toward the higher clamped level, the charge on C864 increases due to the rising voltage.

The increase in charge acquired by C864 is proportional to the amplitude of the positive transistion. When the ac-drive voltage starts its negative transition from the upper clamped level to the lower clamped level, the negative transition is coupled through C864 to reverse bias CR867 and to forward bias CR868. The increased charge of C864 is then transferred to C865 as C864 discharges toward the Z-Axis output level. The amount of charge that is transferred is proportional to the setting of the AUTO INTENSITY control, since that control sets the lower clamping level of the ac-drive voltage.

The added charge on C865 also determines the controlgrid bias voltage. If more charge is added to the charge already present on C865, the control grid becomes more negative, and less crt writing-beam current will flow. Conversely, if less charge is added, the control-grid voltage level will be closer to the cathode-voltage level, and more crt writing-beam current flows.

During periods that C864 is charging, the crt control-grid voltage is held constant by the long time-constant discharge path of C865 through R868.

Fast-rise and fast-fall transitions of the Z-Axis output signal are coupled to the crt control grid through C865. The fast transitions start the crt writing-beam current toward the new intensity level. The DC Restorer output level then follows the Z-Axis output-voltage level to set the new bias voltage for the crt control grid.

Neon lamps DS867 and DS868 protect the crt from excessive grid-to-cathode voltage if the potential on either the control grid or the cathode is lost for any reason.

# PERFORMANCE CHECK PROCEDURE 

## INTRODUCTION

## PURPOSE

The "Performance Check Procedure" is used to verify the instrument's Performance Requirements as listed in the "Specification" (Section 1) and to determine the need for readjustment. These checks may also be used as an acceptance test, as a preliminary troubleshooting aid, and as a check of the instrument after repair. Removing the instrument's cover is not necessary to preform this procedure. All checks are made using the operator-accessible front- and rear-panel controls and connectors.

To ensure instrument accuracy, its performance should be checked after every 2000 hours of operation or once each year, if used infrequently.

## TEST EQUIPMENT REQUIRED

The test equipment listed in Table $4-1$ is a complete list of the equipment required to accomplish both the "Performance Check Procedure" in this section and the "Adjustment Procedure" in Section 5. Test equipment specifications described in Table 4-1 are the minimum necessary to provide accurate results. Therefore, equipment used must meet or exceed the listed specifications. Detailed operating instructions for test equipment are not given in this procedure. If more operating information is required, refer to the appropriate test-equipment instruction manual.

When equipment other than that recommended is used, control settings of the test setup may need to be altered. If the exact item of equipment given as an example in Table 4-1 is not available, first check the "Purpose" column to verify use of this item. If it is used for a check that is of little or no importance to your measurement requirements, the item and corresponding steps may be deleted. If the check is important, use the "Minimum Specification" column carefully to determine if any other available test equipment might suffice.

Special fixtures are used only where they simplify the test setup and procedure. These fixtures are available from Tektronix, Inc. and can be ordered by part number through your local Tektronix Field Office or representative.

## LIMITS AND TOLERANCES

The tolerances given in this procedure are valid for an instrument that is operating in and has been previously calibrated in an ambient temperature between $+20^{\circ} \mathrm{C}$ and $+30^{\circ} \mathrm{C}$. The instrument also must have had as least a $20-$ minute warm-up period. Refer to the "Specification" (Section 1) for tolerances applicable to an instrument operating outside this temperature range. All tolerances specified are for the instrument only and do not include test-equipment error.

## PREPARATION

Test equipment items 1 through 9 in Table 4-1 are required to accomplish a complete Performance Check. At the beginning of each subsection, in both the "Performance Check Procedure" and the "Adjustment Procedure" sections, there is an equipment-required list showing only the test equipment necessary for performing the steps in that subsection. In this list, the item number that follows each piece of equipment corresponds to the item number listed in Table 4-1.

This procedure is structured in subsections, which can be performed independently, to permit checking individual portions of the instrument. At the beginning of each subsection is a list of all the front-panel control settings required to prepare the instrument for performing Step 1 in that subsection. Each succeeding step within a particular subsection should then be performed, both in the sequence presented and in its entirety, to ensure that control-setting changes will be correct for ensuing steps.

Table 4-1
Test Equipment Required

| Item No. and Description | Minimum Specification | Purpose | Examples of Suitable Test Equipment |
| :---: | :---: | :---: | :---: |
| 1. Calibration Generator | Standard-amplitude signal levels: 10 mV to 50 V . Accuracy: $\pm 0.3 \%$. <br> High Amplitude signal levels: 1 V to 60 V . <br> Repetition rate: 1 kHz . <br> Fast-rise signal level: 1 V . <br> Repetition rate: 1 MHz . <br> Rise time: 1 ns or less. <br> Flatness: $\pm 0.5 \%$. | Vertical and horizontal checks and adjustments. | TEKTRONIX PG 506 Calibration Generator. ${ }^{\text {a }}$ |
| 2. Leveled Sine-Wave Generator | Frequency: 250 kHz to above 70 MHz . Output amplitude: variable from 10 mV to 5 V p-p. Output impedance: $50 \Omega$. Reference frequency: 50 kHz . Amplitude accuracy: constant within $3 \%$ of reference frequency as output frequency changes. | Vertical, horizontal, and triggering checks and adjustments. <br> Display adjustment and Z-axis check. | TEKTRONIX SG 503 Leveled Sine-Wave Generator. ${ }^{\text {a }}$ |
| 3. Time-Mark Generator | Marker outputs: 10 ns to 0.5 s . Marker accuracy: $\pm 0.1 \%$. Trigger output: 1 ms to $0.1 \mu \mathrm{~s}$, time-coincident with markers. | Horizontal checks and adjustments. <br> Display adjustment. | TEKTRONIX TG 501 Time Mark Generator. ${ }^{\text {a }}$ |
| 4. Cable (2 required) | Impedance: $50 \Omega$. Length: 42 in. Connectors: bnc. | Signal interconnection. | Tektronix Part Number 012-0057-01. |
| 5. Termination (2 required) | Impedance: $50 \Omega$. <br> Connectors: bnc. | Signal termination. | Tektronix Part Number 011-0049-01. |
| 6. Dual-Input Coupler | Connectors: bnc-female-to-dual-bne male. | Vertical checks and adjustments. | Tektronix Part Number 067-0525-01. |
| 7. $10 \times$ Attenuator | Ratio: 10X. Impedance: $50 \Omega$. Connectors: bnc. | Vertical compensation and triggering checks. | Tektronix Part Number 011-0059-02. |
| 8. T-Connector | Connectors: bnc. | Signal interconnection. | Tektronix Part Number 103-0030-00. |
| 9. Adapter | Connectors: bnc-male-tominiature probe tip. | Signal interconnection. | Tektronix Part Number 013-0084-02. |
| 10. Variable Autotransformer | Capable of supplying 1.5 A at 115 V . | Instrument input voltage adjustment. | General Radio W8MT3VM Variac Autotransformer. |

Table 4-1 (cont)

| Item No. and <br> Description | Minimum <br> Specification | Purpose | Examples of Suitable <br> Test Equipment |
| :--- | :--- | :--- | :--- |
| 11. Digital Voltmeter | Range: 0 to 140 V . Dc voltage <br> accuracy: $\pm 0.15 \% .4$ 1/2-digit <br> display. | Powver supply checks and <br> adjustment. <br> Vertical adjustment. | TEKTRONIX DM 501A <br> Digital Multimeter. |
| 12. Test Oscilloscope <br> with included 10X probe <br> (Standard Accessory) and <br> 1X probe (1X probe is <br> optional accessory). | Bandwidth: dc to 10 MHz. <br> Minimum deflection factor: | Power supply ripple check <br> and general troubleshooting. | a. TEKTRONIX 2213 <br> Oscilloscope. <br> b. TEKTRONIX P6101 <br> Probe (1X). Part Number <br> 010-6101-03. |
| 13. DC Voltmeter | Range: 0 to 2500 V. <br> calibrated to $1 \%$ accuracy at <br> -2000 V. | High-voltage power supply <br> check. | Triplett Model 630-NA. |
| 14. Screwdriver | Length: 3-in shaft. <br> Bit size: $3 / 32$ in. | Adjust variable resistors. | Xcelite R-3323. |

${ }^{\text {a }}$ Requires a TM 500-series power-module mainframe.
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## VERTICAL

## Equipment Required (see Table 4-1):

Calibration Generator (Item 1)
Leveled Sine-Wave Generator (Item 2)
$50-\Omega$ BNC Cable (Item 4)

$$
\begin{aligned}
& 50-\Omega \text { BNC Termination (Item 5) } \\
& \text { Dual-Input Coupler (Item 6) }
\end{aligned}
$$

## INITIAL CONTROL SETTINGS

POWER

## CRT <br> AUTO INTENSITY AUTO FOCUS

Vertical
POSITION (both)
VERTICAL MODE
CH 1 VOLTS/DIV
CH 2 VOLTS/DIV
VOLTS/DIV Variable
(both)
INVERT
AC-GND-DC (both)

Horizontal
POSITION HORIZONTAL MODE A AND B SEC/DIV A AND B SEC/DIV Variable X10 Magnifier

## Trigger

VAR HOLDOFF A TRIGGER MODE A TRIGGER SLOPE A TRIGGER LEVEL A \& B INT A SOURCE

ON (button in)

As desired Best focused display

Midrange
CH 1
2 mV
10 V
CAL detent
Normal (button out)
DC

Midrange
A
0.5 ms

CAL detent
Off (knob in)

NORM
AUTO
J
Midrange
VERT MODE
INT

## PROCEDURE STEPS

1. Check Deflection Accuracy and Variable Range
a. Connect a $10-\mathrm{mV}$ standard-amplitude signal to the $\mathrm{CH} 1 \mathrm{OR} \times$ input connector using a $50-\Omega$ cable.
b. CHECK-Deflection accuracy is within the limits given in Table 4-2 for each CH 1 VOLTS/DIV switch setting and corresponding standard-amplitude signal. When at the $20-\mathrm{mV}$ VOLTS/DIV switch setting, rotate the CH 1 VOLTS/DIV Variable control fully counterclockwise and CHECK that the display decreases to 2 divisions or less. Then return the VOLTS/DIV Variable control to the CAL detent and continue with the $50-\mathrm{mV}$ check.
c. Set the VERTICAL MODE switch to CH 2 and move the cable from the CH 1 OR X input connector to the CH 2 OR Y input connector.

Table 4-2
Deflection Accuracy Limits

| VOLTS/DIV <br> Switch <br> Setting | Standard <br> Amplitude <br> Signal | Vertical <br> Deflection <br> (Divisions) | $3 \%$ Accuracy <br> Limits <br> (Divisions) |
| :---: | :---: | :---: | :---: |
| 2 mV | 10 mV | 5 | 4.85 to 5.15 |
| 5 mV | 20 mV | 4 | 3.88 to 4.12 |
| 10 mV | 50 mV | 5 | 4.85 to 5.15 |
| 20 mV | 0.1 V | 5 | 4.85 to 5.15 |
| 50 mV | 0.2 V | 4 | 3.88 to 4.12 |
| 0.1 V | 0.5 V | 5 | 4.85 to 5.15 |
| 0.2 V | 1 V | 5 | 4.85 to 5.15 |
| 0.5 V | 2 V | 4 | 3.88 to 4.12 |
| 1 V | 5 V | 5 | 4.85 to 5.15 |
| 2 V | 10 V | 5 | 4.85 to 5.15 |
| 5 V | 20 V | 4 | 3.88 to 4.12 |
| 10 V | 50 V | 5 | 4.85 to 5.15 |

d. CHECK-Deflection accuracy is within the limits given in Table 4-2 for each CH 2 VOLTS/DIV switch setting and corresponding standard-amplitude signal. Perform the checks from the bottom to the top of Table 4-2 to avoid unnecessary switch-position changes. When at the $20-\mathrm{mV}$ VOLTS/DIV switch setting, rotate the CH 2 VOLTS/DIV Variable control fully counterclockwise and CHECK that the display decreases to 2 divisions or less. Then return the VOLTS/DIV Variable control to the CAL detent and finish the check.
e. Disconnect the test setup.

## 2. Check Bandwidth

a. Set:

| VOLTS/DIV (both) | 2 mV |
| :--- | :--- |
| SEC/DIV | $20 \mu \mathrm{~s}$ |

b. Connect the leveled sine-wave generator output via a $50-\Omega$ cable and a $50-\Omega$ termination to the CH 1 OR $\times$ input connector.
c. Set the generator output amplitude for a 5-division, $50-\mathrm{kHz}$ display.
d. Change the generator output frequency to the value shown in Table 4-3 for the corresponding VOLTS/DIV switch setting.

Table 4-3
Settings for Bandwidth Checks

| VOLTS/DIV <br> Switch Settings | Generator <br> Output Frequency |
| :---: | :---: |
| 2 mV to 10 mV | 50 MHz |
| 20 mV to 10 V | 60 MHz |

e. CHECK-Display amplitude is 3.5 divisions or greater.
f. Repeat parts cthrough e for all indicated CH 1 VOLTS/DIV switch settings, up to the output-voltage upper limit of the sine-wave generator being used.
g. Move the generator output signal from the CH 1 OR $X$ input connector to the CH 2 OR $Y$ input connector. Set the VERTICAL MODE switch to CH 2.
h. Repeat parts c through e for all indicated CH 2 VOLTS/DIV switch settings, up to the output-voltage upper limit of the sine-wave generator being used.

## 3. Check Common-Mode Rejection Ratio

a. Set both VOLTS/DIV switches to 20 mV .
b. Connect a $10-\mathrm{MHz}$, leveled sine-wave signal via a $50-\Omega$ cable, a $50-\Omega$ termination, and a dual-input coupler to the CH 1 ORX and the CH 2 ORY input connectors.
c. Set the generator output amplitude to produce a 6 -division display.
d. Vertically center the display using the Channel 2 POSITION control. Then set VERTICAL MODE to CH 1 and vertically center the display using the Channel 1 POSITION control.
e. Set the VERTICAL MODE switches to BOTH and ADD; then push in the INVERT button.
f. CHECK-Display amplitude is 0.6 division or less.
g. If the check in part $f$ meets the requirement, skip to part $n$. If it does not, continue with part $h$.
h. Set VERTICAL MODE to CH 1.
i. Change the generator frequency to 50 kHz and adjust the output to obtain a 6 -division display.
j. Set VERTICAL MODE to BOTH.
k. Adjust the CH 2 VOLTS/DIV Variable contol for minimum display amplitude (best CMRR).

1. Change the generator frequency to 10 MHz .
m. CHECK-Display amplitude is 0.6 division or less.
n. Disconnect the test setup.

## HORIZONTAL

## Equipment Required (see Table 4-1):

Calibration Generator (Item 1)
Leveled Sine-Wave Generator (Item 2)
Time-Mark Generator (Item 3)

Two 50- $\Omega$ BNC Cables (Item 4)
Two $50-\Omega \mathrm{BNC}$ Terminations (Item 5)

## INITIAL CONTROL SETTINGS

POWER

## CRT <br> AUTO INTENSITY AUTO FOCUS

Vertical

| Channel 1 POSITION | Midrange |
| :--- | :--- |
| VERTICAL MODE | CH 1 |
| CH 1 VOLTS/DIV | 0.5 V |
| CH 1 VOLTS/DIV |  |
| Variable | CAL detent |
| INVERT | Normal (button out) |
| Channel 1 AC-GND-DC | DC |
| Channel 2 AC-GND-DC | GND |


| Trigger |  |
| :--- | :--- |
| VAR HOLDOFF | NORM |
| A TRIGGER MODE | NORM |
| SLOPE (both) | $J$ |
| LEVEL (both) | Midrange |
| A \& B INT | VERT MODE |
| A SOURCE | EXT |
| A EXT COUPLING | DC $\div 10$ |

## PROCEDURE STEPS

## 1. Check Timing Accuracy

a. Connect 50 -ns time markers from the time-mark generator via a $50-\Omega$ cable and a $50-\Omega$ termination to the CH 1 OR X input connector. Connect the generator Trigger output via a $50-\Omega$ cable and a $50-\Omega$ termination to the EXT INPUT connector.
b. Use the Channel 1 POSITION control to center the trace vertically. Adjust the A TRIGGER LEVEL control for a stable, triggered display.
c. Use the Horizontal POSITION control to align the first time marker that is 50 ns beyond the start of the sweep with the 2 nd vertical graticule line.

NOTE
When making timing measurements, use as a reference the same point on each time marker.
d. CHECK-Timing accuracy is within the limits shown in Table 4-4 for the applicable position of the $\times 10 \mathrm{Mag}$ nifier. When making the check with the X10 Magnifier On, exclude any portion of the sweep past the 100th magnified division.

Table 4-4
$A$ and $B$ Timing Accuracy

| X10 Magnifier | Accuracy at 10th Vertical Graticule Line |
| :---: | :---: |
| Off (knob in) | $3 \%(0.24$ division) |
| On (knob out) | $5 \%(0.40$ division) |

e. Set the HORIZONTAL MODE switch to B and adjust the B TRIGGER LEVEL control for a stable display.
f. Align the first time marker that is 50 ns beyond the start of the sweep with the 2nd vertical graticule line, using the Horizontal POSITION control.
g. CHECK-Timing accuracy is within the limits shown in Table 4-4 for the applicable position of the $\times 10$ Magnifier. When making the check with the X10 Magnifier On, exclude any portion of the sweep past the 100th magnified division.
h. Set the HORIZONTAL MODE switch to A.
i. Repeat parts $b$ through $h$ for the $A$ and B SEC/DIV and time-mark generator setting combinations shown in Table 4-5 under the "Normal" column.

Table 4-5
Settings for Timing Accuracy Checks

| A AND B | Time-Mark Generator Output |  |
| :---: | :---: | :---: |
| Switch Setting | Normal | X10 Magnified |
| $0.05 \mu \mathrm{~s}$ | 50 ns | 10 ns |
| $0.1 \mu \mathrm{~s}$ | $0.1 \mu \mathrm{~s}$ | 10 ns |
| $0.2 \mu \mathrm{~s}$ | $0.2 \mu \mathrm{~s}$ | 20 ns |
| $0.5 \mu \mathrm{~s}$ | $0.5 \mu \mathrm{~s}$ | 50 ns |
| $1 \mu \mathrm{~s}$ | $1 \mu \mathrm{~s}$ | $0.1 \mu \mathrm{~s}$ |
| $2 \mu \mathrm{~s}$ | $2 \mu \mathrm{~s}$ | $0.2 \mu \mathrm{~s}$ |
| $5 \mu \mathrm{~s}$ | $5 \mu \mathrm{~s}$ | $0.5 \mu \mathrm{~s}$ |
| $10 \mu \mathrm{~s}$ | $10 \mu \mathrm{~s}$ | $1 \mu \mathrm{~s}$ |
| $20 \mu \mathrm{~s}$ | $20 \mu \mathrm{~s}$ | $2 \mu \mathrm{~s}$ |
| $50 \mu \mathrm{~s}$ | $50 \mu \mathrm{~s}$ | $5 \mu \mathrm{~s}$ |
| 0.1 ms | 0.1 ms | $10 \mu \mathrm{~s}$ |
| 0.2 ms | 0.2 ms | $20 \mu \mathrm{~s}$ |
| 0.5 ms | 0.5 ms | $50 \mu \mathrm{~s}$ |
| 1 ms | 1 ms | 0.1 ms |
| 2 ms | 2 ms | 0.2 ms |
| 5 ms | 5 ms | 0.5 ms |
| 10 ms | 10 ms | 1 ms |
| 20 ms | 20 ms | 2 ms |
| 50 ms | 50 ms | 5 ms |
|  | eep Only |  |
| 0.1 s | 0.1 s | 10 ms |
| 0.2 s | 0.2 s | 20 ms |
| 0.5 s | 0.5 s | 50 ms |

j. Set:
A and B SEC/DIV
$0.05 \mu \mathrm{~s}$
X10 Magnifier
On (knob out)
k. Select 10 -ns time markers from the time-mark generator.
I. Repeat parts $b$ through $h$ for the $A$ and B SEC/DIV and time-mark generator setting combinations shown in Table 4-5 under the "X10 Magnified" column.

## 2. Check Delay Time Position Range

a. Set:

| Channel 1 AC-GND-DC | GND |
| :--- | :--- |
| HORIZONTAL MODE | ALT |
| A AND B SEC/DIV | 0.2 ms |

b. Align the start of the A sweep with the 1st vertical graticule line.
c. CHECK-Intensified portion of the trace starts within 0.5 division of the start of the sweep.
d. Rotate the B DELAY TIME POSITION control fully clockwise.
e. CHECK-Intensified zone is past the 11th vertical graticule line.

## 3. Check SEC/DIV Variable Range

a. Set:

| CH 1 VOLTS/DIV | 0.5 V |
| :--- | :--- |
| Channel 1 AC-GND-DC | DC |
| HORIZONTAL MODE | A |
| A SEC/DIV | 0.2 ms |
| SEC/DIV Variable | Fully counterclockwise |
| X10 Magnifier | Off (knob in) |

b. Select $0.5-\mathrm{ms}$ time markers from the time-mark generator.
c. CHECK-Time markers are 1 division or less apart.
d. Return the SEC/DIV Variable control to the CAL detent.

## 4. Check Delay Time Dial Accuracy

a. Set:

| HORIZONTAL. MODE | B |
| :--- | :--- |
| A SEC/DIV | $0.2 \mu \mathrm{~s}$ |
| B SEC/DIV | $0.05 \mu \mathrm{~s}$ |
| B TRIGGER LEVEL | CW -RUN AFTER DLY |

b. Select $0.2-\mu$ s time markers.
c. Set the B DELAY TIME POSITION control to 1.00 . Adjust the Horizontal POSITION control so that the top of the first fully displayed time marker is aligned with the center vertical graticule line.
d. Without changing the Horizontal POSITION control setting, set the B DELAY TIME POSITION dial setting to 9.00. Slightly readjust the B DELAY TIME POSITION control to align the top of the time marker with the center vertical graticule line.
e. CHECK-The B DELAY TIME POSITION dial setting is between 8.87 and 9.14 .
f. Set:
A SEC/DIV
0.5 ms
B SEC/DIV
$50 \mu \mathrm{~s}$
g. Select $0.5-\mu \mathrm{s}$ time markers.
h. Repeat parts c through e .

## 5. Check Delay Jitter

a. Set the B SEC/DIV switch to $0.5 \mu \mathrm{~s}$.
b. Select $10-\mu$ s time markers.
c. Slightly readjust the B DELAY TIME POSITION dial to position a time marker with in the graticule area.
d. CHECK-Jitter on the leading edge of the time marker does not exceed 1 division. Disregard slow drift.

## 6. Check POSITION Control Range

a. Set:

A SEC/DIV
HORIZONTAL MODE
$10 \mu \mathrm{~s}$
A
b. Select $50-\mu$ s time markers.
c. Align the 3rd time marker with the center vertical graticule line.
d. Set the X 10 Magnifier knob to On (knob out).
e. CHECK-Magnified time marker can be positioned to the left of the center vertical graticule line by rotating the Horizontal POSITION control fully counterclockwise.
f. CHECK-Start of the sweep can be positioned to the right of the center vertical graticule line by rotating the Horizontal POSITION control fully clockwise.
g. Disconnect the test setup.

## 7. Check X-Gain

a. Set:
CH 1 VOLTS/DIV
A SEC/DIV
20 mV
X-Y
b. Connect a $0.1-\mathrm{V}$ standard-amplitude signal to the CH 1 OR $X$ input connector using a $50-\Omega$ cable.
c. CHECK-Display is 5 divisions $\pm 0.25$ division ( 4.75 to 5.25 divisions).
d. Disconnect the test setup.

## 8. Check X-Bandwidth

a. Connect a $50-\mathrm{kHz}$ leveled sine-wave signal via a $50-\Omega$ cable and a $50-\Omega$ termination to the CH 1 OR X input connector.
b. Set the generator to obtain a 5-division horizontal display.
c. Adjust the generator output frequency to 2 MHz .
d. CHECK-Display is at least 3.5 divisions in length.
e. Disconnect the test setup.

## TRIGGERING

Equipment Required (see Table 4-1):
Leveled Sine-Wave Generator (Item 2)
$50-\Omega$ BNC Cable (Item 4)
$50-\Omega$ BNC Termination (Item 5)
10X Attenuator (Item 7)

BNC T-Connector (Item 8)
Probe-tip-to-BNC Adapter (Item 9)
P6120 Probe (provided with instrument)

## INITIAL CONTROL SETTINGS

POWER
CRT
AUTO INTENSITY
AUTO FOCUS

ON (button in)

As desired Best focused display

## Vertical

POSITION (both)
VERTICAL MODE
CH 1 VOLTS/DIV
CH 2 VOLTS/DIV VOLTS/DIV Variable (both)
INVERT
AC-GND-DC (both)

## Horizontal

POSITION
HORIZONTAL MODE
A AND B SEC/DIV
A AND B SEC/DIV
Variable
X10 Magnifier

Midrange
CH 1
2 mV
20 mV
CAL detent
Normal (button out)
DC

## Midrange

A
$0.2 \mu \mathrm{~s}$
CAL detent
Off (knob in)

PROCEDURE STEPS

## 1. Check Internal Triggering

a. Connect the leveled sine-wave generator output via a $50-\Omega$ cable and a $50-\Omega$ termination to the CH 1 OR X input connector.
b. Set the generator output to produce a 4-division, $2-\mathrm{MHz}$ display.
c. Set the CH 1 VOLTS/DIV switch to 20 mV .
d. CHECK-Stable display can be obtained by adjusting the A TRIGGER LEVEL control for each switch combination given in Table 4-6.

Table 4-6
Switch Combinations for A Triggering Checks

| TRIGGER MODE | TRIGGER SLOPE |
| :---: | :---: |
| NORM | $\Gamma$ |
| NORM | $\nearrow$ |
| AUTO | $\boxed{ }$ |
| AUTO | $\Gamma$ |

e. Set the HORIZONTAL MODE switch to B.
f. CHECK-Stable display can be obtained by adjusting the B TRIGGER LEVEL control for both positive- and negative-going positions of the B TRIGGER SLOPE switch.
g. Set:

VERTICAL MODE HORIZONTAL MODE

CH 2
A
n. Repeat part d.
o. Adjust the generator output and the A TRIGGER LEVEL control for a stable, 2-division display.
h. Move the generator output from the CH 1 OR $X$ input connector to the CH 2 OR Y input connector. Set VERTICAL MODE to CH 2.
i. Repeat parts $d$ through $f$.
j. Set:

## HORIZONTAL MODE A SEC/DIV <br> A $0.05 \mu \mathrm{~s}$

k. Set the generator to produce a 1.5 -division, $60-\mathrm{MHz}$ display.
I. Repeat part d.
m. Move the generator output from the CH 2 OR $Y$ input connector to the CH 1 OR $X$ input connector. Set VERTICAL MODE to CH 1.
p. Repeat parts $e$ and $f$.
q. Move the generator output from the CH 1 OR X input connector to the CH 2 OR $Y$ input connector. Set VERTICAL MODE to CH 2.
r. Repeat part f.
s. Disconnect the test setup.
2. Check External Triggering
a. Set:

| VOLTS/DIV | 10 mV |
| :--- | :--- |
| A SEC/DIV | $10 \mu \mathrm{~S}$ |
| VERTICAL MODE | CH 1 |



Figure 4.1. Test setup for external trigger and jitter checks.
b. Connect the test setup as shown in Figure 4-1.
c. Set the leveled sine-wave generator to produce a 5 -division, $50-\mathrm{kHz}$ display.
d. Set:

| VERTICAL MODE | CH 2 |
| :--- | :--- |
| A SEC/DIV | $0.2 \mu$ S |
| A SOURCE | EXT |

e. Move the signal from the CH 1 OR X input connector to the EXT INPUT connector.
f. Set the generator to 2 MHz .
g. CHECK-Stable display can be obtained by adjusting the A TRIGGER LEVEL control for each switch combination given in Table 4-6.
h. Remove the 10X attenuator from the test setup and set the A EXT COUPLING switch to $\mathrm{DC} \div 10$.
j. Set:

| VOLTS/DIV (both) | 50 mV |
| :--- | :--- |
| VERTICAL MODE | CH 1 |
| A SEC/DIV | $20 \mu \mathrm{~S}$ |
| A SOURCE | INT |

k. Reconnect the test setup as shown in Figure 4-1.
I. Set the leveled sine-wave generator to produce a 5 -division, $50-\mathrm{kHz}$ display.
m. Set:

VERTICAL MODE CH 2
A SEC/DIV $\quad 0.05 \mu \mathrm{~S}$
X10 Magnifier On (knob out)
A SOURCE EXT
n. Repeat part e.
o. Set the generator to 60 MHz .
p. Repeat parts $g$ and $h$.
q. Repeat part g.
r. Disconnect the test setup.

## EXTERNAL Z-AXIS AND PROBE ADJUST

## Equipment Required (see Table 4-1):

Leveled Sine-Wave Generator (Item 2)
Two 50- $\Omega$ BNC Cables (Item 4)

BNC T-Connector (Item 8)
P6120 Probe (provided with instrument)

## INITIAL CONTROL SETTINGS

POWER
CRT
AUTO INTENSITY
AUTO FOCUS

Vertical
Channel 1 POSITION
VERTICAL MODE
CH 1 VOLTS/DIV
CH 1 VOLTS/DIV
Variable
Channel 1 AC-GND-DC

Horizontal
POSITION
HORIZONTAL MODE
A SEC/DIV
A AND B SEC/DIV
Variable

## Trigger

VAR HOLDOFF
A TRIGGER MODE
A TRIGGER SLOPE
A TRIGGER LEVEL
A \& B INT
A SOURCE

ON

As desired
Best defined display

Midrange
CH 1
2 V

CAL detent
DC

Midrange
A
$20 \mu \mathrm{~s}$
CAL detent

NORM
AUTO
J
Midrange
VERT MODE
INT

## PROCEDURE STEPS

## 1. Check EXT Z-AXIS Operation

a. Connect the leveled sine-wave generator output via a T-connector and two $50-\Omega$ cables to the EXT Z-AXIS INPUT connector on the rear panel and to the CH 1 OR $X$ input connector.
b. Adjust the generator controls to produce a 5 -volt, 50 kHz display.
c. CHECK-For noticeable intensity modulation. The positive part of the sine wave should be of lower intensity than the negative part.
d. Disconnect the test setup.

## 2. Check PROBE ADJUST Operation

a. Set:

```
CH 1 VOLTS/DIV
A SEC/DIV
10 mV 0.5 ms
```

b. Connect the P 6120 Probe to the CH 1 OR X input connector and insert the probe tip into the PROBE ADJUST jack on the instrument front panel. If necessary, adjust the probe compensation for a flat-topped squarewave display.
c. CHECK-Display is 5 divisions $\pm 1$ division ( 4 to 6 divisions).
d. Disconnect the test setup.

# ADJUSTMENT PROCEDURE 

## INTRODUCTION

## IMPORTANT—PLEASE READ BEFORE USING THIS PROCEDURE

## PURPOSE

The "Adjustment Procedure" is used to return the instrument to conformance with its "Performance Requirements" as listed in the "Specification" (Section 1). These adjustments should be performed only after the checks in the "Performance Check Procedure" (Section 4) have indicated a need for adjustment of the instrument.

## TEST EQUIPMENT REQUIRED

The test equipment listed in Table $4-1$ is a complete list of the equipment required to accomplish both the "Adjustment Procedure" in this section and the "Performance Check Procedure" in Section 4. Test equipment specifications described in Table 4-1 are the minimum necessary to provide accurate results. Therefore, equipment used must meet or exceed the listed specifications. Detailed operating instructions for test equipment are not given in this procedure. If more operating information is required, refer to the appropriate test equipment instruction manual.

When equipment other than that recommended is used, control settings of the test setup may need to be altered. If the exact item of equipment given as an example in Table 4-1 is not available, first check the "Purpose" column to verify use of this item. Then use the "Minimum Specification" column to determine if any other available test equipment might suffice.

## LIMITS AND TOLERANCES

The limits and tolerances stated in this procedure are instrument specifications only if they are listed in the "Performance Requirements" column of the "Specification" (Section 1). Tolerances given are applicable only to the instrument undergoing adjustment and do not include test equipment error. Adjustment of the instrument must
be accomplished at an ambient temperature between $+20^{\circ} \mathrm{C}$ and $+30^{\circ} \mathrm{C}$, and the instrument must have had a warm-up period of at least 20 minutes.

## PARTIAL PROCEDURES

This procedure is structured in subsections to permit adjustment of individual sections of the instrument (except the Power Supply) whenever a complete readjustment is not required. For example, if only the Vertical section fails to meet the Performance Requirements (or has had repairs made or components replaced), it can be readjusted with little or no effect on other sections of the instrument. However, if the Power Supply section has undergone repairs or adjustments that change the absolute value of any of the supply voltages, a complete readjustment of the instrument may be required.

At the beginning of each subsection is a list of all the front-panel control settings required to prepare the instrument for performing Step 1 in that subsection. Each succeeding step within a subsection should then be performed both in the sequence presented and in its entirety to ensure that control settings will be correct for ensuing steps.

## ADJUSTMENT INTERACTION

The use of Table 5-1 is particularly important if a partial procedure is performed or if a circuit requires readjustment due to a component replacement. To use this table, first find the adjustment that was made (extreme left column). Then move to the right, across the row, until you come to a darkened square. From the darkened square, move up the table to find the affected adjustment at the heading of that column. Check the accuracy of this adjustment and, if necessary, perform readjustment.

| Table 5-1 <br> Adjustment Interactions |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Adjustments or Replacements Made | AdJustments Affected |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | (1) |  |
| -8.6V ADJ |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| HEAD ROOM VOLTACE |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| TRACE ROTATION |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| GRID BIAS |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| ASTIC |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| AUTO FOCUS ADJ |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| CEOMETRY |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| VERTICAL CAIN |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| ATTENUATOR STEP BALANCE |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| ATTENUATOR XID BALANCE |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| INVERT BALANCE |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| CH 1/CH 2 BALANCE |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| ATTENUATOR COMP |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| VERTICAL OUTPUT COMP |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| CH 18 CH 2 HF MATCH |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| HORIZ CAIN |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| HORIZ B GAIN |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| HORIZ XID CAIN |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| MAG RECISTRATION |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| DELAY DIAL START ADJ |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| DELAY DIAL CAIN |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| $5 \mu s$ TIMING (A AND B) |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| HIGH SPEED TIMING |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| $\times$ GAIN |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| SLOPE BALANCE |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| AUTO TRIGCER CENTERING |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| CRT REPLACEMENT |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | 3826-43 |

Specific interactions are also called out within certain adjustment steps to indicate that adjustments must be repeated until no further improvement is noted.

## PREPARATION FOR ADJUSTMENT

It is necessary to remove the instrument cabinet to perform the Adjustment Procedure. See the "Cabinet" removal instructions located in the "Maintenance" section of the manual.

Before performing this procedure, do not preset any internal controls and do not change the -8.6 V PowerSupply adjustment, since that will typically necessitate a complete readjustment of the instrument, when only a partial readjustment might otherwise be required. To avoid unnecessary readjustment, only change an internal control setting whenever a Performance Characteristic cannot be met with the original setting. If it is necessary to change the setting of any internal control, always check Table 5-1 for possible interacting adjustments that might be required.

All test equipment items described in Table 4-1 are required to accomplish a complete Adjustment Procedure. At the beginning of each subsection there is an equipmentrequired list showing only the test equipment necessary for performing the steps in that subsection. In this list, the item number following each piece of equipment corresponds to the item number listed in Table 4-1.

Make initial control settings as listed at the beginning of each subsection. Then connect the test equipment to an appropriate ac-power-input source and connect the 2215 to a variable autotransformer (Item 10 in Table 4-1) that is set for 115 V ac. Apply power and allow a 20 -minute warm-up period before commencing any adjustments.

The most accurate display adjustments are made with a stable, well-focused, low-intensity display. Unless otherwise noted, adjust the AUTO INTENSITY, AUTO FOCUS, and TRIGGER LEVEL controls as needed to view the display.

Wherever possible in this procedure, instrument performance is first checked before an adjustment is made. Steps containing both checks and adjustments are titled "Check/Adjust." Those steps with only checks are titled "Check."

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## POWER SUPPLY AND CRT DISPLAY

## Equipment Required (see Table 4-1)

Leveled Sine-Wave Generator (Item 2)
Time-Mark Generator (Item 3)
$50-\Omega$ BNC Cable (Item 4)
$50-\Omega$ BNC Termination (Item 5)
Variable Autotransformer (Item 10)

Digital Voltmeter (Item 11)
Test Oscilloscope and 1X Probe (Item 12)
DC Voltmeter (Item 13)
Screwdriver (Item 14)

## NOTE

Before applying power to the 2215, make the initial control settings. Connect the 2215 to an appropriate power source through a variable autotransformer, adjusted for an output of 115 V . Apply power to both the instrument and the test equipment and allow a 20 -minute warm-up period before commencing the adjustments and checks.

## INITIAL CONTROL SETTINGS

## CRT <br> AUTO INTENSITY AUTO FOCUS

## Vertical (both)

POSITION
VERTICAL MODE
VOLTS/DIV
VOLTS/DIV Variable
AC-GND-DC

## Horizontal

POSITION
HORIZONTAL MODE
A SEC/DIV
A AND B SEC/DIV
Variable
$\times 10$ Magnifier
Trigger
VAR HOLDOFF
A TRIGGER MODE
A TRIGGER SLOPE
A TRIGGER LEVEL
A \& B INT
A SOURCE

As desired Best focused display

Midrange
CH 1
0.1 V

CAL detent
GND

Midrange
A
$5 \mu \mathrm{~s}$
CAL detent
Off (knob in)

NORM
TV FIELD
$\Gamma$
Midrange
VERT MODE
INT

## PROCEDURE STEPS

## 1. Check/Adjust Power Supply DC Levels and Ripple (R946 and R952) <br> NOTE <br> Review the information at the beginning of the Adjustment Procedure before starting this step.

a. Remove the High-Voltage shield (see the "HighVoltage Shield" removal procedure in Section 6).

## WARNING

When checking the Head Room Voltage, use a digital voltmeter that is isolated from ground, because the Inverter power-supply circuitry common is at line potential.
b. Connect the digital voltmeter low lead to common (TP934) and connect the volts lead to TP952.
c. $\mathrm{CHECK}-$ Reading is +4.2 V to +4.4 V . If the reading is within these limits, skip to part e.
d. ADJUST-Head Room Voltage Adjust (R952) for +4.3 V .
e. Disconnect the voltmeter leads.
f. Connect the digital voltmeter low lead to chassis ground (TP501) and connect the volts lead to the $-8.6 \cdot \mathrm{~V}$ supply (TP500).
g. CHECK-Reading is -8.64 V to -8.56 V . If the reading is within these limits, skip to part i.
h. ADJUST-The -8.6-V Adj (R946) for -8.6 V .
i. Replace the High-Voltage shield (see the "HighVoltage Shield" reinstallation procedure in Section 6).
j. CHECK-Voltage levels of the remaining power supplies listed in Table 5-2 are within their specified limits.

Table 5-2
Power Supply Limits and Ripple

| Power <br> Supply | Test <br> Point | Reading <br> (Volts) | P-P Ripple <br> (mV) |
| :---: | :---: | :---: | :---: |
| -8.6 V | TP500 | -8.56 to -8.64 | $<10$ |
| +5 V | W985 | 4.75 to 5.25 | $<10$ |
| +8.6 V | W975 | 8.34 to 8.86 | $<10$ |
| +30 V | W965 | 28.5 to 31.5 | $<50$ |
| +100 V | W966 | 95 to 105 | $<200$ |

k. Connect the test oscilloscope, using a 1 X probe, to the first test point indicated in Table 5-2 and connect the probe ground lead to TP501.
I. CHECK-Ripple amplitude of the de supply is within the typical value given in Table 5-2.
m. Repeat parts $k$ and $\mid$ for each test point in Table 5-2.
n. Disconnect the test setup.

## 2. Check High-Voltage Supply

a. Set the POWER switch to OFF (button out).
b. Set the dc voltmeter to a range of at least -2500 V dc and connect the volts lead to chassis ground. Remove the crt base-socket cover and connect the common lead of the dc voltmeter to pin 2 on the socket.
c. Set the POWER switch to ON (button in).
d. CHECK - High Voltage Supply dc level is -1900 V to
-2100 V.
e. Set the POWER switch to OFF (button out).
f. Disconnect the voltmeter leads and re-install the crt base-socket cover.
g. Set the POWER switch to ON (button in).

## 3. Adjust CRT Grid Bias (R860)

a. Set the A SEC/DIV switch to $X-Y$.
b. Rotate the AUTO INTENSITY control fully counterclockwise.
c. Connect a $50-\Omega$ termination to the EXT $Z$ AXIS INPUT connector located on the rear panel.
d. ADJUST-Both the Grid Bias adjustment (R860) and the AUTO FOCUS control for a visible dot. Then back off the Grid Bias potentiometer until the dot just disappears.
e. Disconnect the test setup.

## 4. Adjust Astigmatism and Auto Focus Tracking (R887 and R875)

a. Set:

| Channel 1 AC-GND-DC | DC |
| :--- | :--- |
| A SEC/DIV | $20 \mu \mathrm{~s}$ |
| A TRIGGER MODE | AUTO |

b. Connect a leveled sine-wave generator via a $50-\Omega$ cable and a $50-\Omega$ termination to the CH 1 OR $X$ input connector.
c. Adjust the generator output for a 4 -division, $50-\mathrm{kHz}$ display.
d. ADJUST-Both the Astig adjustment (R887) and the AUTO FOCUS control for the best focused display over the range of the AUTO INTENSITY control.
e. Set the A SEC/DIV switch to $5 \mu \mathrm{~s}$.
f. ADJUST-Auto Focus Adj (R875) for the best focused display. Do not change the front panel AUTO FOCUS control.
g. Disconnect the test setup.

## 5. Check/Adjust Trace Alignment (TRACE ROTATION)

a. Set the Channel 1 AC-GND-DC switch to GND.
b. CHECK-That the trace is parallel to the center horizontal graticule line.
c. ADJUST-The front-panel TRACE ROTATION control to align the trace with the center horizontal graticule line.

## 6. Adjust Geometry (R870)

a. Set:

| CH 1 VOLTS/DIV | 50 mV |
| :--- | :--- |
| Channel 1 AC-GND-DC | DC |

b. Connect $50-\mu \mathrm{s}$ time markers from the time-mark generator via a $50-\Omega$ cable and a $50-\Omega$ termination to the CH 1 ORX input connector.
c. Adjust the A AND B SEC/DIV Variable control for 5 markers per division.
d. ADJUST-Geom (R870) for minimum curvature of the markers across the graticule area.
e. Disconnect the test setup.

## VERTICAL

## Equipment Required (see Table 4-1):

Calibration Generator (Item 1)
Leveled Sine-Wave Generator (Item 2)
$50-\Omega$ BNC Cable (Item 4)
$50-\Omega$ BNC Termination (Item 5)
Dual-Input Coupler (Item 6)
10X Attenuator (Item 7)

Adapter (Item 9)
Digital Voltmeter (Item 11)
1X Probe (Item 12)
Screwdriver (Item 14)
Low-Capacitance Alignment Tool (Item 15)
P6120 Probe (Included with instrument)

## INITIAL CONTROL SETTINGS

## POWER

## CRT <br> AUTO INTENSITY AUTO FOCUS

Vertical (both)
POSITION VERTICAL MODE VOLTS/DIV
VOLTS/DIV Variable INVERT
AC-GND-DC

ON (button in)

As desired
Best focused display

Midrange
CH 1
20 mV
CAL detent
Normal (button out)
DC

## Midrange

A
0.5 ms

CAL detent
Off (knob in)

## Trigger

VAR HOLDOFF
A TRIGGER MODE
A TRIGGER SLOPE
A TRIGGER LEVEL
A \& B INT
A SOURCE

NORM
AUTO
$\Gamma$
Midrange
VERT MODE
INT

PROCEDURE STEPS

1. Adjust Vertical Gain (R186, R286, R145, and R245)
a. Connect a $100-\mathrm{mV}$ standard-amplitude signal via a $50-\Omega$ cable to the CH 1 OR X input connector.
b. ADJUST-Ch 1 Gain (R186) for an exact 5-division display.
c. Move the cable from the CH 1 OR X input connector to the CH 2 OR Y input connector. Change the VERTICAL MODE switch to CH 2.
d. ADJUST-Ch 2 Gain (R286) for an exact 5-division display.
e. Change the generator output to 10 mV and set the CH 1 and CH 2 VOLTS/DIV switches to 2 mV .
f. ADJUST-Ch $2 \times 10$ Vert Gain (R245) for an exact 5-division display.
g. Move the cable from the $\mathrm{CH} 2 \mathrm{OR} Y$ input connector to the CH 1 OR $X$ input connector. Change the VERTICAL MODE switch to CH 1.
h. ADJUST-Ch $1 \times 10$ Vert Gain (R145) for an exact 5 -division display.

## 2. Adjust Attenuator Step Balance (R138 and R238)

a. Set both AC-GND-DC switches to GND.
b. Set the CH 1 VOLTS/DIV switch to 10 mV and position the trace on the center horizontal graticule line using the Channel 1 POSITION control.
c. Change the CH 1 VOLTS/DIV switch to 2 mV .
d. ADJUST-Ch 1 Step Bal (R138) to set the trace on the center horizontal graticule line.
e. Repeat parts $b$ through d until there is no trace shift when changing the $\mathrm{CH} 1 \mathrm{VOLTS} / \mathrm{DIV}$ switch from 10 mV to 2 mV .
f. Change the VERTICAL MODE switch to CH 2.
g. Repeat parts $b$ through e for Channel 2, adjusting Ch 2 Step Bal (R238) in step d.

## 3. Adjust Attenuator X10 Balance (R146 and R246)

a. Set the CH 2 VOLTS/DIV switch to 20 mV .
b. Position the trace on the center horizontal graticule line using the Channel 2 POSITION control.
c. Change the CH 2 VOLTS/DIV switch to 10 mV .
d. ADJUST-Ch $2 \times 10$ Bal (R246) to set the trace on the center horizontal graticule line.
e. Repeat parts a through d until there is no trace shift when changing the CH 2 VOLTS/DIV switch from 20 mV to 10 mV .
f. Change the VERTICAL MODE switch to CH 1.
g. Repeat parts a through e for Channel 1, adjusting Ch $1 \times 10$ Bal (R146) in step $d$.

## 4. Check Deflection Accuracy and Variable Range

a. Set:

| CH 1 VOLTS/DIV | 2 mV |
| :--- | :--- |
| CH 2 VOLTS/DIV | 10 V |
| AC-GND-DC (both) | DC |

b. CHECK-Deflection accuracy is within the limits given in Table 5-3 for each CH 1 VOLTS/DIV switch setting and corresponding standard-amplitude signal. When at the $20-\mathrm{mV}$ VOLTS/DIV switch setting, rotate the CH 1 VOLTS/DIV Variable control fully counterclockwise and CHECK that the display decreases to 2 divisions or less. Then return the VOLTS/DIV Variable control to the CAL detent and continue with the $50-\mathrm{mV}$ check.

Table 5-3
Deflection Accuracy Limits

| VOLTS/DIV <br> Switch <br> Setting | Standard <br> Amplitude <br> Signal | Vertical <br> Deflection <br> (Divisions) | $3 \%$ Accuracy <br> Limits <br> (Divisions) |
| :---: | :---: | :---: | :---: |
| 2 mV | 10 mV | 5 | 4.85 to 5.15 |
| 5 mV | 20 mV | 4 | 3.88 to 4.12 |
| 10 mV | 50 mV | 5 | 4.85 to 5.15 |
| 20 mV | 0.1 V | 5 | 4.85 to 5.15 |
| 50 mV | 0.2 V | 4 | 3.88 to 4.12 |
| 0.1 V | 0.5 V | 5 | 4.85 to 5.15 |
| 0.2 V | 1 V | 5 | 4.85 to 5.15 |
| 0.5 V | 2 V | 4 | 3.88 to 4.12 |
| 1 V | 5 V | 5 | 4.85 to 5.15 |
| 2 V | 10 V | 5 | 4.85 to 5.15 |
| 5 V | 20 V | 4 | 3.88 to 4.12 |
| 10 V | 50 V | 5 | 4.85 to 5.15 |

c. Set the VERTICAL MODE switch to CH 2 and move the cable from the CH 1 OR $X$ input connector to the $\mathrm{CH} 2 \mathrm{OR} Y$ input connector.
d. CHECK-Deflection accuracy is within the limits given in Table $5-3$ for each CH 2 VOLTS/DIV switch setting and corresponding standard-amplitude signal. Perform the checks from the bottom to the top of Table 5-3 to avoid unnecessary switch-position changes. When at the $20-\mathrm{mV}$ VOLTS/DIV switch setting, rotate the CH 2 VOLTS/DIV Variable control fully counterclockwise and CHECK that the display decreases to 2 divisions or less.

Then return the VOLTS/DIV Variable control to the CAL detent and finish the check.

## 5. Check Input Coupling

a. Set both VOLTS/DIV switches to $50-\mathrm{mV}$.
b. Set the calibration generator to produce a $200-\mathrm{mV}$ standard-amplitude signal.
c. Position the bottom of the signal on the center horizontal graticule line using the Channel 2 POSITION control.
d. Set the Channel 2 input coupling switch to AC.
e. CHECK-Display is centered about the center horizontal graticule line.
f. Set the VERTICAL MODE switch to CH 1 and move the input signal from the CH 2 OR Y input connector to the CH 1 OR X input connector.
g. Repeat parts c through e for Channel 1.

## 6. Check ALT and CHOP Operation

a. Set:

| VERTICAL MODE | BOTH-ALT |
| :--- | :--- |
| AC-GND-DC (both) | GND |
| A SEC/DIV | 10 ms |

b. CHECK-Display alternates between the CH 1 and CH 2 displays. If necessary, use both POSITION controls to separate the two traces.
c. Set VERTICAL MODE to CHOP.
d. CHECK-CH 1 and CH 2 displays are both displayed simultaneously.

## 7. Check VOLTS/DIV Variable Control Trace Shift

a. Set:

| VERTICAL MODE | CH 1 |
| :--- | :--- |
| VOLTS/DIV (both) | 2 mV |
| AC-GND-DC (both) | DC |
| A SEC/DIV | 0.2 ms |

b. Center the trace on the center horizontal graticule line using the Channel 1 POSITION control.
c. Rotate the CH 1 VOLTS/DIV Variable control counterclockwise through its full range.
d. CHECK-That the trace does not shift more than 2.5 divisions.
e. Return the CH 1 VOLTS/DIV Variable control to its CAL detent.
f. Set the VERTICAL MODE switch to CH 2.
g. Repeat parts $b$ through e for CH 2.

## 8. Adjust Invert Balance (R264)

a. Set the CH 2 VOLTS/DIV switch to 20 mV .
b. Center the trace on the center horizontal graticule line using the Channel 2 POSITION control.
c. Push in the INVERT button.
d. ADJUST-Invert Bal (R264) to position the trace on the center horizontal graticule line.
e. Return the INVERT button to Normal (button out).
f. Repeat parts $c$ through e until there is no trace shift when switching the INVERT button between Invert and Normal.

## 9. Adjust Trigger Balance (R154)

a. Set the A \& B INT switch to CH 2.
b. Connect the digital voltmeter low lead to chassis ground (TP501) and the volts lead to pin 16 of U421; note the voltage reading for use in part $d$.
c. Set the A \& B INT switch to CH 1.
d. ADJUST-Ch 1/Ch 2 Balance (R154) so that the voltage reading is the same as that obtained in part $b$.
e. Disconnect the test setup.
10. Adjust Attenuator Compensation (C105, C104, C111, C110, C205, C204, C211, and C210)
a. Set:
CH 1 VOLTS/DIV
AC-GND-DC (both) A SEC/DIV

```
20 mV
DC
0.2 ms
```

b. Connect a $1-\mathrm{kHz}$, high-amplitude square wave via a $50-\Omega$ termination, a probe-tip-to-bnc adapter, and a P6120 Probe to the CH 1 OR X input connector.
c. Set the generator output to produce a 5-division display and compensate the probe using the probe compensation adjustment (see the probe instruction manual).
d. Set the CH 1 VOLTS/DIV switch to 0.2 V .
e. Replace the probe and probe-tip-to-bnc adapter with a $50-\Omega$ cable.
f. Adjust the generator output for a 5-division display.

## NOTE

Use Table 5-4 to identify the correct capacitor for each channel adjustment.
g. AD.JUST-The $\div 10$ LF Comp capacitor for best front corner.

Table 5-4
Attenuator Compensation Adjustments

| Adjustment | Channel 1 | Channel 2 |
| :---: | :---: | :---: |
| $\div 10$ LF Comp | C105 | C205 |
| $\div 10$ Input C | C104 | C204 |
| $\div 100$ LF Comp | C111 | C 211 |
| $\div 100$ Input C | C 110 | C 210 |

h. Replace the cable and $50-\Omega$ termination with the P6120 Probe and probe-tip-to-bnc adapter.
i. Adjust the generator output for a 5 -division display.
j. ADJUST-The $\div 10$ Input $C$ capacitor for best flat top.
k. Repeat parts e through $\mathbf{j}$ until no further improvement is noted. Add the $50-\Omega$ termination to the cable in part e.
I. Set the CH 1 VOLTS/DIV switch to 2 V .
m . Replace the probe and probe-tip-to-bnc adapter with the $50-\Omega$ cable.
n. Adjust the generator output for a 5 -division display.
o. ADJUST-The $\div 100$ LF Comp capacitor for best front corner.
p. Replace the $50-\Omega$ cable with the probe and probe-tip-to-bnc adapter.
q. Adjust the generator output to produce a display as close as possible to 5 divisions.
r. ADJUST-The $\div 100$ Input C capacitor for best flat top.
s. Repeat parts $m$ through $r$ until no further improvement is noted.
t. Set the VERTICAL MODE switch to CH 2.
u. Repeat parts b through s for CH 2 .
v. Disconnect the test setup.

## 11. Adjust Vertical Output Amplifier Compensation (R357, C357, R367, R366, and C366)

a. Set:
$\begin{array}{ll}\text { VOLTS/DIV (both) } & 20 \mathrm{mV} \\ \text { A SEC/DIV } & 0.05 \mu \mathrm{~s}\end{array}$
b. Connect a $1-\mathrm{MHz}$, positive-going fast-rise square-wave via a $50-\Omega$ cable, a $10 \times$ attenuator, and a $50-\Omega$ termination to the $\mathrm{CH} 2 \mathrm{OR} Y$ input connector.
c. Adjust the generator output for a 5 -division display.
d. Preset High Freq Comp (R357) fully counterclockwise.
e. ADJUST-High Freq Comp (C357) until ringing just disappears on the front corner.
f. ADJUST-Low Freq Comp (R367) and Mid Freq Comp (R366 and C366) for best flat top beyond 20 ns from the corner.
g. ADJUST-R357 and C357 for best corner on the first 20 ns of the displayed signal.
h. Repeat parts $f$ and $g$ until no further improvement is noted.
i. Set the CH 2 VOLTS/DIV switch to 0.1 V and repeat parts $f$ and $g$ for best compromise with the $20-\mathrm{mV}$ VOLTS/ DIV switch setting.
j. Disconnect the test setup.

## 12. Adjust Channel Matching (C167) and Check Bandwidth

a. Set:

| VOLTS/DIV (both) | 20 mV |
| :--- | :--- |
| A SEC/DIV | $20 \mu \mathrm{~s}$ |

b. Connect the leveled sine-wave generator output via a $50-\Omega$ cable and a $50-\Omega$ termination to the CH 2 OR $\gamma$ input connector.
c. Set the generator output for a 5 -division, $50-\mathrm{kHz}$ display.
d. Increase the generator frequency until the display reduces to 3.5 divisions.
e. Move the signal from the CH 2 OR Y input connector to the CH 1 OR X input connector. Set the VERTICAL MODE switch to CH 1.
f. ADJUST-CH 1 \& CH 2 HF Match (C167) for a vertical display amplitude of 3.5 divisions.
g. Set both VOLTS/DIV switches to 2 mV .
h. Connect the leveled sine-wave generator output via a $50-\Omega$ cable and a $50-\Omega$ termination to the CH 1 OR X input connector.
i. Set the generator output amplitude for a 5-division, $50-\mathrm{kHz}$ display.
j. Change the generator output frequency to the value shown in Table 5-5 for the corresponding VOLTS/DIV switch setting.

Table 5-5
Settings for Bandwidth Checks

| VOLTS/DIV <br> Switch Settings | Generator <br> Output Frequency |
| :---: | :---: |
| 2 mV to 10 mV | 50 MHz |
| 20 mV to 10 V | 60 MHz |

k. CHECK-Display amplitude is 3.5 divisions or greater.
I. Repeat parts i through k for all indicated CH 1 VOLTS/DIV switch settings, up to the output-voltage upper limit of the sine-wave generator being used.
m . Move the generator output signal from the CH 1 OR $X$ input connector to the CH 2 OR Y input connector. Set the VERTICAL MODE switch to CH 2.
n. Repeat parts $\mathbf{i}$ through $k$ for all indicated CH 2 VOLTS/DIV switch settings up to the output-voltage upper limit of the sine-wave generator being used.
o. Disconnect the test setup.

## 13. Check Common-Mode Rejection Ratio

a. Set both VOLTS/DIV switches to 20 mV .
b. Connect a $10-\mathrm{MHz}$, leveled sine-wave signal via a $50-\Omega$ cable, a $50-\Omega$ termination, and a dual-input coupler to the CH 1 OR X and the CH 2 OR Y input connectors.
c. Set the generator output amplitude to produce a 6 -division display.
d. Vertically center the display using the Channel 2 POSITION control. Then set VERTICAL MODE to CH 1 and vertically center the display using the Channel 1 POSITION control.
e. Set the VERTICAL MODE switches to BOTH and ADD; then push in the INVERT button.
f. CHECK-Display amplitude is 0.6 division or less.
g. If the check in part f meets the requirement, skip to part n . If it does not, continue with part h .
h. Set VERTICAL MODE to CH 1.
i. Change the generator frequency to 50 kHz and adjust the output to obtain a 6 -division display.
j. Set VERTICAL MODE to BOTH.
k. Adjust the CH 2 VOLTS/DIV Variable control for minimum display amplitude (best CMRR).
I. Change the generator frequency to 10 MHz .
m. CHECK-Display amplitude is 0.6 division or less.

## 14. Check POSITION Control Range

a. Set:

| VERTICAL MODE | CH 1 |
| :--- | :--- |
| VOLTS/DIV (both) | 50 mV |
| AC-GND-DC (both) | AC |

b. Connect a $0.5-\mathrm{V}$ standard-amplitude signal via a $50-\Omega$ cable to the CH 1 OR X input connector.
c. Adjust the CH 1 VOLTS/DIV Variable control for a 4.4-division display. Then set the CH 1 VOLTS/DIV switch to 10 mV .
d. CHECK-Rotating the Channel 1 POSITION control fully counterclockwise positions the top of the trace below the center horizontal graticule line.
e. CHECK-Rotating the Channel 1 POSITION control fully clockwise positions the bottom of the trace above the center horizontal graticule line.
f. Move the signal from the CH 1 OR X input connector to the CH 1 OR $X$ input connector to the CH 2 OR $Y$ input connector and set the VERTICAL MODE switch to CH 2.
g. Repeat parts c through e for Channel 2.
h. Disconnect the test setup.

## 15. Check Channel Isolation

a. Set:

| CH 1 VOLTS/DIV | 0.5 V |
| :--- | :--- |
| CH 2 VOLTS/DIV | 10 mV |
| VERTICAL MODE | CH 1 |

b. Connect a $10-\mathrm{MHz}$ leveled sine-wave signal via a $50-\Omega$ cable and a $50-\Omega$ termination to the CH 1 ORX input connector.
c. Adjust the generator output for an 8-division input connector.
d. Set the VERTICAL MODE switch to CH 2.
e. CHECK-Display amplitude is 4 divisions or less.
f. Move the input signal from the CH 1 ORX input connector to the CH 2 OR $Y$ input connector
g. Set:

| CH 1 VOLTS/DIV | 10 mV |
| :--- | :--- |
| CH 2 VOLTS/DIV | 0.5 V |
| VERTICAL MODE | CH 1 |

h. CHECK - Display amplitude is 4 divisions or less.
i. Disconnect the test setup.

## HORIZONTAL

## Equipment Required (see Table 4-1):

Calibration Generator (Item 1)
Leveled Sine-Wave Generator (Item 2)
Time-Mark Generator (Item 3)
Two 50- $\Omega$ BNC Cables (Item 4)

Two 50- $\Omega$ BNC Terminations (Item 5)
Screwdriver (Item 14)
Low-Capacitance Alignment Tool (Item 15)

## INITIAL CONTROL SETTINGS

POWER

CRT

## AUTO INTENSITY AUTO FOCUS

Vertical
Channel 1 POSITION
VERTICAL MODE
CH 1 VOLTS/DIV
CH 1 VOLTS/DIV
Variable
INVERT
Channel 1 AC-GND-DC
Channel 2 AC-GND-DC

ON (button in)

As desired Best focused display

## PROCEDURE STEPS

## 1. Adjust Horizontal Amplifier Gain (R752, R682, and R733)

a. Connect 0.1 -ms time markers from the time-mark generator via a $50-\Omega$ cable and a $50-\Omega$ termination to the CH 1 OR $X$ input connector. Connect the generator Trigger output via a $50-\Omega$ cable and a $50-\Omega$ termination to the EXT INPUT connector.
b. ADJUST-Horiz Gain (R752) for 1 time marker per division.
c. Set the HORIZONTAL MODE switch to B.
d. ADJUST-B Gain (R682) for 1 time marker per division.

Horizontal
POSITION

## Midrange

A
0.1 ms

A AND B SEC/DIV
A AND B SEC/DIV
Variable
X10 Magnifier
B DELAY TIME
POSITION

Trigger
VAR HOLDOFF NORM
A TRIGGER MODE AUTO
SLOPE (both)
LEVEL (both)
A \& B INT
A SOURCE
A EXT COUPLING

Midrange
CH 1
0.5 V

CAL detent
Normal (button out)
DC
GND
1.00

」
Midrange
VERT MODE
EXT
CAL detent
Off (knob in)
$D C \div 10$
e. Set the HORIZONTAL MODE switch to A.
f. Set the X 10 Magnifier on (knob out) and select $10-\mu \mathrm{s}$ time markers from the time-mark generator.
g. ADJUST-X10 Gain (R733) for 1 time marker per division.

## 2. Adjust Magnifier Registration (R758)

a. Select $0.5-\mathrm{ms}$ time markers from the time-mark generator and set the X10 Magnifier off (knob in).
b. Position the middle time marker to the center vertical graticule line using the Horizontal POSITION control.
c. Set the X 10 Magnifier on (knob out).
d. ADJUST-Mag Registration (R758) to position the middle time marker on the center vertical graticule line.
e. Set the $\times 10$ Magnifier off (knob in).
f. CHECK-There is no discernable shift in the time marker when switching between X10 Magnifier on and X10 Magnifier off.
g. Turn the $\times 10$ Magnifier on (knob out) and repeat parts $b$ through e until no further improvement is noted.

## 3. Adjust Delay Dial Timing (R659 and R654)

a. Set:

| HORIZONTAL MODE | ALT |
| :--- | :--- |
| A SEC/DIV | 0.1 ms |
| B SEC/DIV | $1 \mu \mathrm{~s}$ |
| X10 Magnifier | Off (knob in) |

b. Select $0.1-\mathrm{ms}$ time markers from the time-mark generator and verify that the B DELAY TIME POSITION control is set to 1.00 .
c. ADJUST-Delay Dial Start Adj (R659) so that the 2nd A-sweep time marker is intensified and the B-sweep time marker starts at the beginning of the $B$ sweep.
d. Set the B DELAY TIME POSITION control to 9.00 .
e. ADJUST-Delay Dial Gain (R654) so that the 10 th A-sweep time marker is intensified and the B-sweep time marker starts at the beginning of the $B$ sweep.
f. Set the B DELAY TIME POSITION control to 1.00 and repeat parts c through e until no further improvement is noted.

## 4. Adjust $5-\mu \mathrm{s}$ Timing (C676 and C626)

a. Set:

| HORIZONTAL MODE | B |
| :--- | :--- |
| A AND B SEC/DIV | $5 \mu \mathrm{~s}$ |

b. Select $5-\mu_{\mathrm{s}}$ time markers from the time-mark generator,
c. ADJUST $-5 \mu$ s Timing (C676) for 1 time marker per division across the graticule area.
d. Set the HORIZONTAL MODE switch to A.
e. ADJUST-5 $\mu$ s Timing (C626) for 1 time marker per division across the graticule area.

## 5. Adjust High-Speed Timing (C754, C774, C784, and C734)

a. Set the A SEC/DIV switch to $0.05 \mu \mathrm{~s}$.
b. Select 50 -ns time markers from the time-mark generator.
c. ADJUST-50 ns Linearity (C754) for equally spaced time markers at the start of the sweep.
d. Set the $\times 10$ Magnifier on (knob out) and select $10-\mathrm{ns}$ time markers from the time-mark generator.

## NOTE

In the next part, keep the adjustment screws for C774 and C784 as close to the same length as possible.
e. ADJUST-5 ns Timing (C774 and C784) alternately for one time marker every 2 divisions over the center 8 divisions of the magnified sweep.
f. Adjust the Horizontal POSITION control so that the 5th time marker is aligned with the 2nd vertical graticule line.
g. ADJUST-5 ns Linearity (C734) for one time marker every 2 divisions over the center 8 divisions of the magnified sweep. Adjust the Horizontal POSITION control to check the linearity to the 15th time marker.
h. Repeat parts e through g until no further improvement is noted.
i. Set the $\times 10$ Magnifier off (knob in) and recenter the trace using the Horizontal POSITION control.
j. Repeat parts $b$ through $i$ until no further improvement is noted.

## 6. Check Timing Accuracy

a. Select 50 -ns time markers from the time-mark generator.
b. Use the Channel 1 POSITION control to center the trace vertically. Adjust the A TRIGGER LEVEL control for a stable, triggered display.
c. Use the Horizontal POSITION control to align the first time marker that is 50 ns beyond the start of the sweep with the 2 nd vertical graticule line.

## NOTE

When making timing measurements, use as a reference the same point on each time marker.
d. CHECK-Timing accuracy is within the limits shown in Table 5-6 for the applicable position of the X10 Magnifier. When making the check with the $\times 10$ Magnifier On, exclude any portion of the sweep past the 100th magnified division.
e. Set the HORIZONTAL MODE switch to $B$ and adjust the B TRIGGER LEVEL control for a stable display.
f. Align the first time marker that is 50 ns beyond the start of the sweep with the 2nd vertical graticule line, using the Horizontal POSITION control.
g. CHECK-Timing accuracy is within the limits shown in Table 5-6 for the applicable position of the $\times 10 \mathrm{Mag}$ nifier. When making the check with the $\times 10$ Magnifier On, exclude any portion of the sweep past the 100th magnified division.

## h. Set the HORIZONTAL MODE switch to A.

i. Repeat parts $b$ through $h$ for the $A$ and B SEC/DIV and time-mark generator setting combinations shown in Table 5-7 under the "Normal" column.
j. Set:
$\begin{array}{ll}\text { A and B SEC/DIV } & 0.05 \mu \mathrm{~s} \\ \times 10 \text { Magnifier } & \text { On (knob out) }\end{array}$
k. Select 10 -ns time markers from the time-mark generator.

1. Repeat parts $b$ through $h$ for the $A$ and $B$ SEC/DIV and time-mark generator setting combinations shown in Table 5-7 under the "X10 Magnified" column.

Table 5-6
A and B Timing Accuracy

| X10 Magnifier | Accuracy at 10th Vertical Graticule Line |
| :--- | :---: |
| Off (knob in) | $3 \%$ (0.24 division) |
| On (knob out) | $5 \%(0.40$ division) |

Table 5-7
Settings for Timing Accuracy Checks

| A AND B SEC/DIV Switch Setting | Time-Mark Generator Output |  |
| :---: | :---: | :---: |
|  | Normal | X10 Magnified |
| $0.05 \mu \mathrm{~s}$ | 50 ns | 10 ns |
| $0.1 \mu \mathrm{~s}$ | $0.1 \mu \mathrm{~s}$ | 10 ns |
| $0.2 \mu \mathrm{~s}$ | $0.2 \mu \mathrm{~s}$ | 20 ns |
| $0.5 \mu \mathrm{~s}$ | $0.5 \mu \mathrm{~s}$ | 50 ns |
| $1 \mu \mathrm{~s}$ | $1 \mu \mathrm{~s}$ | $0.1 \mu \mathrm{~s}$ |
| $2 \mu \mathrm{~s}$ | $2 \mu \mathrm{~s}$ | $0.2 \mu \mathrm{~s}$ |
| $5 \mu \mathrm{~s}$ | $5 \mu \mathrm{~s}$ | $0.5 \mu \mathrm{~s}$ |
| $10 \mu \mathrm{~s}$ | $10 \mu \mathrm{~s}$ | $1 \mu \mathrm{~s}$ |
| $20 \mu \mathrm{~s}$ | $20 \mu \mathrm{~s}$ | $2 \mu \mathrm{~s}$ |
| $50 \mu \mathrm{~s}$ | $50 \mu \mathrm{~s}$ | $5 \mu \mathrm{~s}$ |
| 0.1 ms | 0.1 ms | $10 \mu \mathrm{~s}$ |
| 0.2 ms | 0.2 ms | $20 \mu \mathrm{~s}$ |
| 0.5 ms | 0.5 ms | $50 \mu \mathrm{~s}$ |
| 1 ms | 1 ms | 0.1 ms |
| 2 ms | 2 ms | 0.2 ms |
| 5 ms | 5 ms | 0.5 ms |
| 10 ms | 10 ms | 1 ms |
| 20 ms | 20 ms | 2 ms |
| 50 ms | 50 ms | 5 ms |
| A Sweep Only |  |  |
| 0.1 s | 0.1 s | 10 ms |
| 0.2 s | 0.2 s | 20 ms |
| 0.5 s | 0.5 s | 50 ms |

## 7. Check B DELAY TIME POSITION Control Range

a. Set:

| Channel 1 AC-GND-DC | GND |
| :--- | :--- |
| HORIZONTAL MODE | ALT |
| A AND B SEC/DIV | 0.2 ms |

b. Align the start of the A sweep with the 1st vertical graticule line.
c. CHECK-Intensified portion of the trace starts within 0.5 division of the start of the sweep.
d. Rotate the B DELAY TIME POSITION control fully clockwise.
e. CHECK-Intensified zone is past the 11th vertical graticule line.

## 8. Check SEC/DIV Variable Range

a. Set:

| CH 1 VOLTS/DIV | 0.5 V |
| :--- | :--- |
| Channel 1 AC-GND-DC | DC |
| HORIZONTAL MODE | A |
| A SEC/DIV | 0.2 ms |
| SEC/DIV Variable | Fully counterclockwise |
| X10 Magnifier | Off (knob in) |

b. Select $0.5-\mathrm{ms}$ time markers from the time-mark generator.
c. CHECK-Time markers are 1 division or less apart.
d. Return the SEC/DIV Variable control to the CAL detent.
9. Check B DELAY TIME POSITION Dial
Accuracy
a. Set:

HORIZONTAL MODE B
A SEC/DIV $0.2 \mu \mathrm{~S}$
B SEC/DIV $\quad 0.05 \mu \mathrm{~s}$
B TRIGGER LEVEL CW-RUN AFTER DLY
b. Select $0.2-\mu \mathrm{s}$ time markers.
c. Set the B DELAY TIME POSITION control to 1,00 . Adjust the Horizontal POSITION control so that the top of the first fully displayed time marker is aligned with the center vertical graticule line.
d. Without changing the Horizontal POSITION control setting, set the B DELAY TIME POSITION dial setting to 9.00. Slightly readjust the B DELAY TIME POSITION control to align the top of the time marker with the center vertical graticule line.
e. CHECK-The B DELAY TIME POSITION dial setting is between 8.87 and 9.14 .
f. Set:
A SEC/DIV
0.5 ms
B SEC/DIV
$50 \mu \mathrm{~s}$
g. Select $0.5-\mu_{\mathrm{S}}$ time markers.
h. Repeat parts cthrough e.

## 10. Check Delay Jitter

a. Set the B SEC/DIV switch to $0.5 \mu \mathrm{~s}$.
b. Select $10-\mu$ s time markers.
c. Slightly readjust the B DELAY TIME POSITION dial to position a time marker with in the graticule area.
d. CHECK-Jitter on the leading edge of the time marker does not exceed 1 division. Disregard slow drift.

## 11. Check POSITION Control Range

a. Set:
$\begin{array}{ll}\text { A SEC/DIV } & 10 \mu \mathrm{~s} \\ \text { HORIZONTALMODE }\end{array}$
HORIZONTAL MODE A
b. Select $50-\mu$ s time markers.
c. Align the 3rd time marker with the center vertical graticule line.
d. Set the X10 Magnifier knob to On (knob out).
e. CHECK-Magnified time marker can be positioned to the left of the center vertical graticule line by rotating the Horizontal POSITION control fully counterclockwise.
f. CHECK-Start of the sweep can be positioned to the right of the center vertical graticule line by rotating the Horizontal POSITION control fully clockwise.
g. Disconnect the test setup.
12. Adjust X-Gain (R709)
a. Set:

```
CH }1\mathrm{ VOLTS/DIV
A SEC/DIV
20 mV
X-Y
```

b. Connect a $0.1-\mathrm{V}$ standard-amplitude signal to the CH 1 OR X input connector using a $50-\Omega$ cable .
c. ADJUST-X Gain (R709) for exactly 5 divisions of horizontal deflection.
d. Disconnect the test setup.

## 13. Check $X$-Bandwidth

a. Connect a $50-\mathrm{kHz}$ leveled sine-wave signal via a $50-\Omega$ cable and a $50-\Omega$ termination to the CH 1 OR $X$ input connector.
b. Set the generator to obtain a 5 -division horizontal display.
c. Adjust the generator output frequency to 2 MHz .
d. CHECK-Display is at least 3.5 divisions in length.
e. Disconnect the test setup.

## TRIGGERING

| Equipment Required (see Table 4-1): |  |
| :--- | :--- |
| Leveled Sine-Wave Generator (Item 2) | BNC T-Connector (Item 8) |
| $50-\Omega$ BNC Cable (Item 4) | Probe-tip-to-BNC Adapter (Item 9) |
| $50-\Omega$ BNC Termination (Item 5) | Screwdriver (Item 14) |
| 10X Attenuator (Item 7) | P6120 Probe (provided with instrument) |
| See ADJUSTMENT Locations 1\% | at the back of this manual for test point and adjustment locations. |

## INITIAL CONTROL SETTINGS

## PROCEDURE STEPS

## POWER

## CRT

AUTO INTENSITY AUTO FOCUS

ON (button in)

As desired
Best focused display

Midrange
CH 1
20 mV
CAL detent
Normal (button out) DC

Midrange
A
$20 \mu \mathrm{~s}$
CAL detent
Off (knob in)
c. ADJUST-Slope Bal (R482) for a positive vertical shift of 0.15 division at the sweep start when changing the A TRIGGER SLOPE switch from $\backslash$ to $\Gamma$.

## 2. Check/Adjust Auto Trigger Centering (R511 and R512) and TRIG‘D LED Operation

a. Set:

A TRIGGER LEVEL Fully clockwise
A TRIGGER SLOPE」
b. Adjust the generator output for a 1-division display.
c. ADJUST-(+) Auto (R511) so that the display just triggers on the positive peak of the signal.
d. Set:

A TRIGGER LEVEL $\quad$ Fully counterclockwise
A TRIGGER SLOPE
e. ADJUST-(-) Auto (R512) so that the display just triggers on the negative peak of the signal.
f. Set A TRIGGER MODE to NORM.
g. CHECK-TRIG'D LED is illuminated when a stable display is present and is off when the display is not triggered.

## 3. Check Internal Triggering

a. Set the CH 1 VOLTS/DIV switch to 2 mV .
b. Set the generator output to produce a 4-division, $2-\mathrm{MHz}$ display.
c. Set the CH 1 VOLTS/DIV switch to 20 mV .
d. CHECK-Stable display can be obtained by adjusting the A TRIGGER LEVEL control for each switch combination given in Table 5-8.

Table 5-8
Switch Combinations for A Triggering Checks

| TRIGGER MODE | TRIGGER SLOPE |
| :---: | :---: |
| NORM | $\nearrow$ |
| NORM | $\swarrow$ |
| AUTO | $\nearrow$ |
| AUTO | $\digamma$ |

e. Set the HORIZONTAL MODE switch to B.
f. CHECK-Stable display can be obtained by adjusting the B TRIGGER LEVEL control for both positive- and negative-going positions of the B TRIGGER SLOPE switch.
g. Set:

VERTICAL MODE
CH 2
HORIZONTAL MODE
A
h. Move the generator output from the CH 1 OR $X$ input connector to the CH 2 OR Y input connector. Set VERTICAL MODE to CH 2.
i. Repeat parts $d$ through $f$.
j. Set:

| HORIZONTAL MODE | A |
| :--- | :--- |
| A SEC/DIV | $0.05 \mu \mathrm{~s}$ |

k. Set the generator to produce a $1.5-$ division, $60-\mathrm{MHz}$ display.
I. Repeat part d.
m. Move the generator output from the CH 2 OR Y input connector to the CH 1 OR X input connector. Set VERTICAL MODE to CH 1.
n. Repeat part d.
o. Adjust the generator output and the A TRIGGER LEVEL control for a stable, 2-division display.
p. Repeat parts e and f.
q. Move the generator output from the CH 1 OR $X$ input connector to the CH 2 OR Y input connector. Set VERTICAL MODE to CH 2.
r. Repeat part f.
s. Disconnect the test setup.

## 4. Check External Triggering

a. Set:

| VOLTS/DIV (both) | 10 mV |
| :--- | :--- |
| A SEC/DIV | $10 \mu \mathrm{~S}$ |
| VERTICAL MODE | CH 1 |

b. Connect the test setup as shown in Figure 4-1.
c. Set the leveled sine-wave generator to produce a 5 -division, $50-\mathrm{kHz}$ display.
d. Set:

| VERTICAL MODE | CH 2 |
| :--- | :--- |
| A SEC/DIV | $0.2 \mu \mathrm{~S}$ |
| A SOURCE | EXT |

e. Move the signal from the CH 1 OR X input connector to the EXT INPUT connector.
f. Set the generator to 2 MHz .
g. CHECK-Stable display can be obtained by adjusting the A TRIGGER LEVEL control for each switch combination given in Table 5-8.
h. Remove the 10X attenuator from the test setup and set the A EXT COUPLING switch to DC $\div 10$.
k. Reconnect the test setup as shown in Figure 4-1.
I. Set the leveled sine-wave generator to produce a 5 -division, $50-\mathrm{kHz}$ display.
m. Set:
i. Repeat part g.
j. Set:

VOLTS/DIV (both)
VERTICAL MODE A SEC/DIV
A SOURCE
50 mV
CH 1
$20 \mu \mathrm{~S}$
INT

| VERTICAL MODE | CH 2 |
| :--- | :--- |
| A SEC/DIV | $0.05 \mu \mathrm{~s}$ |
| X10 MAGNIFIER | On (knob out) |
| A SOURCE | EXT |

n. Repeat part e.
o. Set the generator to 60 MHz . $0.05 \mu \mathrm{~s}$ On (knob out) EXT
p. Repeat parts g and h .
q. Repeat part g.
r. Disconnect the test setup.

## EXTERNAL Z-AXIS AND PROBE ADJUST

## Equipment Required (see Table 4-1):

Leveled Sine-Wave Generator (Item 2)
Two $50-\Omega$ BNC Cables (Item 4)

## INITIAL CONTROL SETTINGS

POWER

CRT

## AUTO INTENSITY AUTO FOCUS

ON

As desired Best defined display

BNC T-Connector (Item 8)
P6120 Probe (provided with instrument)

## 1. Check EXT Z-AXIS Operation

a. Connect the leveled sine-wave generator output via a T-connector and two $50-\Omega$ cables to the EXT Z-AXIS INPUT connector on the rear panel and to the CH 1 OR X input connector.
b. Adjust the generator controls to produce a 5 -volt, 50 kHz display.
c. CHECK-For noticeable intensity modulation. The positive part of the sine wave should be of lower intensity than the negative part.
d. Disconnect the test setup.

## 2. Check PROBE ADJUST Operation

a. Set:

CH 1 VOLTS/DIV 10 mV
A SEC/DIV $\quad 0.5 \mathrm{~ms}$
b. Connect the P6120 Probe to the CH 1 OR X input connector and insert the probe tip into the PROBE ADJUST jack on the instrument front panel. If necessary, adjust the probe compensation for a flat-topped squarewave display.
c. CHECK-Display is 5 divisions $\pm 1$ division ( 4 to 6 divisions).
d. Disconnect the test setup.

## MAINTENANCE

This section of the manual contains information for conducting preventive maintenance, troubleshooting, and corrective maintenance on the 2215 Oscilloscope.

## STATIC-SENSITIVE COMPONENTS

The following precautions are applicable when performing any maintenance involving internal access to the instrument.


Static discharge can damage any semiconductor component in this instrument.

This instrument contains electrical components that are susceptible to damage from static discharge. Table 6-1 lists the relative susceptibility of various classes of semiconductors. Static voltages of 1 kV to 30 kV are common in unprotected environments.

When performing maintenance observe the following precautions to avoid component damage:

1. Minimize handling of static-sensitive components.
2. Transport and store static-sensitive components or assemblies in their original containers or on a metal rail. Label any package that contains static-sensitive components or assemblies.
3. Discharge the static voltage from your body by wearing a grounded antistatic wrist strap while handling these components. Servicing static-sensitive components or assemblies should be performed only at a static-free work station by qualified service personnel.
4. Nothing capable of generating or holding a static charge should be allowed on the work station surface.
5. Keep the component leads shorted together whenever possible.
6. Pick up components by their bodies, never by their leads.

Table 6-1
Relative Susceptibility to Static-Discharge Damage

| Semiconductor Classes | $\begin{array}{c}\text { Relative } \\ \text { Susceptibility } \\ \text { Levels }\end{array}$ |
| :--- | :---: |
| $\begin{array}{l}\text { MOS or CMOS microcircuits or } \\ \text { discretes, or linear microcircuits with } \\ \text { MOS inputs }\end{array}$ | Most Sensitive) |$]-1$

[^5]7. Do not slide the components over any surface.
8. Avoid handling components in areas that have a floor or work-surface covering capable of generating a static charge.
9. Use a soldering iron that is connected to earth ground.
10. Use only approved antistatic, vacuum-type desoldering tools for component removal.

## PREVENTIVE MAINTENANCE

## INTRODUCTION

Preventive maintenance consists of cleaning, visual inspection, lubrication, and checking instrument performance. When accomplished regularly, it may prevent instrument malfunction and enhance instrument reliability. The severity of the environment in which the instrument is used determines the required frequency of maintenance. An appropriate time to accomplish preventive maintenance is just before instrument adjustment.

## GENERAL CARE

The cabinet minimizes accumulation of dust inside the instrument and should normally be in place when operating the 2215 . The optional front-panel cover provides both dust and damage protection for the front panel and crt face, and it should be in place whenever the instrument is stored or is being transported.

## INSPECTION AND CLEANING

The instrument should be visually inspected and cleaned as often as operating conditions require. Accumulation of dirt in the instrument can cause overheating and component breakdown. Dirt on components acts as an insulating blanket, preventing efficient heat dissipation. It also provides an electrical conduction path that could result in instrument failure, especially under high-humidity conditions.

Avoid the use of chemical cleaning agents which might damage the plastics used in this instrument. Use a nonresidue-type cleaner, preferably isopropyl alcohol, denatured ethyl alcohol, or a solution of $1 \%$ mild detergent with $99 \%$ water. Before using any other type of cleaner, consult your Tektronix Service Center or representative.

## Exterior

INSPECTION. Inspect the external portions of the instrument for damage, wear, and missing parts; use Table 6-2 as a guide. Instruments that appear to have been dropped or otherwise abused should be checked thoroughly to verify correct operatiorı and performance. Deficiencies found that could cause personal injury or could lead to further damage to the instrument should be repaired immediately.


To prevent getting moisture inside the instrument during external cleaning, use only enough liquid to dampen the cloth or applicator.

CLEANING. Loose dust on the outside of the instrument can be removed with a soft cloth or small soft-bristle brush. The brush is particularly useful for dislodging dirt on and around the controis and connectors. Dirt that remains can be removed with a soft cloth dampened in a mild detergent-and-water solution. Do not use abrasive cleaners. Clean the light filter and the crt face with a soft lint-free cloth dampened with either denatured alcohol or a mild detergent-and-water solution.

## Interior

To gain access to internal portions of the instrument for inspection and cleaning, refer to the "Removal and Replacement Instructions" in the "Corrective Maintenance" part of this section.

INSPECTION. Inspect the internal portions of the instrument for damage and wear, using Table 6-3 as a guide. Deficiencies found should be repaired immediately. The corrective procedure for most visible defects is obvious; however, particular care must be taken if heat-damaged components are found. Overheating usually indicates other trouble in the instrument; therefore, it is important that the cause of overheating be corrected to prevent recurrence of the damage.

Table 6-2
External Inspection Checklist

| Item | Inspect For | Repair Action |
| :--- | :--- | :--- |
| Cabinet and Front <br> Panel | Cracks, scratches, deformations, and damaged <br> hardware or gaskets. | Touch up paint scratches and replace defective <br> parts. |
| Front-panel Controls | Missing, damaged, or loose knobs, buttons, and <br> controls. | Repair or replace missing or defective items. |
| Connectors | Broken shells, cracked insulation, and deformed <br> contacts. Dirt in connectors. | Replace defective parts. Clean or wash out dirt. |
| Carrying Handle | Correct operation. | Replace defective parts. |
| Accessories | Missing items or parts of items, bent pins, <br> broken or frayed cables, and damaged <br> connectors. | Replace damaged or missing items, frayed <br> cables, and defective parts. |

Table 6-3
Internal Inspection Checklist

| Item | Inspect For | Repair Action |
| :--- | :--- | :--- |
| Circuit Boards | Loose, broken, or corroded solder connections. <br> Burned circuit boards. Burned, broken, or <br> cracked circuit-run plating. | Clean solder corrosion with an eraser and flush <br> with isopropyl alcohol. Resolder defective <br> connections. Determine cause of burned items <br> and repair. Repair defective circuit runs. |
| Resistors | Burned, cracked, broken, or blistered. | Replace defective resistors. Check for cause of <br> burned component and repair as necessary. |
| Solder Connections | Cold solder or rosin joints. | Resolder joint and clean with isopropyl alcohol. |

If any electrical component is replaced, conduct a Performance Check of the affected circuit and of other closely related circuits (see Section 4). If repair or replacement work is done on any of the power supplies, conduct a complete Performance Check and, if so indicated, an instrument readjustment (see Section 5).


To prevent damage from electrical arcing, ensure that circuit boards and components are dry before applying power to the instrument.

CLEANING. To clean the interior, blow off dust with dry, low-pressure air (approximately 9 psi ). Remove any remaining dust with a soft brush or a cloth dampened with a solution of mild detergent and water. A cotton-tipped applicator is useful for cleaning in narrow spaces and on circuit boards.

If these methods do not remove all the dust or dirt, the instrument may be spray washed using a solution of $5 \%$ mild detergent and $95 \%$ water as follows:

1. Gain access to the parts to be cleaned (see "Removal and Replacement Instructions").
2. Spray wash dirty parts with the detergent-and-water solution; then use clean water to thoroughly rinse them.
3. Dry all parts with low-pressure air.

SWITCH CONTACTS. The Vertical and Horizontal attenuators in this instrument are circuit-board mounted rotary switches. When cleaning them, care must be exercised to preserve their high-frequency characteristics. Switch maintenance is seldom necessary, but if it is required, use the following cleaning method and observe the stated precaution.
> $\{$ CAUTION
> Use only hot deionized or distilled water, $55^{\circ} \mathrm{C}$ $\left(131^{\circ} \mathrm{F}\right)$, to clean a rotary switch in this instrument. Tap water contains impurities which are left as residuals after evaporation.

1. Spray hot water into the slots at the top of each switch housing while rotating the switch control knob. Spray only for approximately five seconds, using an atomizing spray device.
2. Dry both the switch and the circuit board on which it is mounted, using dry low-pressure air.
3. Bake the switch and the circuit board at $75^{\circ} \mathrm{C}$ $\left(167^{\circ} \mathrm{F}\right)$ for 15 minutes to eliminate all moisture.
4. Spray a very small amount (only about a $1 / 2$-second squirt) of a recommended lubricant, such as No Noise, into the slots at the top of the switch housing.
5. Rotate the switch control knob about $180^{\circ}$ and again spray a very small amount of lubricant into each slot.

## LUBRICATION

Most of the potentiometers used in this instrument are permanently sealed and generally do not require periodic lubrication. All switches, both rotary- and lever-type, are installed with proper lubrication applied where necessary and will rarely require any additional lubrication. Therefore, a regular periodic lubrication program for the instrument is not recommended.

## SEMICONDUCTOR CHECKS

Periodic checks of the transistors and other semiconductors in the oscilloscope are not recommended. The best check of semiconductor performance is actual operation in the instrument.

## PERIODIC READJUSTMENT

To ensure accurate measurements, check the performance of this instrument after every 2000 hours of operation, or if used infrequently, once each year. In addition, replacement of components may necessitate readjustment of the affected circuits.

Complete Performance Check and Adjustment instructions are given in Sections 4 and 5 . The Performance Check Procedure can also be helpful in localizing certain trouble in the instrument. In some cases, minor problems may be revealed or corrected by readjustment. If only a partial adjustment is performed, see the interaction chart, Table 5-1, for possible adjustment interactions with other circuits.

## TROUBLESHOOTING

## INTRODUCTION

Preventive maintenance performed on a regular basis should reveal most potential problems before an instrument malfunctions. However, should troubleshooting be required, the following information is provided to facilitate location of a fault. In addition, the material presented in the "Theory of Operation" and the "Diagrams" sections of this manual may be helpful while troubleshooting.

## TROUBLESHOOTING AIDS

## Schematic Diagrams

Complete schematic diagrams are located on tabbed foldout pages in the "Diagrams" section. The portions of circuitry that are mounted on each circuit board are enclosed within heavy black lines. Also within the black lines, near either the top or the bottom edge, are the assembly number and name of the circuit board.

Component numbers and electrical values of components in this instrument are shown on the schematic diagrams. Refer to the first page of the "Diagrams" section for definitions of the reference designators and symbols used to identify components. Important voltages and waveform reference numbers (enclosed in hexagonal-shaped boxes) are also shown on each diagram. Waveform illustrations are located adjacent to their respective schematic diagram, and the physical location of each waveform test point is shown on the appropriate circuit board illustration.

## Circuit Board Illustrations

Circuit board illustrations (showing the physical location of each component) are provided for use in conjunction with each schematic diagram. Each board illustration can be found on the back side of a foldout page, preceding the schematic diagram(s) to which it relates. If more than one schematic diagram is associated with a particular circuit board, the board illustration is located on a left-hand page preceding the diagram with which the board is first associated.

Also provided in the "Diagrams" section is an illustration of the bottom side of the Main circuit board. This drawing facilitates troubleshooting by showing the connection pads and the location of components that are mounted on the top side of the board. Probing of Main board component signals that are inaccessible from the
top side can be achieved without the necessity of disassembling portions of the instrument.

Waveform test-point locations are also identified on the circuit board illustration by hexagonal-outlined numbers that correspond to the waveform numbers appearing on both the schematic diagram and the waveform illustration.

## Circuit Board Locations

An illustration depicting the location of a circuit board within the instrument is shown on the foldout page adjacent to the circuit board illustration.

## Circuit Board Interconnection Diagram

A circuit board interconnection diagram is also provided in the "Diagrams" section to aid in tracing a signal path or power source between boards. The entire oscilloscope is illustrated, with plug and jack numbers shown along with associated pin numbers. The off-board components are also shown, and the schematic diagram numbers on which these components can be found are identified.

## Power Distribution Diagram

A Power Distribution diagram is provided to aid in troubleshooting power-supply problems. This diagram shows service jumpers used to remove power from the various circuit boards. Excessive loading on a power supply by a circuit board can be isolated to the faulty board by disconnecting appropriate service jumpers.

## Grid Coordinate System

Each schematic diagram and circuit board illustration has a grid border along its left and top edges. A table located adjacent to each schematic diagram lists the grid coordinates of each component shown on that schematic. To aid in physically locating a component on the respective circuit board, this table also lists the circuit-board grid coordinate of each component.

Adjacent to each circuit board illustration is an alphanumeric listing of every component mounted on that board. A second column in this listing identifies the schematic diagram in which each component can be found. These component-locator tables are especially useful when more than one schematic diagram is associated with a particular circuit board.

## Troubleshooting Charts

The troubleshooting charts contained in the "Diagrams" section are to be used as an aid in locating malfunctioning circuitry. To use the charts, begin with the Troubleshooting Guide. This chart will help identify a particular problem area for further troubleshooting.

Note that some troubleshooting-procedure boxes on each chart contain numbers along their lower edges. These numbers identify the applicable schematic diagram(s) to be used when performing the action specified in the box.

Both General and Specific notes may be called out in the troubleshooting-chart boxes. These notes are located on the inner panels of the foldout pages. Specific Notes contain procedures or additional information to be used in performing the particular troubleshooting step called for in that box. General Notes contain information that pertains to the overall troubleshooting procedure.

Some malfunctions, especially those involving multiple simultaneous failures, may require more elaborate troubleshooting approaches with references to circuit descriptions in the "Theory of Operation" section of this manual.

## Component Color Coding

Information regarding color codes and markings of resistors and capacitors is located in the color-coding illustration (Figure 9-1) at the beginning of the "Diagrams" section.

RESISTOR COLOR CODE. Resistors used in this instrument are carbon-film, composition, or precision metal-film types. They are color coded with the EIA color code; however, some metal-film resistors may have the value printed on the body. The color code is interpreted by starting with the stripe that is nearest to one end of the resistor. Composition resistors have four stripes; these represent two significant figures, a multiplier, and a tolerance value. Metal-film resistors have five stripes which represent three significant figures, a multiplier, and a tolerance value.

CAPACITOR MARKINGS. Capacitance values of common disc capacitors and small electrolytics are marked on the side of the capacitor body. White ceramic capacitors are color coded in picofarads, using a modified EIA code.

Dipped tantalum capacitors are color coded in microfarads. The color dot indicates both the positive lead and the voltage rating. Since these capacitors are easily destroyed by reversed or excessive voltage, be careful to observe the polarity and voltage rating.

DIODE COLOR CODE. The cathode end of each glassencased diode is indicated by either a stripe, a series of stripes, or a dot. For most silicon or germanium diodes marked with a series of stripes, the color combination of the stripes identifies three digits of the Tektronix Part Number, using the resistor color-code system (e.g., a diode having either a pink or a blue stripe at the cathode end, then a brown-gray-green stripe combination, indicates Tektronix Part Number 152-0185-00). The cathode and anode ends of a metal-encased diode can be identified by the diode symbol marked on its body.

## Semiconductor Lead Configurations

Figure 9-2 in the "Diagrams" section shows the lead configurations for semiconductor devices used in the instrument. These lead configurations and case styles are typical of those available at completion of the design of the instrument. Vendor changes and performance improvement changes may result in changes of case styles or lead configurations. If the device in question does not appear to match the configuration in Figure 9-2, examine the associated circuitry or consult a semiconductor manufacturer's data sheet.

## Multipin Connectors

Multipin connector orientation is indicated by two triangles: one on the holder and one on the circuit board. Slot numbers are usually molded into the hoider. When a connection is made to circuit-board pins, ensure that the triangle on the holder and the triangle on the circuit board are aligned with each other (see Figure 6-1).

## TROUBLESHOOTING EQUIPMENT

The equipment listed in Table 4-1, or equivalent equipment, may be useful when troubleshooting this instrument.

## TROUBLESHOOTING TECHNIQUES

The following procedure is arranged in an order that enables checking simple trouble possibilities before requiring more extensive troubleshooting. The first four checks ensure proper control settings, connections, operation, and adjustment. If the trouble is not located by these checks, the remaining steps will aid in locating the defective component. When the defective component is


Figure 6-1. Multipin connector orientation.
located, replace it, using the appropriate replacement procedure given under "Corrective Maintenance" in this section.


Before using any test equipment to make measurements on static-sensitive, current-sensitive, or voltagesensitive components or assemblies, ensure that any voltage or current supplied by the test equipment does not exceed the limits of the component to be tested.

## 1. Check Control Settings

Incorrect control settings can give a false indication of instrument malfunction. If there is any question about the correct function or operation of any control, refer to either the "Operating Instructions" (Section 2) in this manual or to the instrument Operators Manual.

## 2. Check Associated Equipment

Before proceeding, ensure that any equipment used with this instrument is operating correctly. Verify that input signals are properly connected and that the interconnecting cables are not defective. Check the power-inputsource voltages.

## WARNING

To avoid electric shock, disconnect the instrument from the power-input source before performing visual inspection.

## 3. Visual Check

Perform a visual inspection. This check may reveal broken connections or wires, damaged components, semiconductors not firmly mounted, damaged circuit boards, or other clues.

## WARNING

Dangerous potentials exist at several points throughout this instrument. If it is operated with the cabinet removed, do not touch exposed connections or components.

## 4. Check Instrument Performance and Adjustment

Check the performance of either those circuits where trouble appears to exist or the entire instrument. The apparent trouble may only be the result of misadjustment. Complete performance check and adjustment instructions are given in Sections 4 and 5 of this manual.

## 5. Isolate Trouble to a Circuit

To isolate problems to a particular area, use the trouble symptom to help identify the circuit in which the trouble is located. Refer to the troubleshooting charts in the "Diagrams" section as an aid in locating a faulty circuit.

## 6. Check Power Supplies

## WARNING

It is recommended for safety that an isolation transformer be connected between the ac-power source and the autotransformer whenever troubleshooting is done in the Preregulator and the Inverter Power Supply sections. Most autotransformers are NOT isolation transformers.

Check the power supplies whenever trouble symptoms appear in more than one circuit. The correct output voltage and ripple for each supply should be measured between the supply test point and chassis ground (see Diagram 9 and its associated circuit board illustration). When checking powersupply circuitry utilizing common as the reference, use either a DMM or an oscilloscope and observe the preceding WARNING. If power supply voltages and ripple are within their listed ranges, the supply can be assumed to be operating correctly. If any are outside their ranges, the supply may be either misadjusted or operating incorrectly. A defective component elsewhere in the instrument can create the appearance of a power-supply problem and may also affect the operation of other circuits.

## 7. Check Circuit Board Interconnections

After the trouble has been isolated to a particular circuit, again check for loose or broken connections and heat-damaged components.

## 8. Check Voltages and Waveforms

Often the defective component can be located by checking the appropriate voltage or waveform in the circuit. Typical voltages are listed on the schematic diagrams. Waveforms are shown adjacent to the schematics, and waveform test points are indicated on both the schematics and circuit board illustrations by hexagonal-outlined numbers.

## NOTE

Voltages and waveforms given on the schematic diagrams are not absolute and may vary slightly between instruments. To establish operating conditions similar to those used to obtain these readings, see the "Voltage and Waveform Setup" conditions in the "Diagrams" section for the preliminary equipment setup. Note the recommended test equipment, initial front-panel control settings, and cableconnection instructions. The control-setting changes (from initial setup) required to obtain the given waveforms and voltages are located on the waveformdiagram page.

## WARNING

To avoid electric shock, always disconnect the instrument from the power input source before removing or replacing components.

## 9. Check Individual Components

The following procedures describe methods of checking individual components. Two-lead components that are soldered in place are most accurately checked by first disconnecting one end from the circuit board. This isolates the measurement from the effects of surrounding circuitry. See Figure 9-1 for value identification or Figure 9-2 for typical semiconductor lead configuration.


When checking semiconductors, observe the staticsensitivity precautions located at the beginning of this section.

TRANSISTORS. A good check of transistor operation is actual performance under operating conditions. A transistor can most effectively be checked by substituting a
known good component. However, be sure that circuit conditions are not such that a replacement transistor might also be damaged. If substitute transistors are not available, use a dynamic tester. Static-type testers are not recommended, since they do not check operation under simulated operating conditions.

When troubleshooting transistors in the circuit with a voltmeter, measure both the emitter-to-base and emitter-to-collector voltages to determine whether they are consistent with normal circuit voltages. Voltages across a transistor may vary with the type of device and its circuit function.

Some of these voltages are predictable. The emitter-to-base voltage for a conducting silicon transistor will normally range from 0.6 to 0.8 V , and the emitter-tobase voltage for a conducting germanium transistor ranges from 0.2 to 0.4 V . The emitter-to-collector voltage for a saturated transistor is about 0.2 V . Because these values are small, the best way to check them is by connecting a sensitive voltmeter across the junction rather than comparing two voltages taken with respect to ground. If the former method is used, both leads of the voltmeter must be isolated from ground.

If values less than these are obtained, either the device is shorted or no current is flowing in the external circuit. If values exceed the emitter-to-base values given, either the junction is reverse biased or the device is defective. Voltages exceeding those given for typical emitter-to-collector values could indicate either a nonsaturated device operating normally or a defective (open-circuited) transistor. If the device is conducting, voltage will be developed across the resistors in series with it; if it is open, no voltage will be developed across the resistors in series with it, unless current is being supplied by a parallel path.


When checking emitter-to-base junctions, do not use an ohmmeter range that has a high internal current. High current can damage the transistor. Reverse biasing the emitter-to-base junction with a high current mav degrade the transistor's current-transfer ratio (Beta).

A transistor emitter-to-base junction also can be checked for an open or shorted condition by measuring the resistance between terminals with an ohmmeter set to a range having a low internal source current, such as the $R \times 1 \mathrm{k} \Omega$ range. The junction resistance should be very high in one direction and very low when the meter leads are reversed.

When troubleshooting a field-effect transistor, the voltage across its elements can be checked in the same manner as previously described for other transistors. However, remember that in the normal depletion mode of operation, the gate-to-source junction is reverse biased; in the enhanced mode, the junction is forward biased.

INTEGRATED CIRCUITS. An integrated circuit (IC) can be checked with a voltmeter, test oscilloscope, or by direct substitution. A good understanding of circuit operation is essential to troubleshooting a circuit having an IC. Use care when checking voltages and waveforms around the IC so that adjacent leads are not shorted together. The grabber tip or an IC test clip provides a convenient means of clipping a test probe to an IC.


When checking a diode, do not use an ohmmeter range that has a high internal current. High current can damage the diode. Checks on diodes can be performed in much the same manner as on transistor emitter-to-base junctions. Do not check tunnel diodes or back diodes with an ohmmeter; use a dynamic tester, such as the TEKTRONIX 576 Curve Tracer.

DIODES. A diode can be checked for either an open or a shorted condition by measuring the resistance between terminals with an ohmmeter set to a range having a low internal source current, such as the $R \times 1 \mathrm{k} \Omega$ range. The diode resistance should be very high in one direction and very low when the meter leads are reversed.

Silicon diodes should have 0.6 to 0.8 V across their junctions when conducting. Higher readings indicate that they are either reverse biased or defective, depending on polarity.

RESISTORS. Check resistors with an ohmmeter. Refer to the "Replaceable Electrical Parts" list for the tolerances of resistors used in this instrument. A resistor normally does not require replacement unless its measured value varies widely from its specified value and tolerance.

INDUCTORS. Check for open inductors by checking continuity with an ohmmeter. Shorted or partially shorted inductors can usually be found by checking the waveform response when high-frequency signals are passed through the circuit.

CAPACITORS. A leaky or shorted capacitor can best be detected by checking resistance with an ohmmeter set to one of the highest ranges. Do not exceed the voltage rating of the capacitor. The resistance reading should be high after the capacitor is charged to the output voltage of the ohmmeter. An open capacitor can be detected with a capacitance meter or by checking whether the capacitor passes ac signals.

## 10. Repair and Adjust the Circuit

If any defective parts are located, follow the replacement procedures given under "Corrective Maintenance" in this section. After any electrical component has been replaced, the performance for that particular circuit should be checked, as well as the performance of other closely related circuits. Since the power supplies affect all circuits, performance of the entire instrument should be checked if work has been done in any of the power supplies or if the power transformer has been replaced. Readjustment of the affected circuitry may be necessary. Refer to the "Performance Check Procedure" and "Adjustment Procedure" (Sections 4 and 5) and to Table 5-1 (Adjustment Interactions).

## CORRECTIVE MAINTENANCE

## INTRODUCTION

Corrective maintenance consists of component replacement and instrument repair. This part of the manual describes special techniques and procedures required to replace components in this instrument. If it is necessary to ship your instrument to a Tektronix Service Center for repair or service, refer to the "Repackaging for Shipment" instructions at the end of this section.

## MAINTENANCE PRECAUTIONS

To reduce the possibility of personal injury or instrument damage, observe the following precautions.

1. Disconnect the instrument from the ac power input source before removing or installing components.
2. Use care not to interconnect instrument grounds which may be at different potentials (cross grounding).
3. When soldering on circuit boards or small insulated wires, use only a 15 -watt, pencil-type soldering iron.

## OBTAINING REPLACEMENT PARTS

Most electrical and mechanical parts can be obtained through your local Tektronix Field Office or representative. However, many of the standard electronic components can usually be obtained from a local commercial source. Before purchasing or ordering a part from a source other than Tektronix, Inc., please check the "Replaceable Electrical Parts" list (Section 8) for the proper value, rating, tolerance, and description.

## NOTE

Physical size and shape of a component may affect instrument performance, particularly at high frequencies. Always use direct-replacement components, unless it is known that a substitute will not degrade instrument performance.

## Special Parts

In addition to the standard electronic components, some special parts are used in this instrument. These
parts are manufactured or selected by Tektronix, Inc. to meet specific performance requirements, or are manufactured for Tektronix, Inc. in accordance with our specifications. The various manufacturers can be identified by referring to the "Cross Index-Mfr Code Number to Manufacturer" at the beginning of the "Replaceable Electrical Parts" list. Most of the mechanical parts used in this instrument were manufactured by Tektronix, Inc. Order all special parts directly from your local Tektronix Field Office or representative.

## Ordering Parts

When ordering replacement parts from Tektronix, Inc., be sure to include all of the following information:

1. Instrument type (include modification or option numbers).
2. Instrument serial number.
3. A description of the part (if electrical, include its component number).

## 4. Tektronix part number.

## MAINTENANCE AIDS

The maintenance aids listed in Table 6-4 include items required for performing most of the maintenance procedures on this instrument. Equivalent products may be substituted for the examples given, provided their characteristics are similar.

## INTERCONNECTIONS

Pin connectors are used to connect wires to the interconnecting pins. They are grouped together and mounted in a plastic holder and should be removed, reinstalled, or replaced as a unit. If an individual wire or connector in the assembly is faulty, the entire cable assembly should be replaced. To provide correct orientation of this multipin connector when it is reconnected to its mating pins, an arrow is stamped on the circuit board, and a matching arrow is molded into the plastic housing of the multipin connector. Be sure these arrows are aligned with each other when the multipin connector is reinstalled.

Table 6-4
Maintenance Aids

| Description | Specifications | Usage | Example |
| :--- | :--- | :--- | :--- |
| 1. Soldering Iron | 15 to 25 W. | $\begin{array}{l}\text { General soldering and } \\ \text { unsoldering. }\end{array}$ | Antex Precision Model C. |
| 2. Torx Screwdrivers | $\begin{array}{l}\text { Torx tips \#T7, \#T8, \#T9, } \\ \text { \#T15 and \#T20. }\end{array}$ | Assembly and disassembly. | $\begin{array}{l}\text { Tektronix Part Numbers } \\ \text { \#T7) 003-1293-00 } \\ \text { \#T8) 003-0964-00 } \\ \text { \#T9) 003-0965-00 }\end{array}$ |
| \#T15) 003-0966-00 |  |  |  |
| \#T20) 003-0866-00 |  |  |  |$]$| Xcelite \#8, \#9, \#10, \#16, |
| :--- |
| and \#18. |

## TRANSISTORS AND INTEGRATED CIRCUITS

Transistors and integrated circuits should not be replaced unless they are actually defective. If unsoldered from the circuit board during routine maintenance, return them to their original board locations. Unnecessary replacement or transposing of semiconductor devices may affect the adjustment of the instrument. When a semiconductor is replaced, check the performance of any instrument circuit that may be affected.

Any replacement component should be of the original type or a direct replacement. Bend transistor leads to fit their circuit board holes and cut the leads to the same length as the original component. See Figure 9-2 for typical lead-configuration illustrations.

To remove a soldered dual-in-line packaged (DIP) IC, do not heat adjacent conductors consecutively. Apply heat to pins at alternate sides and ends of the IC as solder is removed. Allow a moment for the circuit board to cool before proceeding to the next pin.

The heat-sink-mounted power supply transistors are insulated from the heat sink. In addition, a heat-sink compound is used to increase heat transfer capabilities. Reinstall the insulators and replace the heat-sink compound when replacing these transistors. The compound should be applied to both sides of the insulators and should be applied to the bottom side of the transistor where it comes in contact with the insulator.

NOTE
After replacing a power transistor, check that the collector is not shorted to the heat sink before applying power to the instrument.

## SOLDERING TECHNIQUES

The reliability and accuracy of this instrument can be maintained only if proper soldering techniques are used to remove or replace parts. General soldering techniques, which apply to maintenance of any precision electronic equipment, should be used when working on this instrument.

## WARNING

To avoid an electric-shock hazard, observe the following precautions before attempting any soldering: turn the instrument off, disconnect it from the ac power source, and allow approximately three minutes for the power-supply capacitors to discharge.

Use rosin-core wire solder containing $63 \%$ tin and $37 \%$ lead. Contact your local Tektronix Field Office or representative to ohtain the names of approved solder types.

When soldering on circuit boards or small insulated wires, use only a 15 -watt, pencil-type soldering iron. A higher wattage soldering iron can cause etched circuit conductors to separate from the board base material and melt the insulation on small wires. Always keep the soldering-iron tip properly tinned to ensure best heat transfer from the iron tip to the solder joint. To protect heatsensitive components, either hold the component lead with long-nose pliers or place a heat block between the component body and the solder joint. Apply only enough solder to make a firm joint. After soldering, clean the area around the solder connection with an approved fluxremoving solvent (such as isopropyl alcohol) and allow it to air dry.


Attempts to unsolder, remove, and resolder leads from the component side of a circuit board may cause damage to the reverse side of the circuit board.

The following techniques should be used to replace a component on any of the circuit boards:

1. Touch the vacuum desoldering tool to the lead at the solder connection. Never place the iron directly on the board; doing this may damage the board.

## NOTE

Some components are difficult to remove from the circuit board due to a bend placed in each lead during machine insertion of the component. The purpose of the bent leads is to hold the component in place during a solder-flow manufacturing process that solders all the components at once. To make removal of machine-inserted components easier, straighten the component leads on the reverse side of the circuit board with a small screwdriver or pliers. It may be necessary to remove the circuit board to gain access to the component leads on the reverse side of the circuit board. Circuit-board removal and reinstallation procedures are discussed later in this section.
2. When removing a multipin component, especially an IC, do not heat adjacent pins consecutively. Apply heat to pins at alternate sides and ends of the $I C$ as solder is removed. Allow a moment for the circuit board to cool before proceeding to the next pin.


Excessive heat can cause the etched circuit conductors to separate from the circuit board. Never allow the solder extractor tip to remain at one place on the board for more than three seconds. Solder wick, spring-actuated or squeeze-bulb solder suckers, and heat blocks (for desoldering multipin components) must not be used. Damage caused by poor soldering techniques can void the instrument warranty.
3. To replace the component, bend the leads of the replacement item to fit the holes in the circuit board. If the component is replaced while the board is installed in the instrument, cut the leads so they protrude only a small amount through the reverse side of the circuit board. Excess lead length may cause shorting to other conductive parts.
4. Insert the leads into the holes of the board so that the replacement component is positioned the same as the original component. Most components should be firmly seated against the circuit board.
5. Touch the soldering iron to the connection and apply enough solder to make a firm solder joint. Do not move the component while the solder hardens.
6. Cut off any excess lead protruding through the circuit board (if not clipped to size in step 3 ).
7. Clean the area around the solder connection with an approved flux-removing solvent. Be careful not to remove any of the printed information from the circuit board.

## REMOVAL AND REPLACEMENT INSTRUCTIONS

The exploded view drawings in the "Replaceable Mechanical Parts" list may be helpful during the removal and reinstallation of individual subassemblies or components. Circuit board and component locations are shown in the "Diagrams" section.

## Cabinet

## WARNING

To avoid electric shock, disconnect the instrument from the ac-power-input source before removing or replacing any component or assembly.

To remove the instrument cabinet, perform the following steps:

1. Disconnect the instrument from its ac-power-input source.
2. On instruments with detachable power cords, disconnect the power cord from the instrument.
3. Remove the screw from the right rear side of the cabinet and two screws from the rear panel. Then remove the rear panel and, if applicable, feed the nondetachable power cord through the rear panel as the panel is removed.
4. Pull the front panel and attached chassis forward and out of the cabinet.

To reinstall the cabinet, perform the following steps:
5. Slide the chassis frame into the cabinet from the front until the cabinet is fully into the front-panel groove and the rear of the cabinet is flush with the rear of the chassis.
6. Feed the attached power cord (if applicable) through the hole in the rear panel. Align the rear-panel and side mounting holes with the screw holes in the chassis frame and reinstall the three screws removed in step 3.


To ensure that the cabinet is grounded to the instrument chassis, the screw at the right rear side of the cabinet should be tightly secured.
7. Reconnect the power cord (if disconnected in step 2).

Cathode-Ray Tube

## WARNING

Use care when handling a crt. Breakage of the crt may cause high-velocity scattering of glass fragments (implosion). Protective clothing and safety glasses should be worn. Avoid striking the crt on any object which may cause it to crack or implode. When storing a crt, either place it in a protective carton or set it face down on a smooth surface in a protected focation with a soft mat under the faceplate.

To remove the cr , perform the following steps:

1. Disconnect four deflection-plate wires at the middle of the crt neck and unplug the Trace Rotation connector (P8006) from the Front-Panel circuit board (note the connection locations and wire color for reinstallation reference).

## WARNING

The crt anode and output terminal of the HighVoltage Multiplier will retain a high-voltage charge after the instrument is turned off. To avoid electrical shock, ground both the output terminal of the multiplier and the crt high-voltage anode lead to the main instrument chassis after disconnecting the high-voltage lead.
2. Unplug the crt anode lead connector from the HighVoltage Multiplier at the front left corner of the HighVoltage shield and discharge it to the chassis.
3. Remove two screws that retain the plastic crt frame and light filter to the front panel. Remove the crt frame and light filter from the instrument.
4. With the rear of the instrument facing you, place the fingers of both hands over the front edge of the front subpanel. Then, using both thumbs, press forward gently on the crt funnel near the front of the crt. When the crt
base pins disengage from the socket, remove the crt and crt shield through the instrument front subpanel. Place the crt in a safe place until it is ready to reinstall. If the plastic crt corner pads fall out, save them for reinstallation.

To reinstall the crt, perform the following steps:
5. Reinstall any plastic crt corner pads that may be out of place. Insert the crt, crt shield, anode lead, and Trace Rotation leads through the front-panel opening. Make sure all pins are straight and that the indexing keys on the crt base and socket are aligned. Make sure the crt shield ground clip only makes contact with the outside of the crt shield.
6. Push the crt base into the socket. Check that they are flush together as viewed from the rear and that the crt is seated properly in its front-panel opening.
7. Reinstall the crt frame and light filter; then secure them with two screws (removed in step 3).
8. Reconnect the crt anode lead to the High-Voltage Multiplier (disconnected in step 2).
9. Reconnect the four deflection-plate wires and the Trace Rotation connector (disconnected in step 1).

## High-Voltage Shield

To remove the High-Voltage shield, perform the following steps:

1. Remove the screw from the plastic high-voltage cover on the bottom section of the Main circuit board. Press gently on the rear of the cover and slide it forward.
2. Remove the screw securing the High-Voltage shield to the Main circuit board llocated at the bottom of the circuit board near the right side of the frame).
3. Remove two screws securing the left rear of the High-Voltage shield to the back of the chassis frame.
4. Remove the screw from the front upper right-hand corner of the High-Voltage shield.
5. Remove the screw at the front upper left-hand corner and rotate the support bracket away from the High-Voltage shield.
6. Lift the shield up and out of the chassis frame by removing the right rear corner first.

To reinstall the High-Voltage shield, perform the following steps:
7. Insert the shield into the chassis frame. Make sure that the shield's right and back top edges are in their chassis frame guides, that the crt socket-wire assembly is in its cutout, and that the Alt Sweep board is in its plastic holder.
8. Rotate the support bracket back into place and secure it with the screw removed in step 5 .
9. Reinstall the screw at the upper right-hand corner of the shield (removed in step 4).
10. Reinstall two screws securing the shield to the back of the chassis frame (removed in step 3).
11. Reinstall the screw holding the shield to the Main circuit board at the right side of the frame (removed in step 2).
12. Reinstall the plastic high-voltage cover on the bottom of the Main circuit board and secure the shield and cover with one screw (removed in step 1).

## Alt Sweep Circuit Board

To remove the Alt Sweep circuit board, perform the following steps:

1. Use a vacuum-desoldering tool to unsolder the 27 pins (which secure the Alt Sweep circuit board to the Main circuit board) from the Main circuit board.
2. Remove the Alt Sweep circuit board from the instrument by unclipping it from the plastic holder attached to the High-Voltage shield.
3. If component removal is desired, remove the two nuts which secure the shield to the Alt Sweep circuit board and remove the shield.

To reinstall the Alt Sweep circuit board, perform the following steps:
4. Reinstall the shield to the Alt Sweep circuit board (if previously removed in step 3).
5. Insert the 27 pins of the Alt Sweep circuit board into the Main circuit board.
6. Reinstall the Alt Sweep circuit board into the plastic holder attached to the High-Voltage shield.
7. Resolder the 27 pins to the Main circuit board (unsoldered in step 1).

## Attenuator/Sweep Circuit Board

To remove the Attenuator/Sweep circuit board, perform the following steps:

1. Use a $1 / 16$-inch Allen wrench to loosen the set screws in the following knobs and remove the knobs: CH 1 and CH 2 VOLTS/DIV Variable and SEC/DIV Variable.
2. Set the CH 1 and CH 2 VOLTS/DIV switches to the same position; then remove their knobs by pulling straight out from the front panel. Note switch positions for reinstallation reference.
3. Use a $9 / 16$-inch nut driver to remove the nuts securing the VOLTS/DIV switches to the front panel.
4. Lock the A and B SEC/DIV knobs together and note their position for reinstallation reference. Use a $1 / 4$-inch nut driver to remove the nut and washers securing the B SEC/DIV knob; pull off the knob and collet from the shaft assembly.
5. Use a 1/16-inch Allen wrench to loosen the set screws which secure the A SEC/DIV dial to the shaft assembly.
6. Disconnect the following connectors from the Attenuator/Sweep circuit board:
a. P1011, a four-wire connector located behind the CH 1 VOLTS/DIV switch assembly.
b. P2011, a four-wire connector located behind the CH 2 VOLTS/DIV switch assembly.
c. P7000, a seven-wire connector located on the rear edge of the circuit board.
d. P6000, a ten-wire connector located on the right edge of the circuit board.
7. Remove three screws which secure the shield to the Main circuit board (located on the bottom of the Main circuit board).
8. Loosen but do not remove two screws securing the front of the shield to a bracket on the front panel. These screws are accessible from the bottom of the instrument through twoholes along the front of the Main circuit board.
9. Pull the Attenuator/Sweep circuit board and shield assembly straight back from the front of the instrument until the circuit board interconnecting pins are disengaged and the switch shafts are clear of the holes in the FrontPanel circuit board. Then lift out the entire assembly through the top of the instrument.
10. If accessibility to the bottom of the Attenuator/ Sweep circuit board is desired, remove three screws located at three corners of the circuit board and two screws from the bottom of the shield and separate the shield from the circuit board.

To reinstall the Attenuator/Sweep circuit board, perform the following steps:
11. If the shield has been removed, secure it to the Attenuator/Sweep circuit board using three screws (removed in step 10). Insert two screws in the bottom of the shield at the front edge (removed in step 10), but do not tighten them.
12. Insert the three switch shafts through the holes in the Front-Panel circuit board and the front panel. Carefully align the 10 interconnecting pins on the Front-Panel circuit board with their corresponding connectors on the Attenuator/Sweep circuit board. Push the board forward into position, ensuring that the two screws in the bottom shield engage the front-panel bracket.
13. Tighten two screws securing the shield to the frontpanel bracket (loosened in step 8).
14. Reinstall three screws securing the shield to the Main circuit board (removed in step 7). Then tighten the two screws installed at the front edge of the shield in step 11.
15. Reconnect the four connectors to the Attenuator/ Sweep circuit board that were disconnected in step 6.
16. Reinstall two $9 / 16$-inch nuts securing the VOLTS/ DIV switch shafts to the front panel (removed in step 3).
17. Reinstall the two VOLTS/DIV knobs at the positions noted in step 2.
18. Reinstall the A SEC/DIV dial in the position noted in step 4 and secure it with two set screws loosened in step 5.
19. Reinstall the collet and B SEC/DIV knob (at the position noted in step 4) and secure it with the washers and nut (removed in step 4).
20. Rotate the three Variable control shafts fully clockwise to their calibrated detent positions.
21. Reinstall the Variable knobs onto their shafts (with the lettering horizontal and right-side up) and tighten their set screws.

## Front-Panel Circuit Board

To remove the Front-Panel circuit board, perform the following steps:

1. Remove the crt (see the "Cathode-Ray Tube" removal procedure).
2. Remove the Attenuator/Sweep circuit board (see the "Attenuator/Sweep Circuit Board" removal procedure).
3. Remove the knobs from the following control shafts by pulling them straight out from the front panel: Channel 1 and Channel 2 POSITION, A/B SWP SEP, Horizontal POSITION, AUTO FOCUS, AUTO INTENSITY, A TRIGGER LEVEI., and B TRIGGER LEVEL.
4. Unplug the three-wire B DELAY TIME POSITION potentiometer connector (P7055) from the Main circuit board (located in front of the High-Voltage shield).
5. Unsolder the resistors from the $\mathrm{CH} 1 \mathrm{OR} \mathrm{X}, \mathrm{CH} 2 \mathrm{OR}$ Y , and EXT INPUT connectors and disconnect the twowire connector ( P 1000 ) from the Front-Panel circuit board to the PROBE ADJUST jack. Unsolder two wires (from the VAR HOLDOFF control) from the Front-Panel circuit board.
6. Remove two screws securing the Main circuit board to the left bottom side of the chassis frame.
7. Remove three screws securing the upper part of the Front-Panel circuit board to the front panel.
8. Remove four recessed frame-securing screws (two at the right front corner and two at the left rear corner of the frame).
9. Pull the front- and left-frame assembly apart from the rear- and right-frame assembly.

## NOTE

At this point, any component on the Front-Panel circuit board may be accessed for removal and replacement. Skip to step 12 of this procedure after component replacement. If circuit board replacement is intended, continue with the remaining disassembly steps.
10. Use a vacuum-desoldering tool to unsolder the 39 wire straps from the Main circuit board which connect to the Front-Panel circuit board.
11. Remove the Front-Panel circuit board from the instrument and clean the wire-strap holes on the Main circuit board of any remaining solder.

## NOTE

If a vacuum-desoldering tool is not available, lift each strap out of the Main circuit board as its joint is heated.

To reinstall the Front-Panel circuit board, perform the following steps:
12. Insert but do not solder the 39 wire straps on the Front-Panel circuit board into their corresponding holes in the Main circuit board (unsoldered in step 10).
13. Align the two frame assemblies disassembled in step 9, making sure the POWER extension-shaft button is in place in the front panel. Reinstall four frame-securing screws (removed in step 8).
14. Reinstall three screws securing the Front-Panel circuit board to the front panel (removed in step 7).
15. Reinstall two screws securing the left side of the Main circuit board to the frame (removed in step 6).
16. Resolder the resistors to the connectors (unsoldered in step 5) and reconnect the two-wire connector from the PROBE ADJUST jack to the Front-Panel circuit board (disconnected in step 5). Resolder the 39 wire straps on the Front-Panel circuit board to the Main circuit board. Resolder the two wires from the VAR HOLDOFF control (unsoldered in step 5).
17. Reconnect the three-wire B DELAY TIME POSITION potentiometer connector to the Main circuit board (removed in step 4).
18. Replace the front-panel knobs (removed in step 3).
19. Reinstall the Attenuator/Sweep circuit board (see the "Attenuator/Sweep Circuit Board" reinstallation procedure).
20. Reinstall the crt (see the "Cathode-Ray Tube" reinstallation procedure).

## Main Circuit Board

All components on the Main circuit board are accessible either directly or by removing the crt, Attenuator/Sweep circuit board, or High-Voltage shield. Removal of the Main circuit board is required only when it is necessary to replace the board with a new one.

To remove the Main circuit board, perform the following steps:

1. Remove the Attenuator/Sweep circuit board (see the "Attenuator/Sweep Circuit Board" removal procedure).
2. Disconnect the three-wire B DELAY TIME POSITION potentiometer connector (P7055) from the Main circuit board (located in front of the High-Voltage shield).
3. Remove the High-Voltage shield (see the "HighVoltage Shield" removal procedure).
4. Remove the Alt Sweep circuit board (see the "Alt Sweep Circuit Board" removal procedure).
5. Remove the AUTO FOCUS control-knob shaft assembly by pulling it straight out from the front panel.
6. Remove the POWER switch extension-shaft push button assembly by first pressing in the POWER button to the ON position. Insert a scribe or similar tool into the notch between the end of the switch shaft and the end of the extension shaft and gently pry the connection apart. Push the extension shaft forward, then sideways, to clear the switch shaft. Then pull the extension shaft back and out of the instrument.
7. Disconnect the leads of L925 (inductor), the lead of the fuse holder, the lead of the power-cord connector, and four leads (P801, P802, P803, and P804 from the Current Limit board) from the Main circuit board.
8. Unsolder the rear-panel EXT $Z$ AXIS connector wire from the Main circuit board.
9. Unsolder two sets of crt socket wires from the Main circuit board, noting wire color and position for reinstallation reference.
10. Unsolder two sets of delay-line wires from the Main circuit board, noting wire color and position for reinstallation reference.
11. Remove two screws securing the power-supply transistor heat-sink assembly to the right side of the frame.
12. Remove three screws securing the Main circuit board to the instrument frame (one under the EXT $Z$ AXIS connector and two along the left side of the Main circuit board).
13. Use a vacuum-desoldering tool to unsolder the 39 interconnecting wire straps (connecting the Main circuit board to the Front-Panel circuit board) from the Main circuit board.

## NOTE

If a vacuum-desoldering tool is not available, lift each wire strap out of the Main circuit board as its joint is heated. Use care to maintain, as nearly as possible, the original shape and spacing of the wire straps to facilitate replacing the circuit board.
14. Push the wire-strap connection end of the Main circuit board down until it is clear of the wire strap ends; then remove it through the bottom of the instrument frame. Ensure that the interconnecting wire straps are not bent out of place.
15. Unsolder the delay-line holder tabs from the Main circuit board.

To replace the Main circuit board, use the following procedure:
16. Insert the delay-line holder tabs into the replacement circuit board and solder them in place. Ensure that the hole in the front tab and the mounting hole in the circuit board are aligned.
17. Place the Main circuit board into the chassis frame, ensuring that the board is in the guides at the rear and right side of the frame.
18. Reinstall three screws securing the Main circuit board to the frame (removed in step 12).
19. Reinstall two securing screws in the power-supply transistor heat-sink assembly (removed in step 11).
20. Move the front part of the Main circuit board into position. Align the 39 wire straps and insert them into their corresponding holes while maintaining their original shape and spacing.
21. Resolder the wire straps to the Main circuit board.
22. Resolder two sets of delay-line wires at the location noted in step 10.
23. Resolder two sets of crt socket wires at the locations noted in step 9 .
24. Insert and resolder the EXT Z AXIS connector wire into the Main circuit board.
25. Reconnect the leads of L925 (inductor), the fuse holder, the power cord connector, and four wires from the Current Limit board (removed in step 7).
26. Insert the POWER switch extension-shaft push button assembly into the front panel (from the rear). Use a flat-bit screwdriver to hold the POWER switch shaft fully in and align the extension shaft with the switch shaft. Press them together gently until they snap into position.
27. Reinstall the AUTO FOCUS knob shaft assembly (removed in step 5).
28. Reinstall the High-Voltage shield (see the "HighVoltage Shield" reinstallation procedure).
29. Reconnect the B DELAY TIME POSITION potentiometer connector (P7055) to the Main circuit board (located in front of the High-Voltage shield).
30. Reinstall the Alt Sweep circuit board (see the "Alt Sweep Circuit Board" reinstallation procedure).
31. Reinstall the Attenuator/Sweep circuit board (see the "Attenuator/Sweep Circuit Board" reinstallation procedure).

## Current Limit Circuit Board

To remove the Current Limit board, perform the following steps:

1. Remove the High-Voltage shield (see the "HighVoltage Shield" removal procedure).
2. Disconnect the four single-wire connectors from the Current Limit board (P801, P802, P803, and P804).
3. Remove the screw and nut which secure the Current Limit board to the chassis frame.

To reinstall the Current Limit board, perform the following steps:
4. Reinstall the securing screw and nut (removed in step 3).
5. Reconnect the four single-wire connectors (removed in step 2).
6. Reinstall the High-Voltage shield (see the "HighVoltage Shield" reinstallation procedure).

## REPACKAGING FOR SHIPMENT

If the instrument is to be shipped to a Tektronix Service Center for service or repair, attach a tag showing: owner (with address) and the name of an individual at your firm that can be contacted. Include complete instrument serial number and a description of the service required. Listings of Tektronix Sales and Service offices, both domestic and international, are located at the back of the manual following the tabbed "Accessories" page.

Save and reuse the package in which your instument was shipped. If the original packaging is unfit for use or not available, repackage the instrument as follows:

Surround the instrument with polyethylene sheeting to protect its finish. Obtain a carton of corrugated cardboard having a carton test strength of 275 pounds and having inside dimensions of no less than six inches more than the instrument dimensions. Cushion the instrument by tightly packing three inches of dunnage or urethane foam between carton and instrument, on all sides. Seal carton with shipping tape or industrial stapler.

## OPTIONS

There are currently no options available for the 2215, except the optional power cords previously described in Section 2.

# REPLACEABLE <br> ELECTRICAL PARTS 

PARTS ORDERING INFORMATION

Replacement parts are available from or through your local Tektronix, Inc. Field Office or representative.

Changes to Tektronix instruments are sometimes made to accommodate improved components as they become available, and to give you the benefit of the latest circuit improvements developed in our engineering department. It is therefore important, when ordering parts, to include the following information in your order: Part number, instrument type or number, serial number, and modification number if applicable.

If a part you have ordered has been replaced with a new or improved part, your local Tektronix, Inc. Field Office or representative will contact you concerning any change in part number.

Change information, if any, is located at the rear of this manual.

## LIST OF ASSEMBLIES

A list of assemblies can be found at the beginning of the Electrical Parts List. The assemblies arelisted in numerical order. When the complete component number of a part is known, this list will identify the assembly in which the part is located.

## CROSS INDEX-MFR. CODE NUMBER TO MANUFACTURER

The Mfr. Code Number to Manufacturer index for the Electrical Parts List is located immediately after this page. The Cross Index provides codes, names and addresses of manufacturers of components listed in the Electrical Parts List.

## ABBREVIATIONS <br> Abbreviations conform to American National Standard Y1.1.

## COMPONENT NUMBER (column one of the Electrical Parts List)

A numbering method has been used to identify assemblies, subassemblies and parts. Examples of this numbering method and typical expansions are illustrated by the following:


Read: Resistor 1234 of Assembly 23


Read: Resistor 1234 of Subassembly 2 of Assembly 23

Only the circuit number will appear on the diagrams and circuit board illustrations. Each diagram and circuit board illustration is clearly marked with the assembly number. Assembly numbers are also marked on the mechanical exploded views located in the Mechanical Parts List. The component number is obtained by adding the assembly number prefix to the circuit number.

The Electrical Parts List is divided and arranged by assemblies in numerical sequence (e.g., assembly A1 with its subassemblies and parts, precedes assembly A2 with its subassemblies and parts).

Chassis-mounted parts have no assembly number prefix and are located at the end of the Electrical Parts List.

## TEKTRONIX PART NO. (column two of the Electrical Parts List)

Indicates part number to be used when ordering replacement part from Tektronix.

## SERIAL/MODEL NO. (columns three and four of the Electrical Parts List)

Column three (3) indicates the serial number at which the part was first used. Column four (4) indicates the serial number at which the part was removed. No serial number entered indicates part is good for all serial numbers.

## NAME \& DESCRIPTION (column five of the Electrical Parts List)

In the Parts List, an Item Name is separated from the description by a colon (:). Because of space limitations, an Item Name may sometimes appear as incomplete. For further Item Name identification, the U.S. Federal Cataloging Handbook H6-1 can be utilized where possible.

## MFR. CODE (column six of the Electrical Parts List)

Indicates the code number of the actual manufacturer of the part. (Code to name and address cross reference can be found immediately after this page.)

## MFR. PART NUMBER (column seven of the Electrical Parts List)

Indicates actual manufacturers part number.

CROSS INDEX—MFR. CODE NUMBER TO MANUFACTURER

| Mfr. Code | Manufacturer | Address | City, State, Zip |
| :---: | :---: | :---: | :---: |
| 000Fg | rifa world products inc. | 7625 BUSH LAKE RD <br> P.O. BOX 35263 | MINNEAPOLIS, MN 55435 |
| 00019 | hVC CORP. inc. | 600 SOUTH MILWAUKEE St. | FREDONIA, WI 53021 |
| 00779 | AMP, INC. | Р о box 3608 | HARRISBURG PA 17105 |
| 00853 | Sangamo electric co., s. carolina div. | P 0 box 128 | PICKENS, SC 29671 |
| 01121 | allen-bradley company | 1201 2ND STREET SOUTH | MLLWAUKEE, WI 53204 |
| 01281 | trw electronic components, semiconductor operations | 14520 AVIATION BLVD. | LAWNDALE, CA 90260 |
| 01295 | texas instruments, inc., semiconductor GROUP | P o box 5012 , 13500 N CENTRAL EXPRESSHAY | dallas, TX 75222 |
| 0211 | spectrol electronics corporation | 17070 east gale avenue | CITY OF INDUSTRY, CA 91745 |
| 02113 | COILCRAFT inc. | 1102 SLLVER Lake rd. | CARY, IL 60013 |
| 02114 | Ferroxcube corporation | PO box 359, Marion road | Saugerties, ny 12477 |
| 02735 | rca corporation, Solid state division | Route 202 | SOMERVILLE, NY 08876 |
| 03508 | general electric company, semi-conductor products department | Electronics Park | SYRACUSE, NY 13201 |
| 04222 | avx ceramics, division of avx corp. | P o box 867, 19TH AVE. SOUTH | MYRTLE BEACH, SC 29577 |
| 04713 | MOTOROLA, INC., SEMICONDUCTOR PROD. DIV. | 5005 E MCDOWELL Rd, P0 BOX 20923 | phoenix, az 85036 |
| 05245 | corcom inc. | 2635 n Kildare avenue | Chicago, il 60639 |
| 05347 | ULTRONIX, inc. | 461 N 22ND Street | grand Junction, co 81501 |
| 05828 | general instrument corp electronic systems div. | 600 W JOHN ST. | HICKSVILLE LI, NY 11802 |
| 07263 | fairchild semiconductor, a div. of FAIRCHILD CAMERA AND INSTRUMENT CORP. | 464 ELLIS STREET | MOUNTAIN VIEW, CA 94042 |
| 09969 | dale electronics, inc. | P o box 180, EAST highway 50 | Yankton, SD 57078 |
| 12697 | clarostat mfg co., inc. | LOWER Washington street | DOVER, NH 03820 |
| 12969 | UNITRODE CORPORATION | 580 Pleasant Street | WAtErtown, MA 02172 |
| 13511 | AMPHENOL CARDRE diV., bunker ramo corp. |  | LOS Gatos, CA 95030 |
| 14552 | MICRO SEMICONDUCTOR CORP. | 2830 E fairview St. | Santa ana, Ca 92704 |
| 14752 | Electro cube inc. | 1710 S. DEL MAR AVE. | SAN GABRIEL, CA 91776 |
| 15238 | ITT SEMICONDUCTORS, A DIVISION OF INTER natlonal telephone and telegraph corp. | P.O. BOX 168, 500 BrOADWAY | Lahrence, Ma 01841 |
| 15454 | rodan industries, inc. | 2905 blue star St. | ANAHEIM, CA 92806 |
| 18324 | SIGNETICS CORP. | 811 E. Arques | SUNNYVALE, CA 94086 |
| 19396 | illinois tool horks, inc. Paktron div. | 900 Follin lane, SE | vienna, va 22180 |
| 19701 | electra-midland corp., mepco electra inc. | P 0 box 760 | MINERAL WELLS, TX 76067 |
| 20462 | prem enterprises, inc. | 3519 N. CHAPEL HILL | MCHENRY, IL 60050 |
| 20932 | EMCON DIV Of ILLINOIS TOOL WORKS INC. | 11620 Sorrento valley rd f 0 BOX 81542 | SAN DIEGO, CA 92121 |
| 22229 | Solitron devices, inc |  |  |
|  | SEMICONDUCTOR GROUP | 8808 balboa avenue | San diego opers, Ca 92123 |
| 22526 | berg electronics, inc. | Youk expressway | NEW Cumberland, pa 17070 |
| 24444 | general semiconductor industries inc. | 2001 W 10TH PLACE <br> P.O. BOX 3078 | TEMPE, AZ 85281 |
| 24546 | CORNING Glass works, electronic |  |  |
|  | COMPONENTS DIVISION | 550 high street | BRADFORD, PA 16701 |
| 27014 | NATIONAL SEMICONDUCTOR CORP. | 2900 Semiconductor dr. | SANTA Clara, ca 95051 |
| 31918 | iee/Schadow inc. | 8081 wallace road | eden prairle, mn 55343 |
| 32997 | BOURNS, INC., TRIMPOT PRODUCTS DIV. | 1200 columbia ave. | RIVERSIDE, CA 92507 |
| 50157 | MIDWEST COMPONENTS TNC. | $\begin{aligned} & \text { P. o. BOX } 787 \\ & \text { 1981 PORT CITY BLVD. } \end{aligned}$ | MUSKEGON, MI 49443 |
| 50434 | HEWLETT-PACKARD COMPANY | 640 PAGE MILL ROAD | palo alto, ca 94304 |
| 51642 | Centre engineering inc. | 2820 e college avenue | state colcege, pa 16801 |
| 52306 | high voltage devices, inc. | 7485 aVENUE 304 | visalia, CA 93277 |
| 52769 | Sprague goodman elec., inc. | 134 fulton avenue | garden City Park, wy 11040 |
| 53184 | XCITON CORPORATION | 5 hemlock street | latham, ny 12110 |
| 53944 | Elt inc., Glow lite division | в0X 698 | PaUlS Valley, ok 73075 |
| 54473 | matsushita electric, corp. of america | 1 panasonic way | SECAUCUS, NJ 07094 |
| 54937 | DE YOUNG MFG., inc. | PO box 1806, 1517 130Th ave. | bellevise, Wa 98009 |
| 55210 | CETTIG ENC. AND MFG. COMPANY | po box 85, off route 45 | SPRING MILLS, PA 16875 |
| 55680 | NICHICON/AMERICA/CORP. | 6435 N PROESEL AVENUE | CHICAGO, IL 60645 |
| 56289 | Sprague electric co. | 87 MARSHALL ST. | NORTH ADAMS, MA 01247 |
| 59660 71400 | TUSONIX INC. ${ }_{\text {buSSMAN }}$ MFG., division of mccraw- | 2155 N FORBES BLVD | TUCSON, AZ 85705 |
|  | EDISON CO. ERIE TECHNOLOGICAL PRODUCTS, INC, | 2536 W. UNIVERSITY ST. | ST. LOUIS, MO 63107 |
| 72982 | erie technological products, inc. | 644 W. 12 TH ST. | ERIE, PA 16512 |

## CROSS INDEX—MFR. CODE NUMBER TO MANUFACTURER

| Mfr. Code | Manufacturer | Address | City, State, Zip |
| :---: | :---: | :---: | :---: |
| 73138 | BECKMAN INSTRUMENTS, INC., HELIPOT DIV. | 2500 HARBOR BLVD. | FULLERTON, CA 92634 |
| 73899 | JFD ELECTRONICS COMPONENTS CORP. | Pinetree road | OXFORD, NC 27565 |
| 74970 | JOHNSON, E. F., CO. | 299 10TH AVE. S. W. | WASECA, MN 56093 |
| 75042 | TRW ELECTRONIC COMPONENTS, IRC FIXED RESISTORS, PHILADELPHIA DIVISION | 40L N. BROAD ST. | Philadelphia, PA 19108 |
| 77820 | BENDIX CORP., THE, ELECTRICAL COMPONENTS DIVISION | SHERMAN AVE. | SIDNEY, NY 13838 |
| 80009 | TEKTRONIX, INC. | P O BOX 500 | BEAVERTON, OR 97077 |
| 80031 | ELECTRA-MIDLAND CORP., MEPCO DIV. | 22 COLUMBIA ROAD | MORRISTOWN, NJ 07960 |
| 81483 | International rectifier Corp. | 9220 SUNSET BLVD. | LOS ANGELES, CA 90069 |
| 82389 | SWITCHCRAFT, ING. | 5555 N. ELSTON AVE. | CHICAGO, IL, 60630 |
| 84411 | TRW ELECTRONLC COMPONENTS, TRW CAPACITORS | 112 W . FIRST ST. | OGALLALA, NE 69153 |
| 90201 | MALLORY CAPAGITOR CO., DIV. OF P. R. MALLORY AND CO., INC. | 3029 E. WASHINGTON STREET $\text { P. О. BOX } 372$ | INDIANAPOLIS . IN 46206 |
| 91418 | RADIO MATERIALS COMPANY, DIV. OF P.R. MALLORY AND COMPANY, INC. | 4242 W BRXN MANR | CHICAGO, HL, 60646 |
| 91637 | DALE ELEGTRONICS, INC. | P. O. BOX 609 | COLUMBUS, NE 68601 |
| 99392 | MEMPCO/ELEGTRA INC., ROXBORO DIV. | P O BOX 1223 | ROXBORO, NC 27573 |


| Component No. | Tektronix Part No. | Serial/Model No. Eff Dscont | Name \& Description | Mfr Code | Mir Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A10 | 670-6866-00 | B010100 B019849 | CKT BOARD ASSY:MAIN | 80009 | 670-6866-00 |
| Al0 | 670-6866-01 | B019850 | CKT BOARD ASSY:MAIN | 80009 | 670-6866-01 |
| All | 670-6867-00 | B010100 B019849 | CKT BOARD ASSY: FRONT PANEL | 80009 | 670-6867-00 |
| All | 670-6867-01 | B019850 | CKT BOARD ASSY:FRONT PANEL | 80009 | 670-6867-01 |
| Al2 | 670-6868-00 | B010100 B019849 | CKT BOARD ASSY: ATTEN/SWEEP | 80009 | 670-6868-00 |
| Al2 | 670-6868-01 | B019850 | CKT BOARD ASSY:ATTEN/SWEEP | 80009 | 670-6868-01 |
| Al3 | 670-6869-00 |  | CKT BOARD ASSY:ALTERNATE SWEEP | 80009 | 670-6869-00 |
| Al 8 | 670-7706-00 | XB022000 | CKT BOARD ASSY: PREREGULATOR | 80009 | 670-7706-00 |
| A19 | 670-7498-00 | B010100 B021999X | CKT BOARD ASSY:CURRENT LIMIT | 80009 | 670-7498-00 |
| A10 | 670-6866-00 | B010100 B019849 | CKT BOARD ASSY:MAIN | 80009 | 670-6866-00 |
| A10 | 670-6866-01 | B019850 | CKT BOARD ASSY:MAIN | 80009 | 670-6866-01 |
| A10C167 | 281-0064-00 |  | CAP., VAR, PLSTC: $0.25-1.5 \mathrm{PF}, 600 \mathrm{~V}$ | 74970 | 273-0001-101 |
| A10C170 | 281-0862-00 |  | CAP., FXD, CER DI: $0.001 \mathrm{UF},+80-20 \%, 100 \mathrm{~V}$ | 20932 | 401-ES-100AD102Z |
| Al0c173 | 281-0814-00 |  | CAP., FXD, CER DI: $100 \mathrm{PF}, 10 \%, 100 \mathrm{~V}$ | 04222 | GC70-1-A101K |
| A10C174 | 283-0154-00 | B010100 B011399 | CAP.,FXD, CER DI : $22 \mathrm{PF}, 5 \%, 50 \mathrm{~V}$ | 72982 | 8111B061C0G220J |
| Al0C174 | 281-0759-00 | B011400 | CAP., FXD, CER DI: $22 \mathrm{PF}, 10 \%, 100 \mathrm{~V}$ | 72982 | 8035D9AADC1G220K |
| Al0c175 | 281-0791-00 |  | CAP.,FXD, CER DI: $270 \mathrm{PF}, 10 \%, 100 \mathrm{~V}$ | 72982 | 8035D2AADX5R271K |
| Al0C179 | 281-0823-00 |  | CAP., FXD, CER DI: $470 \mathrm{PF}, 10 \%, 50 \mathrm{~V}$ | 12969 | CGB471KDN |
| Al0C180 | 283-0648-00 |  | CAP., FXD, MICA D: $10 \mathrm{PF}, 5 \%, 100 \mathrm{~V}$ | 00853 | D151C100D0 |
| Al0C185 | 281-0791-00 |  | CAP., FXD, CER DI: $270 \mathrm{PF}, 10 \%, 100 \mathrm{~V}$ | 72982 | 8035D2AADX5R271K |
| Al0C193 | 281-0862-00 |  | CAP.,FXD, CER DI: $0.001 \mathrm{UF},+80-20 \%, 100 \mathrm{~V}$ | 20932 | 401 -ES-100AD1022 |
| Al0C199 | 290-0136-00 |  | CAP., FXD, ELCTLT: 2. 2UF, 20\%, 20V | 56289 | 162D225x0020CD2 |
| Al0C253 | 281-0862-00 |  | CAP. , FXD, CER DI: 0.001 UF, $+80-20 \%, 100 \mathrm{~V}$ | 20932 | 401-ES-100AD1027 |
| A10c 255 | 281-0773-00 |  | CAP., FXD, CER DI: 0.01 UF $, 10 \%, 100 \mathrm{~V}$ | 04222 | GC70-1C103K |
| A10C260 | 281-0773-00 |  | CAP., FXD, CER DI: $0.01 \mathrm{UF}, 10 \%, 100 \mathrm{~V}$ | 04222 | GC70-1C103K |
| A10C 264 | 283-0084-00 |  | CAP., FXD, CER DI : 270PF , 5\%, 1000V | 72982 | 838-533B271J |
| Al0C 265 | 281-0773-00 | XB011400 | CAP., FXD, CER DI: $0.01 \mathrm{UF}, 10 \%, 100 \mathrm{~V}$ | 04222 | GC70-1C103K |
| A10C 270 | 281-0862-00 |  | CAP., FXD, CER DL: $0.001 \mathrm{UF},+80-20 \%, 100 \mathrm{~V}$ | 20932 | 401-ES-100AD102\% |
| A10C 273 | 281-0814-00 |  | CAP., FXD, CER DI: $100 \mathrm{PF}, 10 \%, 100 \mathrm{~V}$ | 04222 | GC70-1-Al01K |
| A10c 275 | 281-0791-00 |  | CAP., FXD, CER DI: $270 \mathrm{PF}, 10 \%, 100 \mathrm{~V}$ | 72982 | 8035D2AADX5R271F |
| Al0c 279 | 281-0823-00 |  | CAP.,FXD, CER DI: $470 \mathrm{PF}, 10 \%, 50 \mathrm{~V}$ | 12969 | CGB471KDN |
| A10C 280 | 283-0648-00 |  | CAP., FXD, MICA D $10 \mathrm{PF}, 5 \%, 100 \mathrm{~V}$ | 00853 | D151C100D0 |
| Al0C 284 | 283-0154-00 | B010100 B011399 | CAP., FXD, CER DI: $22 \mathrm{PF}, 5 \%, 50 \mathrm{~V}$ | 72982 | 81118061C0G220J |
| Al0C284 | 281-0759-00 | B011400 | CAP.,FXD, CER DI: $22 \mathrm{PF}, 10 \%, 100 \mathrm{~V}$ | 72982 | 8035D9AADC1G2201 |
| Al0C 285 | 281-0791-00 |  | CAP., FXD, CER DI: $270 \mathrm{PF}, 10 \%, 100 \mathrm{~V}$ | 72982 | 8035D2AADX5R2711 |
| Al0c 293 | 281-0862-00 |  | CAP.,FXD, CER DI : $0.001 \mathrm{UF},+80-20 \%, 100 \mathrm{~V}$ | 20932 | 401-ES-100AD102: |
| A10C 299 | 290-0136-00 |  | CAP. , FXD, ELCTLT: $2.20 \mathrm{~F}, 20 \%, 20 \mathrm{~V}$ | 56289 | 162D225X0020CD2 |
| A10C304 | 281-0773-00 |  | CAP., FXD, CER DI: $0.01 \mathrm{~F}, 10 \%, 100 \mathrm{~V}$ | 04222 | GC70-1C103K |
| Al0C305 | 290-0167-00 |  | CAP., FXD, ELCTLT : 10UF, $20 \%, 15 \mathrm{~V}$ | 56289 | 150D106x0015B2 |
| A10C 308 | 285-0643-00 |  | CAP., FXD, PLSTC: $0.0047 \mathrm{UF}, 5 \%, 100 \mathrm{~V}$ | 84411 | TEK-180 47251 |
| Al0C310 | 281-0775-00 |  | CAP., FXD, CER DI: $0.1 \mathrm{UF}, 20 \%, 50 \mathrm{~V}$ | 04222 | SA205E104MAA |
| Al0C 311 | 281-0862-00 |  | CAP., FXD, CER DI: 0.001 UF, $+80-20 \%, 100 \mathrm{~V}$ | 20932 | 401-ES-100AD102: |
| Al0c314 | 281-0773-00 | B010100 B017149X | CAP., FXD, CER DI: $0.01 \mathrm{UF}, 10 \%, 100 \mathrm{~V}$ | 04222 | GC70-1C103K |
| Al0c316 | 281-0862-00 |  | CAP., FXD, CER DI: $0.001 \mathrm{UF},+80-20 \%, 100 \mathrm{~V}$ | 20932 | 401-ES-100AD102. |
| Al0c317 | 281-0775-00 |  | CAP.,FXD, CER DI: $0.10 \mathrm{~F}, 20 \%, 50 \mathrm{~V}$ | 04222 | SA205E104MAA |
| A10C335 | 281-0810-00 |  | CAP.,FXD, CER DI: 5.6PF,0.5\%,100V | 04222 | GC10-1A5R6D |
| Al0c 340 | 281-0645-00 |  | CAP., FXD, CER DI: $8.2 \mathrm{PF},+/-0.25 \mathrm{PF}, 500 \mathrm{~V}$ | 59660 | 374018 C0H0829 |
| Al0c345 | 281-0810-00 |  | CAP., FXD, CER DI: $5.6 \mathrm{PF}, 0.5 \%, 100 \mathrm{~V}$ | 04222 | GC 10-1A5R6D |
| A10c350 | 281-0823-00 |  | CAP., FXD, CER DI: $470 \mathrm{PF}, 10 \%, 50 \mathrm{~V}$ | 12969 | CGB471KDN |
| A10c357 | 281-0226-00 |  | CAP., VAR, PLSTC:5.5-65PF, 100 V | 52769 | GXD38000 |
| A10C358 | 281-0767-00 |  | CAP., FXD, CER DI : $330 \mathrm{PF}, 20 \%, 100 \mathrm{~V}$ | 12969 | CGB33IMEX |
| A10c 360 | 281-0823-00 |  | CAP.,FXD, CER DI:470PF, $10 \%, 50 \mathrm{~V}$ | 12969 | CGB471KDN |
| A10c 366 | 281-0234-00 |  | CAP.,VAR, PLSTC:5.5-65PF, 100V | $80031$ | $2810 \mathrm{C} 5 R 565 \mathrm{U} \text { J02F }$ |
| A10C367 | 281-0814-00 |  | CAP.,FXD, CER DI : $100 \mathrm{PF}, 10 \%, 100 \mathrm{~V}$ | 04222 | GC70-1-A101K |
| A10C 368 | 283-0051-00 |  | CAP., FXD, CER DI: $0.0033 \mathrm{UF}, 5 \%, 100 \mathrm{~V}$ | 56289 | 273C12 |
| A10C372 | 281-0862-00 | XB016700 | CAP., FXD, CER DI: 0.001 UF , +80-20\%, 100 V | 20932 | 401-ES-100AD102 |


| Component No. | Tektronix Part No. | Serial/Model No. Eff Dscont | Name \& Description | Mfr <br> Code | Mfr Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A10C374 | 290-0187-00 |  | CAP., FXD, ELCTLT: $4.7 \mathrm{UF}, 20 \%, 35 \mathrm{~V}$ | 56289 | 150D475×0035B2 |
| A10C377 | 283-0348-00 |  | CAP., FXD, CER DI: 0.5PF, +/-0.1PF, 100 V | 51642 | 100-100-NP0-508B |
| A10C387 | 283-0348-00 |  | CAP., FXD, CER DI: $0.5 \mathrm{PF},+/-0.1 \mathrm{PF}, 100 \mathrm{~V}$ | 51642 | 100-100-NP0-508B |
| A10c394 | 281-0775-00 |  | CAP., FXD, CER DI: 0.1UF, 20\%,50V | 04222 | SA205E104MAA |
| A10C397 | 290-0507-00 |  | CAP. , FXD, ELCTLT : $1800 \mathrm{UF},+75-10 \%, 75 \mathrm{~V}$ | 56289 | 68D 10472 |
| A10C399 | 281-0773-00 |  | CAP., FXD, CER DI: 0.01UF, $10 \%, 100 \mathrm{~V}$ | 04222 | CC70-1C103K |
| A10C408 | 281-0808-00 |  | CAP, FXX, CER DI: 7PF, 20\%, 100V | 72982 | 8035D9AADC0G709G |
| A10c410 | 281-0862-00 |  | CAP., FXD, CER DI: 0.001 UF, $+80-20 \%, 100 \mathrm{~V}$ | 20932 | 401-ES-100AD102Z |
| A10C412 | 281-0773-00 |  | CAP., FXD, CER DI: 0.01UF, 10\%, 100V | 04222 | GC70-1C103K |
| A10C417 | 281-0862-00 |  | CAP., FXD, CER DI: 0.001 UF, $+80-20 \%, 100 \mathrm{~V}$ | 20932 | 401-ES-100AD102Z |
| A10C418 | 281-0823-00 |  | CAP., FXD, CER DI: $470 \mathrm{PF}, 10 \%, 50 \mathrm{~V}$ | 12969 | CGB471KDN |
| A10c431 | 281-0773-00 |  | CAP., FXD, CER DI: $0.01 \mathrm{UF}, 10 \%, 100 \mathrm{~V}$ | 04222 | GC70-1C103K |
| Al0C432 | 281-0773-00 |  | CAP., FKD, CER DI:0.01UF,10\%,100V | 04222 | GC70-1C103K |
| A10C433 | 281-0862-00 |  | CAP., FXD, CER DL: $0.001 \mathrm{UF},+80-20 \%, 100 \mathrm{~V}$ | 20932 | 401-ES-100AD102Z |
| A10C437 | 281-0862-00 |  | CAP , FXD, CER DI: $0.001 \mathrm{UF},+80-20 \%, 100 \mathrm{~V}$ | 20932 | 401-ES-100AD102Z |
| Al0c438 | 281-0862-00 |  | CAP. , FXD, CER DI: $0.001 \mathrm{UF},+80-20 \%, 100 \mathrm{~V}$ | 20932 | 401-ES-100AD102Z |
| A10C446 | 281-0547-00 |  | CAP., FXD, CER DI: $2.7 \mathrm{PF}, 10 \%, 500 \mathrm{~V}$ | 04222 | 7001-1321 |
| Al0C447 | 285-1189-00 |  | CAP., FXD, MTLZD: $0.1 \mathrm{UF}, 5 \%, 100 \mathrm{~V}$ | 99392 | C280MAH/J100K |
| Al0C448 | 281-0775-00 |  | CAP., FXD, CER DI: $0.1 \mathrm{LF}, 20 \%, 50 \mathrm{~V}$ | 04222 | SA 205E104MAA |
| A10C454 | 281-0773-00 |  | CAP., FXD, CER DI: $0.01 \mathrm{UF}, 10 \%, 100 \mathrm{~V}$ | 04222 | GC70-1C103K |
| Al0c455 | 281-0862-00 |  | CAP., FXD, CER DI: $0.001 \mathrm{UF},+80-20 \%, 100 \mathrm{~V}$ | 20932 | 401-ES-100AD102Z |
| A10C457 | 281-0773-00 |  | CAP., FXD, CER DI: $0.01 \mathrm{UF}, 10 \%, 100 \mathrm{~V}$ | 04222 | GC70-1C103K |
| A10C458 | 281-0773-00 |  | CAP.,FXD, CER DI:0.01UF, $10 \%, 100 \mathrm{~V}$ | 04222 | GC70-1C103K |
| Al0C476 | 281-0773-00 |  | CAP., FXD, CER DI: $0.01 \mathrm{UF}, 10 \%, 100 \mathrm{~V}$ | 04222 | GC70-1C103K |
| A10C477 | 281-0773-00 |  | CAP., FXD, CER DI: $0.01 \mathrm{UF}, 10 \%, 100 \mathrm{~V}$ | 04222 | GC70-1C103K |
| A10C480 | 281-0773-00 |  | CAP. , FXD, CER DI: $0.01 \mathrm{UF}, 10 \%, 100 \mathrm{~V}$ | 04222 | CC70-1C103K |
| A10C503 | 290-0246-00 |  | CAP., FXD, ELCTLT: 3.3UF, $10 \%$, 15V | 56289 | 162D335x9015CD2 |
| A10C504 | 290-0246-00 |  | CAP., FXD, ELCTLT: 3.3UF, $10 \%$, 15V | 56289 | 162D335X9015CD2 |
| A10C505 | 281-0773-00 |  | CAP., FXD, CER DI: $0.01 \mathrm{UF}, 10 \%, 100 \mathrm{~V}$ | 04222 | GC70-1Cl03K |
| Al0C506 | 283-0177-00 |  | CAP., FXD, CER DI: $1 \mathrm{UF},+80-20 \%, 25 \mathrm{~V}$ | 56289 | $273 \mathrm{C5}$ |
| A10C564 | 281-0773-00 |  | CAP., FXD, CER DI: $0.01 \mathrm{UF}, 10 \%, 100 \mathrm{~V}$ | 04222 | GC70-1Cl03K |
| A10C569 | 281-0773-00 |  | CAP., FXD, CER DI: $0.01 \mathrm{UF}, 10 \%, 100 \mathrm{~V}$ | 04222 | GC70-1C103K |
| Al0C601 | 281-0774-00 |  | CAP. , FXD, CER DI: $0.022 \mathrm{UF}, 20 \%$, 100 V | 12969 | CGE223MEZ |
| A10C602 | 281-0862-00 | B010100 B018549X | CAP. , FXD, CER DI: 0.001 UF , $+80-20 \%, 100 \mathrm{~V}$ | 20932 | 401-ES-100AD102Z |
| Al0C603 | 281-0775-00 |  | CAP., FXD, CER DI:0.1UF, 20\%,50V | 04222 | SA205E104MAA |
| A10C604 | 281-0775-00 |  | CAP., FXD, CER DI: 0.1UF, 20\%, 50V | 04222 | SA205E104MAA |
| A10C605 | 281-0775-00 |  | CAP., FXD, CER DI: 0.1UF, 20\%,50V | 04222 | SA205E104MAA |
| A10C606 | 281-0862-00 | B010100 B018549x | CAP., FXD, CER DL: 0.001 UF, $+80-20 \%, 100 \mathrm{~V}$ | 20932 | 401-ES-100AD102Z |
| A10C606 | 281-0862-00 | XB020500 | CAP. , FXD, CER DI: $0.001 \mathrm{UF},+80-20 \%, 100 \mathrm{~V}$ | 20932 | 401-ES-1 00AD 02 Z |
| A10C607 | 281-0862-00 | B010100 B018549X | CAP. , FXD, CER DI: $0.001 \mathrm{UF},+80-20 \%, 100 \mathrm{~V}$ | 20932 | 401-ES-100AD102Z |
| A10C608 | 281-0775-00 |  | CAP., FXD, CER DI: 0.1UF, $20 \%, 50 \mathrm{~V}$ | 04222 | SA205E104MAA |
| A10C610 | 281-0775-00 |  | CAP., FXD, CER DI: 0.1UF, $20 \%, 50 \mathrm{~V}$ | 04222 | SA205E104MAA |
| A10C614 | 290-0135-00 |  | CAP., FXD, ELCTLT: 15UF, 20\%, 20V | 56289 | $150 \mathrm{D} 156 \times 0020 \mathrm{~B} 2$ |
| A10C618 | 281-0773-00 |  | CAP., FXD, CER DI: $0.01 \mathrm{UF}, 10 \%, 100 \mathrm{~V}$ | 04222 | GC70-1C103K |
| A10C619 | 281-0791-00 | B010100 B011229X | CAP., FXD, CER DI: 270 PF, $10 \%, 100 \mathrm{~V}$ | 72982 | 8035D2AADX5R271K |
| A10C628A, B | 295-0138-00 | B010100 B020949 | CAP. SET, MATCHED: 1UF,0.01UF, 1\%, OA RANGE 3\% | 80009 | 295-0138-00 |
| A10C628A, B | 295-0138-01 | B020950 | CAP SET, MATCHED: 1UF,0.01UF, 1\%, 0A RANGE 3\% | 80009 | 295-0138-01 |
| Al0C637 | 281-0810-00 |  | CAP., FXD, CER DI: 5.6PF,0.5\%, 100V | 04222 | GC10-1A5R6D |
| A10C640 | 281-0775-00 |  | CAP., FXD, CER DI: 0.1UF, 20\%, 50V | 04222 | SA205E104MAA |
| A10c642 | 281-0770-00 |  | CAP., FXD, CER DI: $0.001 \mathrm{UF}, 20 \%, 100 \mathrm{~V}$ | 72982 | 8035D9AADX5R102M |
| A10C644 | 281-0770-00 |  | CAP., FXD, CER DI: $0.001 \mathrm{UF}, 20 \%, 100 \mathrm{~V}$ | 72982 | 8035D9AADX5R102M |
| A10C645 | 290-0167-00 |  | CAP., FXD, ELCTLT: $10 \mathrm{UF}, 20 \%, 15 \mathrm{~V}$ | 56289 | 150D 106X0015B2 |
| A10C646 | 281-0775-00 |  | CAP., FXD, CER DI: $0.1 \mathrm{UF}, 20 \%, 50 \mathrm{~V}$ | 04222 | SA20.5E104MAA |
| A10C647 | 281-0772-00 |  | CAP., FXD, CER DI: $0.0047 \mathrm{JF}, 10 \%, 100 \mathrm{~V}$ | 04222 | GC701C472K |
| A10C648 | 281-0773-00 |  | CAP.,FXD, CER DI: $0.01 \mathrm{UF}, 10 \%, 100 \mathrm{~V}$ | 04222 | GC70-1C103K |
| A10C651 | 281-0773-00 |  | CAP.,FXD, CER DI: $0.01 \mathrm{UF}, 10 \%, 100 \mathrm{~V}$ | 04222 | Gc70-1 C103K |
| A10C658 | 290-0745-00 | B010100 B010684X | CAP., FXD, ELCTLT: 22UF,+50-10\%, 25V | 56289 | 502D225 |

## Replaceable Electrical Parts-2215 Service

| Component No. | Tektronix Part No. | Serial/Model No. Eff Dscont | Name \& Description | Mfr Code | Mfr Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Al0C668 | 281-0814-00 |  | CAP., FXD, CER DI: $100 \mathrm{PF}, 10 \%, 100 \mathrm{~V}$ | 04222 | GC70-1-Al01K |
| A10C678 | 281-0773-00 |  | CAP., FXD, CER DK:0.01UF, $10 \%, 100 \mathrm{~V}$ | 04222 | GC70-1C103K |
| Al0C702 | 281-0775-00 |  | CAP., FXD, CER DI: 0.1 l | 04222 | SA205E104MAA |
| Al0C708 | 281-0592-00 |  | CAP.,FXD, CER DI:4.7PF, +/-0.5PF,500V | 59660 | 301-000-C0H0479D |
| A10C725 | 290-0745-00 |  | CAP. , FXD, ELCTLT : 22UF, +50-10\%, 25V | 56289 | 502D225 |
| A10C745 | 281-0775-00 |  | CAP., FXD, CER DI: $0.1 \mathrm{FF}, 20 \%, 50 \mathrm{~V}$ | 04222 | SA205E104MAA |
| A10C748 | 281-0775-00 |  | CAP.,FXD, CER DI: $0.1 \mathrm{UF}, 20 \%, 50 \mathrm{~V}$ | 04222 | SA205E 104MAA |
| A10c754 | 281-0158-00 |  | CAP.,VAR, CER D1: 7-45PF,50V | 73899 | DVJ-5006 |
| A10C770 | 283-0198-00 |  | CAP., FXD, CER DI:0.22UF, $20 \%$, 50 V | 72982 | $8121 \mathrm{N083Z5U0224M}$ |
| A10c773 | 283-0158-00 |  | CAP., FXD, CER DI: $1 \mathrm{PF}, 10 \%, 50 \mathrm{~V}$ | 51642 | 100-050-NP0-109B |
| A10C774 | 281-0214-00 |  | CAP., VAR, CER DI: $0.5-3 \mathrm{PF}, 400 \mathrm{~V}$ | 80031 | 2502A0R503VP02F0 |
| A 10C777 | 281-0771-00 |  | CAP.,FXD, CER DI: $0.0022 \mathrm{UF}, 20 \%, 200 \mathrm{~V}$ | 56289 | 292C 25U222m200B |
| A10C779 | 285-1101-00 |  | CAP. , FXD , PLSTC: $0.022 \mathrm{UF}, 10 \%, 200 \mathrm{~V}$ | 19396 | 223K02PT485 |
| A10C781 | 281-0775-00 |  | CAP.,FXD, CER DI: $0.1 \mathrm{UF}, 20 \%, 50 \mathrm{~V}$ | 04222 | SA205E104MAA |
| Al0C783 | 283-0158-00 |  | CAP., FXD, CER DI: 1PF, $10 \%, 50 \mathrm{~V}$ | 51642 | 100-050-NPO-109B |
| A10C784 | 281-0214-00 |  | CAP., VAR, CER DI: $0.5-3 \mathrm{PF}, 400 \mathrm{~V}$ | 80031 | 2502A0R503VP02F0 |
| A10C787 | 281-0771-00 |  | CAP., FXD, CER DI: $0.0022 \mathrm{JF}, 20 \%, 200 \mathrm{~V}$ | 56289 | 292C Z5U222M200B |
| A10C789 | 285-1101-00 |  | CAP., FXD, PLSTC: $0.022 \mathrm{UF}, 10 \%, 200 \mathrm{~V}$ | 19396 | 223K02PT485 |
| A10C796 | 281-0775-00 |  | CAP., FXD, CER DI: $0.1 \mathrm{UF}, 20 \%$, 50V | 04222 | SA205E104MAA |
| A10C797 | 281-0775-00 |  | CAP., FXI, CER DI: $0.1 \mathrm{UF}, 20 \%, 50 \mathrm{~V}$ | 04222 | SA 205E104MAA |
| Al0C798 | 281-0775-00 |  | CAP., FXD, CER DI: 0.1 l ( ${ }^{\text {c }}$, 20\%, 50V | 04222 | SA205E104MAA |
| A10C799 | 283-0057-00 |  | CAP., FXD, CER DI: 0.1 l | 56289 | 2C2025U104Z200B |
| A10C803 | 281-0820-00 | B010100 B018549 | CAP.,FXD, CER DI: 680PF, $10 \%$, 50 V | 12969 | CGB681 KDX |
| Al0C803 | 281-0791-00 | B018550 | CAP., FXD, CER DI: $270 \mathrm{PF}, 10 \%, 100 \mathrm{~V}$ | 72982 | 8035D2AADX5R271K |
| A10C810 | 281-0773-00 | XB010685 | CAP., FXD, CER DI: $0.01 \mathrm{UF}, 10 \%, 100 \mathrm{~V}$ | 04222 | GC70-1C103K |
| Al0C820 | 281-0773-00 |  | CAP.,FXD, CER DI: $0.01 \mathrm{UF}, 10 \%, 100 \mathrm{~V}$ | 04222 | GC70-1C103K |
| A10C821 | 290-0183-00 |  | CAP., FXD, ELCTLT: 1UF, $10 \%, 35 \mathrm{~V}$ | 90201 | TAC105K035P02 |
| A10C822 | 281-0775-00 |  | CAP.,FXD, CER DI: $0.1 \mathrm{UF}, 20 \%, 50 \mathrm{~V}$ | 04222 | SA205E104MAA |
| A10C824 | 281-0773-00 |  | CAP., FXD, CER DI: $0.01 \mathrm{UF}, 10 \%, 100 \mathrm{~V}$ | 04222 | GC70-1C103K |
| Al0C825 | 290-0183-00 |  | CAP., FXD, ELCTLT: 1UF, $10 \%, 35 \mathrm{~V}$ | 90201 | TAC105K035P02 |
| A10C834 | 281-0756-00 |  | CAP., FXD, CER DI: 2. 2 PF, $0.5 \%, 200 \mathrm{~V}$ | 12969 | CGB2R2DFN |
| A10C836 | 281-0773-00 |  | CAP., FXD, CER DI : $0.01 \mathrm{UF}, 10 \%, 100 \mathrm{~V}$ | 04222 | GC70-1C103K |
| A 10c840 | 281-0775-00 |  | CAP., FXD, CER DI: $0.1 \mathrm{UF}, 20 \%, 50 \mathrm{~V}$ | 04222 | SA205E104MAA |
| Al0C841 | 281-0773-00 |  | CAP., FXD, CER DI: $0.01 \mathrm{UF}, 10 \%, 100 \mathrm{~V}$ | 04222 | GC70-1 Cl03K |
| A10C842 | 281-0775-00 |  | CAP., FXD, CER DI: $0.1 \mathrm{UF}, 20 \%, 50 \mathrm{~V}$ | 04222 | SA205E104MAA |
| Al0C844 | 281-0862-00 |  | CAP., FXD, CER DI $0.001 \mathrm{UF},+80-20 \%, 100 \mathrm{~V}$ | 20932 | 401-ES-100AD1022 |
| Al0C845 | 281-0775-00 |  | CAP.,FXD, CER DI:0.1UF, $20 \%, 50 \mathrm{~V}$ | 04222 | SA205E 104MAA |
| A10C847 | 281-0775-00 |  | CAP., FXD, CER DI: $0.1 \mathrm{UF}, 20 \%, 50 \mathrm{~V}$ | 04222 | SA205E104MAA |
| A10C848 | 281-0775-00 |  | CAP., FXD, CER DI: $0.1 \mathrm{UF}, 20 \%, 50 \mathrm{~V}$ | 04222 | SA205E104MAA |
| A10C849 | 283-0057-00 |  | CAP., FXD, CER DI: 0.1 l , $+880-20 \%, 200 \mathrm{~V}$ | 56289 | 2C20Z5U104Z200B |
| A10C852 | 283-0057-00 |  | CAP., FXD, CER DI: 0.1 l , $+80-20 \%, 200 \mathrm{~V}$ | 56289 | 2C20250104Z200B |
| Al0C854 | 283-0057-00 |  | CAP., FXD, CER DI $: 0.1 \mathrm{l}$, +80-20\%, 200 V | 56289 | 2C2025U1042200B |
| A10C861 | 283-0057-00 |  | CAP., FXD, CER DI: 0.1 l | 56289 | 2C20Z5U1042200B |
| A10C863 | 281-0791-00 |  | CAP., FXD, CER DI: $270 \mathrm{PF}, 10 \%, 100 \mathrm{~V}$ | 72982 | 8035D2AADX5R271K |
| A10C864 | 283-0279-00 |  | CAP., FXD, CER DI: $0.001 \mathrm{UF}, 20 \%, 3000 \mathrm{~V}$ | 59660 | 878-530 Y5S0102M |
| A10C865 | 283-0430-00 |  | CAP., FXD, CER DI $0.02 \mathrm{UF},+80-20 \%, 3000 \mathrm{~V}$ | 0001Q | HV0309 |
| A10c871 | 283-0057-00 |  | CAP., FXD, CER DI: $0.1 \mathrm{UF},+80-20 \%, 200 \mathrm{~V}$ | 56289 | 2C20Z5U104Z200B |
| A10C873 | 283-0057-00 |  | CAP. ,FXD, CER DI: $0.1 \mathrm{UF},+80-20 \%, 200 \mathrm{~V}$ | 56289 | 2C2025U1042200B |
| Al0C876 | 283-0057-00 |  | CAP., FXD, CER DI:0.1UF, $+80-20 \%, 200 \mathrm{~V}$ | 56289 | 2C20Z5U1042200B |
| A10C877 | 283-0057-00 |  | CAP., FXD, CER DI: $0.1 \mathrm{UF},+80-20 \%, 200 \mathrm{~V}$ | 56289 | 2C20Z5U104Z200B |
| A10C878 | 283-0109-00 |  | CAP., FXX, CER DI: $27 \mathrm{PF}, 5 \%, 1000 \mathrm{~V}$ | 56289 | 200376 |
| Al0C879 | 283-0109-00 | - | CAP., FXD, CER DI: $27 \mathrm{PF}, 5 \%, 1000 \mathrm{~V}$ | 56289 | 20c376 |
| A10c886 | 283-0057-00 |  | CAP., FXD, CER DI: $0.1 \mathrm{UF},+80-20 \%, 200 \mathrm{~V}$ | 56289 | 2C20Z5U104Z200B |
| Al0C901 | 285-1 196-00 |  | CAP., FXD, PAPER:0.01UF, $20 \%, 250 \mathrm{~V}$ | 84411 | PME 271 Y 510 |
| A10C912 | 281-0770-00 | B010100 B021999X | CAP., FXD, CER DI:0.001UF, $20 \%$, 100V | 72982 | 8035D9AADX5R102F |
| A10C915 | 290-0188-00 | B010100 B021999X | CAP., FXD, ELCTLT $: 0.1 \mathrm{UF}, 10 \%, 35 \mathrm{~V}$ | 56289 | 162D104X9035BC2 |
| Al0C917 | 290-0808-00 | B010100 B021999X | CAP., FXD, ELCTLT: 2.7UF, 10\%,20V | 56289 | $162 \mathrm{D} 275 \times 9020 \mathrm{CD} 2$ |


| Component No. | Tektronix Part No. | $\begin{aligned} & \text { Serial/Mo } \\ & \text { Eff } \end{aligned}$ | del No. Dscont | Name \& Description | Mrr Code | Mfr Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10C926 | 285-1222-00 | B010100 | B021999X | CAP.,FXD, PLSTC: $0.068 \mathrm{UF}, 20 \%, 250 \mathrm{~V}$ | 000FG | PME271M568 |
| A10c937 | 290-0507-00 | B010100 | B021999 | CAP., FXD, ELCTLT: 1800UF, +75-10\%, 75 V | 56289 | 68 D 10472 |
| A10C937 | 290-0831-00 | B022000 |  | CAP., FXD, ELCTLT: $470 \mathrm{UF},+50-10 \%, 50 \mathrm{~V}$ | 55680 | 50 ULA470 |
| A10C945 | 290-0766-00 | XB011000 |  | CAP., FXD, ELCTLT: $2.2 \mathrm{UF},+50-10 \%, 160 \mathrm{~V}$ | 54473 | ECEA2CS 2R2 |
| A10C947 | 290-0972-00 | X8012543 |  | CAP. , FXD, ELCTLT : 33UF , 20\%, 50VDC | 55680 | TLBlH330M |
| Al0C951 | 290-0768-00 |  |  | CAP. , FXD, ELCTLT: $10 \mathrm{UF},+50-10 \%, 100 \mathrm{~V}$ | 54473 | ECE-A100V10L |
| Al0C956 | 281-0775-00 |  |  | CAP., FXD, CER DI: $0.1 \mathrm{UF}, 20 \%, 50 \mathrm{~V}$ | 04222 | SA205E104MAA |
| Al0C957 | 290-0183-00 |  |  | CAP., FXD, ELCTLT: $1 \mathrm{CF}, 10 \%, 35 \mathrm{~V}$ | 90201 | TAC105K035P02 |
| Al0C961 | 290-0947-00 |  |  | CAP. , FXD, ELCTLT : 33UF, +50-10\%, 160V | 55680 | $160 \mathrm{UHU} 33 \mathrm{VB}-\mathrm{T}$ |
| Al0C965 | 290-0946-00 |  |  | CAP.,FXD, ELCTLT: $270 \mathrm{UF}, 10+100 \%, 40 \mathrm{~V}$ | 90201 | VPR271N040E1E1C |
| Al0C971 | 290-0945-00 |  |  | CAP., FXD, ELCTLT : $840 \mathrm{UF}, 10+100 \%, 12 \mathrm{~V}$ | 90201 | VPR84lN012EIElC |
| A10c972 | 290-0945-00 |  |  | CAP., FXD, ELCTLT : 840UF, $10+100 \%, 12 \mathrm{~V}$ | 90201 | VPR841N012ElElC |
| A10C975 | 290-0945-00 |  |  | CAP . , FXD, ELCTLT : 840UF , 10+100\%, 12V | 90201 | VPR841N012E1E1C |
| Al0C976 | 290-0945-00 |  |  | CAP., FXD, ELCTLT : 840UF, $10+100 \%, 12 \mathrm{~V}$ | 90201 | VPR841N012E1E1C |
| A10C977 | 281-0771-00 |  |  | CAP.,FXD, CER DI:0.0022UF, 20\%, 200V | 56289 | 292C Z5U222M200B |
| A10C985 | 290-0945-00 |  |  | CAP. , FXD, ELCTLT : 840UF, $10+100 \%$, 12V | 90201 | VPR841N012E1E1C |
| Al0C990 | 283-0430-00 | B010100 | B018549 | CAP., FXD, CER DI: $0.02 \mathrm{UF},+80-20 \%, 3000 \mathrm{~V}$ | $0001 Q$ | HV0309 |
| A10C990 | 285-1184-00 | B018550 |  | CAP., FXD, MTLZD: $0.01 \mathrm{UF}, 20 \%, 4000 \mathrm{~V}$ | 84411 | TEK-183103040 |
| A10C992 | 283-0430-00 | B010100 | B019849 | CAP., FXD, CER DI: $0.02 \mathrm{UF},+80-20 \%, 3000 \mathrm{~V}$ | 0001Q | HV0309 |
| A10C992 | 285-1184-00 | B019850 |  | CAP., FXD, MTLZD: 0.01 UF, $20 \%, 4000 \mathrm{~V}$ | 84411 | TEK-183103040 |
| Al0C995 | 283-0430-00 | B010100 | 8018549 | CAP., FXD, CER DI: $0.02 \mathrm{UF},+80-20 \%, 3000 \mathrm{~V}$ | 0001Q | HV0309 |
| A10C995 | 285-1184-00 | B018550 |  | CAP., FXD, MTLZD: 0.01 UF, $20 \%, 4000 \mathrm{~V}$ | 84411 | TEK-183103040 |
| Al0CR177 | 152-0141-02 |  |  | SEMICOND DEVICE:STLICON, 30V, 150 MA | 01295 | 1N4152R |
| Al0CR178 | 152-0141-02 |  |  | SEMICOND DEVICE: SILICON, 30V, 150MA | 01295 | 1N4152R |
| Al0CR187 | 152-0141-02 |  |  | SEMICOND DEVICE:SILICON, 30V, 150MA | 01295 | 1N4152R |
| Al0CR 188 | 152-0141-02 |  |  | SEMICOND DEVICE: SILICON, $30 \mathrm{~V}, 150 \mathrm{MA}$ | 01295 | 1N4152R |
| Al0CR 196 | 152-0141-02 |  |  | SEMICOND DEVICE: SILICON, 30V, 150MA | 01295 | 1 N 4152 R |
| A 10 CR 277 | 152-0141-02 |  |  | SEMICOND DEVICE:SILICON, 30V, 150 MA | 01295 | 1N4152R |
| A10CR278 | 152-0141-02 |  |  | SEMICOND DEVICE: SILICON, 30V,150MA | 01295 | 1N4152R |
| A10CR287 | 152-0141-02 |  |  | SEMICOND DEVICE: SILICON, 30V, 150MA | 01295 | 1N4152R |
| A10CR288 | 152-0141-02 |  |  | SEMICOND DEVICE:SILICON, 30V, 150 MA | 01295 | 1N4152R |
| A10CR296 | 152-0141-02 |  |  | SEMICOND DEVICE:SILICON, 30V,150MA | 01295 | 1N4152R |
| Al0CR 305 | 152-0141-02 |  |  | SEMICOND DEVICE: SILICON, 30V, 150MA | 01295 | 1 N 4152 R |
| Al0Cr 320 | 152-0141-02 |  |  | SEMICOND DEVICE:SILICON, 30V, 150 MA | 01295 | 1N4152R |
| A 10 CR 409 | 152-0141-02 |  |  | SEMICOND DEVICE:SILICON, 30V, 150MA | 01295 | 1N4152R |
| Al0CR418 | 152-0141-02 |  |  | SEMICOND DEVICE:SILICON, 30V, 150 MA | 01295 | 1N4152R |
| A10CR440 | 152-0141-02 |  |  | SEMICOND DEVICE:SILICON, 30V, 150MA | 01295 | 1N4152R |
| A10CR444 | 152-0141-02 |  |  | SEMICOND DEVICE: SILICON, 30V, 150 MA | 01295 | 1N4152R |
| A10CR448 | 152-0141-02 |  |  | SEMICOND DEVICE:SILICON, 30V, 150 MA | 01295 | 1N4152R |
| A10CR503 | 152-0141-02 |  |  | SEMICOND DEVICE:SILICON, 30V,150MA | 01295 | 1N4152R |
| A10CR 504 | 152-0141-02 |  |  | SEMICOND DEVICE:SILICON, $30 \mathrm{~V}, 150 \mathrm{MA}$ | 01295 | 1 N4152R |
| Al0CR610 | 152-0141-02 |  |  | SEMICOND DEVICE: SILICON, 30V, 150MA | 01295 | 1N4152R |
| Al0CR611 | 152-0141-02 |  |  | SEMICOND DEVICE: SILICON, 30V, 150MA | 01295 | 1N4152R |
| Al0Cr615 | 152-0141-02 |  |  | SEMICOND DEVICE:SILICON, 30V, 150 MA | 01295 | 1 N 4152 R |
| Al0CR620 | 152-0141-02 |  |  | SEMICOND DEVICE: SILICON, 30V, 150MA | 01295 | 1N4152R |
| A10CR622 | 152-0141-02 |  |  | SEMICOND DEVICE: SILICON, 30V, 150 MA | 01295 | 1N4152R |
| Al0CR640 | 152-0141-02 |  |  | SEMICOND DEVICE: SILICON, $30 \mathrm{~V}, 150 \mathrm{MA}$ | 01295 | 1N4152R |
| Al0CR644 | 152-0141-02 |  |  | SEMICOND DEVICE: SILICON, 30V, 150MA | 01295 | 1N4152R |
| Al0CR704 | 152-0141-02 |  |  | SEMICOND DEVICE:SILICON, 30V, 150 MA | 01295 | 1N4152R |
| Al0CR745 | 152-0141-02 |  |  | SEMICOND DEVICE:SILICON, 30V,150MA | 01295 | 1N4152R |
| Al0CR 748 | 152-0141-02 |  |  | SEMICOND DEVICE:SILICON, $30 \mathrm{~V}, 150 \mathrm{MA}$ | 01295 | 1N4152R |
| Al0CR749 | 152-0141-02 |  |  | SEMICOND DEVICE: SILICON, 30V, 150MA | 01295 | 1N4152R |
| A10CR770 | 152-0141-02 |  |  | SEMICOND DEVICE:SILICON, 30V, 150MA | 01295 | 1N4152R |
| Al0CR772 | 152-0322-00 |  |  | SEMICOND DEVICE:SILICON, 15 V , HOT CARRIER | 50434 | 5082-2672 |
| A10CR 773 | 152-0141-02 |  |  | SEMICOND DEVICE: SILICON, 30V, 150 MA | 01295 | 1N4152R |
| Al0CR 780 | 152-0141-02 |  |  | SEMICOND DEVICE:SILICON, 30V, 150MA | 01295 | 1N4152R |
| A 10CR 782 | 152-0322-00 |  |  | SEMICOND DEVICE:SILICON,15V,HOT CARRIER | 50434 | -5082-2672 |


| Component No. | Tektronix Part No. | Serial/Model No. <br> Eff Dscont | Name \& Description | Mfr <br> Code | Mfr Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A10CR 783 | 152-0141-02 |  | SEMICOND DEVICE: SILICON, 30V, 150MA | 01295 | 1N4152R |
| A10CR801 | 152-0141-02 |  | SEMICOND DEVICE:SILICON, 30V,150MA | 01295 | 1N4152R |
| A10CR802 | 152-0141-02 |  | SEMICOND DEVICE:SILICON, 30V, 150 MA | 01295 | 1N4152R |
| A10CR809 | 152-0141-02 |  | SEMICOND DEVICE:SILICON, 30V,150MA | 01295 | 1N4152R |
| A10CR828 | 152-0141-02 |  | SEMICOND DEVICE:SILICON, 30V,150MA | 01295 | 1N4152R |
| A10CR830 | 152-0141-02 |  | SEMICOND DEVICE: SILICON, 30V, 150MA | 01295 | 1N4152R |
| A10CR 833 | 152-0141-02 |  | SEMICOND DEVICE:SILICON,30V,150MA | 01295 | 1N4152R |
| A10CR834 | 152-0246-00 |  | SEMICOND DEVICE:SW,SI, 40V, 200MA | 03508 | DE140 |
| A10CR837 | 152-0141-02 |  | SEMICOND DEVICE:SILICON, 30V, 150MA | 01295 | 1N4152R |
| A 10 CR 844 | 152-0141-02 |  | SEMICOND DEVICE:SILICON, 30V,150MA | 01295 | 1N4152R |
| A10CR856 | 152-0242-00 |  | SEMICOND DEVICE: SILICON, $225 \mathrm{~V}, 200 \mathrm{MA}$ | 07263 | FDH5004 |
| A10CR860 | 152-0242-00 |  | SEMICOND DEVICE:SILICON, 225V, 200MA | 07263 | FDH5004 |
| A10CR863 | 152-0242-00 |  | SEMICOND DEVICE:SILICON, 225V, 200MA | 07263 | FDH5004 |
| A10CR867 | 152-0242-00 |  | SEMICOND DEVICE: SILICON, 225V, 200MA | 07263 | FDH5004 |
| AlOCR 868 | 152-0242-00 |  | SEMICOND DEVICE: SILICON, 225V, 200MA | 07263 | FDH5004 |
| Al0CR903 | 152-0040-00 | B010100 B021199X | SEMICOND DEVICE:SILICON, 600V,1A | 15238 | LG109 |
| A10CR904 | 152-0040-00 | B010100 B021199X | SEMICOND DEVICE:SILICON, 600V,1A | 15238 | LG109 |
| A10CR905 | 152-0040-00 | B010100 B021199X | SEMICOND DEVICE:SILICON, 600V, 1A | 15238 | LG109 |
| A10CR906 | 152-0040-00 | B010100 B021199X | SEMICOND DEVICE:SILICON, 600V, 1 A | 15238 | LG109 |
| Al0CR917 | 152-0141-02 | B010100 B021199X | SEMICOND DEVICE:SILICON, 30V,150MA | 01295 | 1N4152R |
| A 10CR931 | 152-0782-00 | B010100 B021199X | SEMICOND DEVICE: RECTIFIER, SILIICON, 600V | 05828 | GP20J-009 |
| A10CR933 | 152-0782-00 | B010100 B021199X | SEMICOND DEVICE: RECTIFIER, SILICON, 600V | 05828 | GP20J-009 |
| A 10 CR 940 | 152-0414-00 |  | SEMICOND DEVICE: SILICON, 200V,0.75A | 12969 | UTR308 |
| A10CR942 | 152-0414-00 |  | SEMICOND DEVICE: SILICON, 200V,0.75A | 12969 | UTR308 |
| Al0CR956 | 152-0141-02 |  | SEMICOND DEVICE:SILICON, 30V, 150MA | 01295 | 1N4152R |
| A 10 CR 961 | 152-0413-00 |  | SEMICOND DEVICE:SILICON, 400V, 750 MA | 12969 | UTR307 |
| A 10 CR 963 | 152-0413-00 |  | SEMICOND DEVICE:SILICON, 400V, 750 MA | 12969 | UTR 307 |
| A10CR 965 | 152-0414-00 |  | SEMICOND DEVICE:SILICON, 200v,0.75A | 12969 | UTR308 |
| A10CR 967 | 152-0414-00 |  | SEMICOND DEVICE: SILICON, 200V, 0.75 A | 12969 | UTR308 |
| A10CR971 | 152-0414-00 |  | SEMICOND DEVICE:SILICON, 200V,0.75A | 12969 | UTR 308 |
| A10CR972 | 152-0414-00 |  | SEMICOND DEVICE:SILICON, 200V,0.75A | 12969 | UTR308 |
| Al0CR973 | 152-0414-00 |  | SEMICOND DEVICE:SILICON, 200V,0.75A | 12969 | UTR308 |
| A10CR974 | 152-0414-00 |  | SEMICOND DEVICE:SILICON, 200V,0.75A | 12969 | UTR308 |
| Al0CR977 | 152-0413-00 |  | SEMICOND DEVICE:SILICON, 400V,750MA | 12969 | UTR307 |
| A10CR 985 | 152-0040-00 |  | SEMICOND DEVICE:SILICON, 600V, 1 A | 15238 | LG109 |
| A10DS854 | 150-0035-00 |  | LAMP, GLOW : $90 \mathrm{~V}, 0.3 \mathrm{MA}$ | 53944 | A1B-3 |
| A10DS856 | 150-0035-00 |  | LAMP, GLOW : 90V,0.3MA | 53944 | AlB-3 |
| A10DS867 | 150-0035-00 |  | LAMP, GLOW: 90V, 0. 3MA | 53944 | AlB-3 |
| Al0DS868 | 150-0035-00 |  | LAMP, GLOW: 90V, 0. 3MA | 53944 | AlB-3 |
| A l0DS870 | 150-0035-00 |  | LAMP, GLOW: 90V, 0. 3MA | 53944 | AlB-3 |
| Al0E199 | 276-0532-00 |  | SHIELDING BEAD, | $02114$ | $56-590-65 / 4 \mathrm{~A} 6$ |
| A10E299 | 276-0532-00 |  | SHIELDINC BEAD, | 02114 | 56-590-65/4A6 |
| A10L971 | 108-1058-00 |  | COIL, RF : FIXED, 10UH | 02113 | B8724 |
| A10L972 | $108-1058-00$ |  | COIL, RF:FIXED, 10UH | $02113$ | B8724 47357 |
| A10P1011 | 131-0608-00 | B010100 B010417X | TERMINAL, PIN: 0.365 L X 0.025 PH BRZ GOLD (QTY 4) | 22526 | 47357 |
| A10P2011 | 131-0608-00 | B010100 B010417X | TERMINAL, PIN: $0.365 \mathrm{~L} X 0.025 \mathrm{PH}$ BRZ GOLD (QTY 4) | 22526 | 47357 |
| A 10 P 6001 | 131-0608-00 | B010100 B010417X | TERMINAL, PIN:0.365 L X 0.025 PH BRZ GOLD (QTY 10) | 22526 | 47357 |
| Al0P7001 | 131-0608-00 | B010100 B010417X | TERMINAL, PIN: $0.365 \mathrm{~L} \times 0.025$ PH BRZ GOLD (QTY 7) | 22526 | 47357 |
| A10P7055 | 131-0608-00 |  | TERMINAL, PIN: $0.365 \mathrm{~L} \times 0.025 \mathrm{PH}$ BRZ GOLD (QTY 3) | 22526 | 47357 |
| A10P9000 | 131-1048-00 |  | TERM.QIK DISC:CKT BD MT,0.11 X 0.02 (QTY 2) | 00779 | 9 61134-1 |
| A10P902.5 | 131-1048-00 | B010100 B021199X | TERM.QIK DISC:CKT BD MT, 0.11 X 0.02 (QTY 2) | 00779 | 9 61134-1 |


| Component No. | Tektronix Part No. | Serial/Model No. Eff Dscont | Name \& Description | IVIII <br> Code | Mfr Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Al0Q157 | 151-0712-00 |  | TRANSISTOR:SILICON,NPN | 04713 | SPS8223 |
| A10Q167 | 151-0712-00 |  | TRANSISTOR:SILICON, NPN | 04713 | SPS8223 |
| A10Q173 | 151-0188-00 |  | TRANSISTOR: SILICON, PNP | 04713 | SPS6868K |
| A10Q177 | 151-0712-00 |  | TRANSISTOR: SILICON,NPN | 04713 | SPS8223 |
| A10Q187 | 151-0712-00 |  | TRANSISTOR:SILICON,NPN | 04713 | SPS8223 |
| A10Q257 | 151-0712-00 |  | TRANSISTOR:SILICON, NPN | 04713 | SPS8223 |
| Al0Q258 | 151-0712-00 |  | TRANSISTOR: SILICON,NPN | 04713 | SPS8223 |
| A10Q267 | 151-0712-00 |  | TRANSISTOR: SILICON, NPN | 04713 | SPS8223 |
| A10Q268 | 151-0712-00 |  | TRANSISTOR: SILICON, NPN | 04713 | SPS8223 |
| A10Q273 | 151-0188-00 |  | TRANSISTOR:SILICON, PNP | 04713 | SPS6868K |
| A10Q277 | 151-0712-00 |  | TRANSISTOR:SILICON, NPN | 04713 | SPS8223 |
| A10Q287 | 151-0712-00 |  | TRANSISTOR:SILICON,NPN | 04713 | SPS8223 |
| A10Q316 | 151-0188-00 |  | TRANS ISTOR: SILICON, PNP | 04713 | SPS6868K |
| A10Q331 | 151-0369-00 |  | TRANSISTOR:SILICON, PNP | 01295 | SKA6664 |
| Al0Q335 | 151-0221-02 |  | TRANSISTOR:SILICON, PNP | 80009 | 151-0221-02 |
| Al0Q341 | 151-0369-00 |  | TRANSISTOR:SILICON, PNP | 01295 | SKA6664 |
| Al0Q345 | 151-0221-02 |  | TRANSISTOR: SILICON, PNP | 80009 | 151-0221-02 |
| A10Q350 | 151-0271-00 |  | TRANSISTOR:SILICON, PNP | 04713 | SPS8236 |
| A10Q360 | 151-0271-00 |  | TRANSISTOR: SILICON, PNP | 04713 | SPS8236 |
| A10Q370 | 151-0188-00 |  | TRANSISTOR: SILICON, PNP | 04713 | SPS6868K |
| A10Q376 | 151-0752-00 |  | TRANSISTOR: SILICON, NPN | 01281 | BFR96 |
| A10Q377 | 151-0127-00 |  | TRANSISTOR: SILICON, NPN | 07263 | 5006075 |
| A10Q380 | 151-0188-00 |  | TRANSISTOR: SILICON, PNP | 04713 | SPS6868K |
| A10Q386 | 151-0752-00 |  | TRANSISTOR: SILICON, NPN | 01281 | BFR96 |
| A10Q387 | 151-0127-00 |  | TRANSISTOR:SILICON, NPN | 07263 | S006075 |
| A10Q392 | 151-0736-00 |  | TRANSISTOR: SILICON, NPN | 04713 | SPS8317 |
| Al0Q411A, B | 151-1042-00 |  | SEMICOND DVC SE:MATCHED PAIR FET | 01295 | SKA5390 |
| A10Q414 | 151-0198-00 |  | TRANSISTOR:SILICON, NPN, SEL FROM MPS918 | 04713 | SPS8802-1 |
| A10Q474 | 151-0276-00 |  | TRANSISTOR: SILICON, PNP | 80009 | 151-0276-00 |
| A10Q476 | 151-0276-00 |  | TRANSISTOR: SILICON, PNP | 80009 | 151-0276-00 |
| A10Q492 | 151-0221-02 |  | TRANSISTOR: SILICON, PNP | 80009 | 151-0221-02 |
| A10Q493 | 151-0221-02 |  | TRANSISTOR: SILICON, PNP | 80009 | 151-0221-02 |
| Al0Q503 | 151-0424-00 |  | TRANSISTOR: SILICON, NPN | 04713 | SPS8246 |
| Al0Q504 | 151-0199-00 |  | TRANSISTOR: SILICON, PNP | 04713 | SPS6866K |
| A10Q507 | 151-0424-00 |  | TRANSISTOR:SILICON, NPN | 04713 | SPS8246 |
| Al0Q508 | 151-0199-00 |  | TRANSISTOR:SILICON, PNP | 04713 | SPS6866K |
| A100519 | 151-0190-00 |  | TRANSISTOR: SILİCON, NPN | 07263 | 5032677 |
| A10Q605 | 151-0190-00 |  | TRANSISTOR: SILILCON, NPN | 07263 | 5032677 |
| A10Q640 | 151-0190-00 |  | TRANSISTOR: SILICON, NPN | 07263 | S032677 |
| A10Q703 | 151-0276-00 |  | TRANSISTOR: SILICON, PNP | 80009 | 151-0276-00 |
| A10Q706 | 151-0276-00 |  | TRANSISTOR: SILICON, PNP | 80009 | 151-0276-00 |
| Al0Q708 | 151-0190-00 |  | TRANSISTOR: SILILICON, NPN | 07263 | 5032677 |
| Al0Q714 | 151-1097-00 |  | TRANSISTOR:SILICON, FE | 04713 | SPF713 |
| A10Q747 | 151-0190-00 |  | TRANSISTOR: SILICON, NPN | 07263 | 5032677 |
| A10Q753 | 151-0198-00 |  | TRANSISTOR: SILICON, NPN, SEL FROM MPS918 | 04713 | SPS8802-1 |
| A10Q763 | 151-0198-00 |  | TRANSISTOR: SILILCON, NPN, SEL FROM MPS918 | 04713 | SPS8802-1 |
| A10Q765 | 151-0190-00 |  | TRANSISTOR: SILICON, NPN | 07263 | S032677 |
| AL0Q770 | 151-0188-00 |  | TRANSISTOR:SILICON, PNP | 04713 | SPS6868K |
| Al0Q775 | 151-0347-00 |  | TRANSISTOR:SILICON, NPN | 56289 | 2N5551 |
| A10Q779 | 151-0350-00 |  | TRANSISTOR: SILICON, PNP | 04713 | SPS6700 |
| A10Q780 | 151-0190-00 |  | TRANSISTOR:SILICON, NPN | 07263 | 5032677 |
| A10Q785 | 151-0347-00 |  | TRANS ISTOR: SILICON, NPN | 56289 | 2N5551 |
| Al0Q789 | 151-0350-00 |  | TRANSISTOR: SILICON, PNP | 04713 | SPS6700 |
| Al0Q811 | 151-0188-00 |  | TRANSISTOR:SILICON, PNP | 04713 | SPS6868K |
| Al0Q812 | 151-0188-00 |  | TRANSISTOR:SILICON, PNP | 04713 | SPS6868K |
| A10Q813 | 151-0188-00 |  | TRANSISTOR: SILICON, PNP | 04713 | SPS6868K |


| Component No. | Tektronix Part No. | Serial/Model No. Eff Dscont | Name \& Description | Mfr <br> Code | Mir Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A10Q841 | 151-0190-00 |  | TRANSISTOR: SILICON, NPN | 07263 | S032677 |
| A10Q844 | 151-0188-00 |  | TRANSISTOR:SILICON, PNP | 04713 | SPS6868K |
| A10Q845 | 151-0188-00 |  | TRANSISTOR:SILICON, PNP | 04713 | SPS6868K |
| A10Q847 | 151-0347-00 |  | TRANSISTOR: SILICON,NPN | 56289 | 2N5551 |
| A10Q850 | 151-0350-00 |  | TRANSISTOR:SILICON, PNP | 04713 | SPS6700 |
| A]0Q877 | 151-0443-00 |  | TRANSISTOR:SILICON, PNP | 80009 | 151-0443-00 |
| A10Q918 | 151-0432-00 | B010100 B021199X | TRANSISTOR: SILICON, NPN | 80009 | 151-0432-00 |
| A10Q921 | 151-0508-00 | B010100 B021199X | TRANSISTOR:UJT, SI, 2N6027,T0-98 | 03508 | 2N6027 |
| A10Q925 | 151-0538-00 | B010100 B021199X | THYRISTOR:TRIAC, SI, 600V , 8.0A, T0-220 | 02735 | OBD |
| A10Q940 | 151-0476-02 |  | TRANSISTOR: SILICON, NPN, SEL | 04713 | OBD |
| A10Q942 | 151-0476-02 |  | TRANSISTOR: SILICON, NPN, SEL | 04713 | OBD |
| A10Q948 | 151-0453-00 |  | TRANSISTOR: SILICON, PNP | 80009 | 151-0453-00 |
| A10Q954 | 151-0453-00 |  | TRANSISTOR: SILICON, PNP | 80009 | 151-0453-00 |
| A10Q956 | 151-0432-00 |  | TRANS ISTOR: SILICON, NPN | 80009 | 151-0432-00 |
| Al0R151 | 315-0360-00 |  | RES.,FXD , CMPSN: 36 OHM, 5\%,0.25W | 01121 | CB3605 |
| A10R152 | 321-0187-00 |  | RES., FXD, FILM: 866 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G866R0F |
| A10R153 | 321-0225-00 |  | RES.,FXD, FILM:2.15K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G21500F |
| AlOR 154 | 311-1568-00 |  | RES., VAR, NONWIR: $50 \mathrm{OHM}, 20 \%, 0.50 \mathrm{~W}$ | 73138 | 91-90-0 |
| A10R158 | 321-0126-00 |  | RES., FXD, FILM: 200 OHM, 1\%,0.125W | 91637 | MFF1816G200R0F |
| AlOR159 | 321-0199-00 |  | RES., FXD, FILM: 1.15 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816C11500F |
| A10R161 | 315-0360-00 |  | RES., FXD, CMPSN: 36 OHM , $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB3605 |
| Al0R162 | 321-0187-00 |  | RES. , FXD , FILM: 866 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G866R0F |
| Al0R163 | 321-0224-00 |  | RES.,FXD,FILM:2.1K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G21000F |
| Al0R168 | 321-0126-00 |  | RES., FXD,FILM: $200 \mathrm{OHM}, 1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G200R0F |
| A10R169 | 321-0199-00 |  | RES., FXD, FILM: 1.15 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G11500F |
| Al0R170 | 321-0203-00 |  | RES.,FXD, FILM: 1.27 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G12700F |
| Al0R172 | 321-0083-00 |  | RES.,FXD, FILM: 71.5 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G71R50F |
| Al0R173 | 315-0102-00 |  | RES.,FXD,CMPSN: 1 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1025 |
| Al0R174 | 315-0111-00 |  | RES., FXD, CMPSN: 110 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1115 |
| A10R175 | 315-0102-00 |  | RES.,FXD, CMPSN: 1 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1025 |
| AlOR176 | 315-0391-00 |  | RES., FXD , CMPSN: 390 OHM , 5\%, 0.25 W | 01121 | CB3915 |
| A10R177 | 321-0091-00 |  | RES. FXD , FILM : $86.6 \mathrm{OHM}, 1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF 1816G86R60F |
| Al0R178 | 321-0162-00 |  | RES., FXD, FILM: 475 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF $1816 \mathrm{G4} 45 \mathrm{R0F}$ |
| Al0R179 | 315-0621-00 |  | RES., FXD, CMPSN: 620 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB6215 |
| Al0R180 | 321-0088-00 |  | RES.,FXD,FILM:80.6 OHM , $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G80R60F |
| A10R182 | 321-0083-00 |  | RES.,FXD, FILM:71.5 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G71R50F |
| Al0R183 | 315-0201-00 |  | RES. , FXD, CMPSN: 200 OHM, 5\%,0.25W | 01121 | CB2015 |
| Al0R185 | 315-0102-00 |  | RES. , FXD, CMPSN: 1 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1025 |
| A 10 R 186 | 311-1238-00 |  | RES., VAR, NONWIR: 5 K OHM, $10 \%, 0.50 \mathrm{~W}$ | 73138 | 72-27-0 |
| A10R187 | 321-0091-00 |  | RES., FXD, FILM: 86.6 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G86R60F |
| A10R188 | 321-0162-00 |  | RES., FXD, FILM: 475 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G475R0F |
| A10R189 | 315-0621-00 |  | RES., FXD, CMPSN: 620 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB6215 |
| Al0R192 | 321-0231-00 |  | RES., FXD, FILM: 2.49 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G24900F |
| Al0R193 | 321-0230-00 |  | RES.,FXD, FILM: 2.43 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816C24300F |
| Al0R194 | 315-0470-00 |  | RES. ,FXD, CMPSN: 47 OHM, $5 \%, 0.25 \mathrm{~W}$ | 011.21 | CB4705 |
| A10R196 | 315-0681-00 |  | RES.,FXD, CMPSN: 680 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB6815 |
| AlOR 197 | 315-0561-00 |  | RES., FXD, CMPSN: 560 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5615 |
| A10R250 | 315-0911-00 |  | RES., FXD, CMPSN: 910 OHM, 5\%,0.25W | 01121 | CB9115 |
| A10R251 | 315-0360-00 |  | RES., FXD, CMPSN: 36 OHM , 5\%, 0.25W | 01121 | CB3605 |
| A10R252 | 321-0188-00 |  | RES.,FXD, FILM:887 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G887R0F |
| A10R253 | 321-0203-00 |  | RES.,FXD, FILM: 1.27 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816C12700F |
| Al0R254 | 315-0821-00 |  | RES., FXD, CMPSN: 820 OHM , $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB8215 |
| Al0R256 | 321-0253-00 |  | RES.,FXD, FILM:4.22K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816C42200F |
| A10R258 | 321-0126-00 |  | RES., FXD, FILM: 200 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G200R0F |
| Al0R259 | 321-0199-00 |  | RES., FXD, FILM: 1.15 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G11500F |
| Al0R261 | 315-0360-00 |  | RES., FXD, CMPSN: 36 OHM, 5\%, 0.25W | 01121 | CB3605 |
| Al0R 262 | 321-0188-00 |  | RES., FXD, FILM: 887 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G887R0F |


| Component No. | Tektronix Part No. | Serial/Model No. Eff Dscont | Name \& Description | Mfr <br> Code | Mfr Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A10R263 | 321-0225-00 |  | RES.,FXD,FILM: 2.15 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF 1816 G 21500 F |
| Al0R264 | 311-1567-00 |  | RES., VAR, NONWIR: TRMR, 100 OHM, 0.50 W | 73138 | 91-89-0 |
| Al0R 266 | 321-0253-00 |  | RES.,FXD, FILM: 4.22 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G42200F |
| Al0R268 | 321-0126-00 |  | RES., FXD,FILM: 200 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G200R0F |
| A10R269 | 321-0199-00 |  | RES.,FXD,FILM:1.15K OHM,1\%,0.125W | 91637 | MFFl81 6Gl1500F |
| A10R270 | 321-0203-00 |  | RES.,FXD,FILM: 1.27 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF $1816 \mathrm{Gl2700F}$ |
| A10R272 | 321-0083-00 |  | RES.,FXD, FILM: 71.5 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G71R50F |
| A10R273 | 315-0102-00 |  | RES., FXD, CMPSN: 1 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1025 |
| A10R275 | 315-0102-00 |  | RES.,FXD,CMPSN: 1K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1025 |
| A10R276 | 315-0391-00 |  | RES., FXD, CMPSN: 390 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB3915 |
| Al0R277 | 321-0091-00 |  | RES.,FXD, FILM: $86.60 \mathrm{HM}, 1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G86R60F |
| A10R278 | 321-0162-00 |  | RES.,FXD,FILM: 475 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G475R0F |
| Al0R279 | 315-0621-00 |  | RES.,FXD, CMPSN: 620 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB6215 |
| Al0R280 | 321-0088-00 |  | RES.,FXD,FILM:80.6 OHM , 1\%,0.125W | 91637 | MFF1816G80R60F |
| Al0R282 | 321-0083-00 |  | RES.,FXD,FILM: 71.5 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G71R50F |
| A10R283 | 315-0201-00 |  | RES., FXD, CMPSN: 200 OHM, 5\%,0.25W | 01121 | CB 2015 |
| Al0R284 | 315-0111-00 |  | RES., FXD, CMPSN: 110 OHM, 5\%,0.25W | 01121 | CB1115 |
| A10R285 | 315-0102-00 |  | RES.,FXD,CMPSN: 1K OHM, 5\%,0.25W | 01121 | CB1025 |
| Al0R286 | 311-1238-00 |  | RES., VAR, NONWIR: 5 K OHM, $10 \%, 0.50 \mathrm{~W}$ | 73138 | 72-27-0 |
| Al0R287 | 321-0091-00 |  | RES.,FXD,FILM:86.6 OHM , $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G86R60F |
| Al0R288 | 321-0162-00 |  | RES.,FXD,FILM:475 OHM, 1\%,0.125W | 91637 | MFF1816G475R0F |
| A10R289 | 315-0621-00 |  | RES.,FXD, CMPSN: 620 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB6215 |
| A10R292 | 321-0231-00 |  | RES.,FXD,FILM: 2.49 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G24900F |
| Al0R293 | 321-0230-00 |  | RES.,FXD,FILM: 2.43 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G24300F |
| A10R294 | 315-0470-00 |  | RES.,FXD, CMPSN: 47 OHM, 5\%, 0.25W | 01121 | CB4705 |
| A10R295 | 315-0102-00 |  | RES.,FXD,CMPSN: 1 K OHM,5\%,0.25W | 01121 | CB1025 |
| A10R296 | 315-0681-00 |  | RES.,FXD,CMPSN: 680 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB6815 |
| Al0R297 | 315-0102-00 |  | RES.,FXD,CMPSN: 1 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1025 |
| Al0R299 | 315-0912-00 |  | RES.,FXD, CMPSN: 9.1 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB9125 |
| Al0R300 | 315-0512-00 |  | RES.,FXD,CMPSN:5.1K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5125 |
| A10R301 | 315-0512-00 |  | RES . ,FXD, CMPS $: 5.1 \mathrm{~K}$ OHM $, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5125 |
| A10R302 | 315-0512-00 |  | RES., FXD, CMPSN: 5.1K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5125 |
| Al0R304 | 315-0512-00 |  | RES. , FXD, CMPSN: 5.1 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5125 |
| Al0R 305 | 315-0512-00 |  | RES., FXD, CMPSN: 5.1 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5125 |
| A10R306 | 315-0512-00 |  | RES.,FXD,CMPSN: 5.1 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5125 |
| Al0R307 | 315-0361-00 |  | RES., FXD, CMPSN: 360 OHM , $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB3615 |
| A10R308 | 315-0911-00 |  | RES . ,FXD, CMPSN: 910 OHM, 5\%, 0.25W | 01121 | CB9115 |
| Al0R310 | 315-0102-00 |  | RES.,FXD, CMPSN: 1 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1025 |
| A10R311 | 315-0102-00 |  | RES.,FXD, CMPSN: 1 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1025 |
| A10R312 | 315-0511-00 | B010100 B010684X | RES., FXD, CMPSN: 510 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5115 |
| A10R313 | 315-0511-00 | B010100 B010684X | RES.,FXD,CMPSN: 510 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5115 |
| A10R315 | 315-0103-00 |  | RES.,FXD, CMPSN: 10 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1035 |
| Al0R316 | 315-0512-00 |  | RES , ,FXD, CMPSN: 5.1 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5125 |
| Al0R317 | 315-0101-00 |  | RES., FXD, CMPSN: 100 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1015 |
| A10R318 | 315-0101-00 |  | RES., FXD, CMPSN: 100 OHM, 5\%,0.25W | 01121 | CB1015 |
| A10R319 | 315-0512-00 |  | RES.,FXD, CMPSN: 5.1K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5125 |
| A10R320 | 315-0102-00 |  | RES., FXD, CMPSN: 1 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB 1025 |
| A10R321 | 315-0512-00 |  | RES . ,FXD, CMPSN: 5.1K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5125 |
| Al0R322 | 315-0201-00 |  | RES.,FXD, CMPSN: 200 OHM, 5\%,0.25W | 01121 | CB2015 |
| A10R323 | 315-0103-00 |  | RES.,FXD, CMPSN: 10K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1035 |
| A10R324 | 321-0253-00 |  | RES.,FXD,FILM:4.22K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G42200F |
| A10R325 | 321-0253-00 |  | RES.,FXD,FILM:4.22K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G42200F |
| A10R326 | 315-0362-00 |  | RES., FXD, CMPSN: 3.6K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB3625 |
| A10R327 | 315-0362-00 |  | RES. ,FXD, CMPSN: 3.6K OHM , $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB3625 |
| A10R330 | 321-0140-00 |  | RES., FXD , FILM: 280 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G280R0F |
| A10R331 | 321-0152-00 |  | RES.,FXD,FILM: 374 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G374R0F |
| A10R332 | 315-0101-00 |  | RES.,FXD, CMPSN: 100 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1015 |


| Component No. | Tektronix Part No. | Serial/Model No. Eff Dscont | Name \& Description | Mtr <br> Code | Mfr Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A10R334 | 321-0189-00 |  | RES. , FXD, FILM: 909 0HM, 1\%,0.125W | 91637 | MFF1816G909R0F |
| Al0r335 | 321-0084-00 |  | RES., FXD, FILM: 73.2 О ${ }^{\text {HM, }} 1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G73R20F |
| A10R336 | 321-0183-00 |  | RES.,FXD, FILM: 787 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G787R0F |
| Al0r338 | 321-0087-00 |  | RES., FXD, FILM: 78.7 ОНM, 1\%,0.125W | 91637 | MFF1816G78R70F |
| A10R340 | 315-0201-00 |  | RES.,FXD, CMPSN: 200 OHM, 5\%,0.25W | 01121 | CB 2015 |
| A10R341 | 321-0152-00 |  | RES., FXD, FILM: 374 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G374R0F |
| A10R342 | 321-0127-00 |  | RES.,FXD,FILM: 205 OHM, 1\%,0.125W | 91637 | MFF1816G205R0F |
| A10R344 | 321-0189-00 |  | RES.,FXD,FILM:909 ОHM, 1\%,0.125W | 91637 | MFF1816G909R0F |
| A10R345 | 321-0084-00 |  | RES., FXD, FILM: 73.2 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G73R20F |
| A10R346 | 321-0183-00 |  | RES.,FXD, FILM: 787 OHM, 1\%,0.125W | 91637 | MFF1816G787R0F |
| A10R348 | 321-0087-00 |  | RES., FXD, FILM:78.7 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G78R70F |
| A10R350 | 315-0221-00 |  | RES., FXD, CMPSN: 220 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB2215 |
| Al0R351 | 321-0130-00 |  | RES.,FXD, FILM:221 OHM, 1\%,0.125W | 91637 | MFF1816G221R0F |
| Al0R353 | 315-0390-00 |  | RES., FXD, CMPSN: 39 О 0 M, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB3905 |
| Al0R354 | 321-0180-00 |  | RES.,FXD, FILM: 732 ОНM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G732R0F |
| Al0r355 | 321-0080-00 |  | RES. , FXD, FILM:66.5 ОНM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G66R50F |
| Al0R356 | 315-0621-00 |  | RES. , FXD , CMPSN: 620 OHM , $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB6215 |
| Al0R357 | 311-1936-00 |  | RES., VAR, NONWIR:CKT BD, 50 OHM, $20 \%, 0.5 \mathrm{~W}$ | 73138 | MODEL 72 X |
| A10R358 | 315-0112-00 |  | RES. , FXD, CMPSN: 1.1 IK OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1125 |
| A10R360 | 315-0221-00 |  | RES., FXD, CMPSN: 220 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB2215 |
| Al0R361 | 321-0130-00 |  | RES.,FXD, FILM: 221 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G221R0F |
| A10R363 | 315-0390-00 |  | RES. , FXD, CMPSN: 39 OHM, 5\%,0.25W | 01121 | CB3905 |
| A10R364 | 321-0180-00 |  | RES. ,FXD, FILM: 732 OHM, 1\%,0.125W | 91637 | MFF1816G732R0F |
| A10R366 | 311-1236-00 |  | RES., VAR, NONWIR: 250 OHM, $10 \%, 0.50 \mathrm{~W}$ | 73138 | 72-22-0 |
| A10R367 | 311-1237-00 |  | RES., VAR, NONWIR: 1 K OHM, $10 \%, 0.50 \mathrm{~W}$ | 32997 | 3386x-T07-102 |
| Al0R368 | 315-0912-00 |  | RES.,FXD, CMPSN:9.1R OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB9125 |
| Al0r370 | 315-0471-00 |  | RES., FXD, CMPSN: 470 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4715 |
| Al0R371 | 315-0561-00 |  | RES., FXD, CMPSN: 560 OHM , 5\%,0.25W | 01121 | CB5615 |
| A10R373 | 321-0068-00 |  | RES. , FXD, FILM: 49.9 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF181649R90F |
| A10R374 | 315-0101-00 |  | RES., FXD, CMPSN: 100 OHM, 5\%, 0.25 W | 01121 | CB1015 |
| A10R376 | 321-0196-00 |  | RES.,FXD, FILM: 1.07 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G10700F |
| Al0r377 | 321-0190-00 |  | RES., FXD, FILM:931 OHM, 1\%,0.125W | 91637 | MFF1816G931R0F |
| Al0R378 | 323-0148-00 |  | RES., FXD, FILM: 340 OHM, 1\%,0.50W | 91637 | MFF1226G340R0F |
| Al0R379 | 323-0148-00 |  | RES., FXD, FILM: 340 OHM, $1 \%, 0.50 \mathrm{~W}$ | 91637 | MFF1226G340R0F |
| Al0R380 | 315-0471-00 |  | RES. , FXD, CMPSN: 470 OHM , 5\%,0.25 W | 01121 | CB4715 |
| A10R383 | 321-0068-00 |  | RES., FXD, FILM:49.9 OHM, 1\%,0.125W | 91637 | MFFl 81 6G49R90F |
| Al0r384 | 321-0198-00 |  | RES.,FXD, FILM:1.13K OHM, 1\%,0.125W | 91637 | MFF1816G11300F |
| A10R386 | 321-0196-00 |  | RES.,FXD, FILM:1.07K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G10700F |
| Al0R387 | 321-0190-00 |  | RES. , FXD, FILM: 931 OHM, 1\%,0.125W | 91637 | MFF1816G931R0F |
| Al0R388 | 323-0148-00 |  | RES., FXD, FILM: 340 OHM, $1 \%$, 0.50 W | 91637 | MFF1226G340R0F |
| Al0r389 | 323-0148-00 |  | RES., FXD, FILM: 340 OHM, $1 \%, 0.50 \mathrm{~W}$ | 91637 | MFF1226G340R0F |
| A10R390 | 322-0084-00 |  | RES., FXD, FILM: 73.2 OHM, 1\%,0.25W | 91637 | CMFl842G73R20F |
| Al0R391 | 315-0271-00 |  | RES., FXD, CMPSN: 270 OHM, 5\%,0.25W | 01121 | CB2715 |
| A10R392 | 315-0752-00 |  | RES., FXD, CMPSN:7.5K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB7525 |
| A10R393 | 315-0621-00 |  | RES. , FXD, CMPSN: 620 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB6215 |
| A10R394 | 315-0821-00 |  | RES., FXD, CMPSN: 820 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB8215 |
| A10R397 | 315-0681-00 |  | RES. , FXD, CMPSN: 680 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB6815 |
| Al0R398 | 301-0510-00 |  | RES., FXD, CMPSN:51 OHM, $5 \%, 0.50 \mathrm{~W}$ | 01121 | EB5105 |
| Al0R399 | 301-0510-00 |  | RES. , FXD, CMPSN: 51 OHM, 5\%, 0.50W | 01121 | EB5105 |
| A10R408 | 321-0427-00 |  | RES., FXD, FILM: 274 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 24546 | NA55D2743F |
| A10R410 | 315-0101-00 |  | RES. , FXD, CMPSN: 100 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1015 |
| Al0R4ll | 315-0121-00 |  | RES., FXD, CMPSN: 120 OHM , $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1215 |
| Al0R412 | 315-0100-00 |  | RES. , FXD, CMPSN: 10 OHM, 5\%, 0.25W | 01121 | CB1005 |
| Al0R414 | 315-0270-00 |  | RES. , FXD, CMPSN: 27 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB2705 |
| A10R415 | 315-0911-00 |  | RES., FXD, CMPSN: 910 OHM, 5\%,0.25W | 01121 | CB91 15 |
| Al0R417 | 315-0751-00 |  | RES., FXD, CMPSN: 750 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB7515 |
| Al0R418 | 315-0360-00 |  | RES.,FXD, CMPSN:36 ОНM,5\%,0.25W | 01121 | CB3605 |


| Component No. | Tektronix Part No. | Serial/Model No. Eff Dscont | Name \& Description | Mtr <br> Code | Mir Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Al0R421 | 315-0430-00 |  | RES . , FXD, CMPSN: 43 0HM, 5\%, 0.25 W | 01121 | CB4305 |
| A10R422 | 315-0430-00 |  | RES., FXD, GMPSN: 43 0HM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4305 |
| Al0R4 23 | 315-0511-00 |  | RES . ,FXD, CMPSN: 510 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5115 |
| Al0R424 | 315-0392-00 |  | RES., FXD, CMPSN: 3.9 K OHM $, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB3925 |
| Al0R426 | 315-0101-00 |  | RES. , FXD , CMPSN: 100 OHM, 5\%,0.25W | 01121 | CB1015 |
| A10R427 | 321-0158-00 |  | RES. ,FXD, FILM 432 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G432R0F |
| A10R428 | 321-0159-00 |  | RES + FXD , FILM $: 442$ 0HM, 1\%, 0.125 W | 91637 | MFF1816G442R0F |
| A10R429 | 315-0471-00 |  | RES., FXD, CMPS $: 470$ OHM $, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4715 |
| Al0R430 | 315-0822-00 |  | RES., FXD, CMPSN: 8.2 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB8225 |
| A10R431 | 307-0107-00 |  | RES., FXD, CMPSN: 5.6 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB56G5 |
| Al0R432 | 307-0107-00 |  | RES . , FXD, CMPSN: 5.6 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB56G5 |
| Al0R433 | 315-0331-00 |  | RES., FXD, CMPSN: $3300 \mathrm{OHM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB3315 |
| A10R435 | 315-0202-00 |  | RES., FXD, CMPSN: 2 K OHM, 5\%,0.25W | 01121 | CB2025 |
| Al0R436 | 315-0620-00 |  | RES., FXD, CMPSN: 62 OHM $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB6205 |
| A10R437 | 315-0911-00 |  | RES., FXD, CMPSN: 910 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB9115 |
| A10R438 | 315-0751-00 |  | RES., FKD, CMPSN: 750 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB7515 |
| Al0R440 | 315-0220-00 |  | RES., FXD, CMPSN: $220 \mathrm{HM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB2205 |
| Al0R442 | 315-0202-00 |  | RES., FXD, CMPSN: 2 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB2025 |
| A10R444 | 315-0750-00 |  | RES . , FXD, CMPS $=750 \mathrm{HM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB7505 |
| Al0R445 | 315-0911-00 |  | RES., FXD, CMPSN: 910 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB9115 |
| Al0R446 | 315-0751-00 |  | RES., FXD, CMPSN: 750 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB7515 |
| A10R447 | 301-0433-00 |  | RES., FXD, CMPSN: 43 K OHM, $5 \%, 0.50 \mathrm{~W}$ | 01121 | EB4335 |
| A10R448 | 315-0473-00 |  | RES.,FXD, CMPSN: 47 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4735 |
| A10R450 | 301-0433-00 |  | RES., FXD, CMPSN: 43 K OHM, $5 \%, 0.50 \mathrm{~W}$ | 01121 | EB4335 |
| Al0R453 | 315-051.0-00 |  | RES.,FXD,CMPSN:51 OHM,5\%,0.25W | 01121 | CB5105 |
| Al0R454 | 315-0514-00 |  | RES.,FXD, CMPSN:510K OHM, 5\%,0.25W | 01121 | CB5145 |
| A10R456 | 315-0302-00 |  | RES., FXD, CMPSN: 3K OHM , $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB3025 |
| A10R457 | 315-0100-00 |  | RES., FXD, CMPSN: 10 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1005 |
| Al0R458 | 315-0100-00 |  | RES., FXD , CMPSN: 10 OHM, 5\%, 0.25W | 01121 | CB1005 |
| Al0R459 | 315-0103-00 |  | RES., FXD, CMPSN: 10 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1035 |
| A10R460 | 321-0207-00 |  | RES., FXD, FILM $: 1.4 \mathrm{~K}$ OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G14000F |
| A10R461 | 321-0197-00 |  | RES., FXD, FILM: 1.1 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G11000F |
| Al0R462 | 321-0203-00 |  | RES., FXD, FILM $: 1.27 \mathrm{~K}$ OHM $, 1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF $1816 \mathrm{Gl2700F}$ |
| A10R463 | 321-0201-00 |  | RES., FXD, FILM: 1.21 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G12100F |
| A10R464 | 315-0242-00 |  | RES., FXD, CMPSN: $2.4 \mathrm{~K} 0 \mathrm{OHM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB24 25 |
| A 10R466 | 315-0101-00 |  | RES., FXD, CMPSN: $1000 \mathrm{HM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1015 |
| A10R467 | 315-0681-00 |  | RES., FXD, CMPSN: 680 OHM , $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB6815 |
| A10R468 | 315-0820-00 |  | RES., FXD, CMPSN: 82 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB8205 |
| A10R469 | 315-0113-00 |  | RES., FXD, CMPSN: 11 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1135 |
| Al0R470 | 315-0201-00 |  | RES. , FXD, CMPSN: 200 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB2015 |
| Al0R471 | 315-0432-00 |  | RES., FXD, CMPSN: $4.3 \mathrm{~K} 0 \mathrm{HM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4325 |
| A10R472 | 315-0221-00 |  | RES., FXD, CMPSN: 220 OHM , 5\%,0.25W | 01121 | CB2215 |
| A10R473 | 315-0562-00 |  | RES., FXD, CMPSN: 5.6 K OHM $, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5625 |
| A10R474 | 315-0182-00 |  | RES., FXD, CMPSN: 1.8 K OHM $, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1825 |
| Al0R476 | 315-0392-00 |  | RES., FXD, CMPSN: 3.9K OHM , $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB3925 |
| Al0R477 | 315-0392-00 |  | RES., FXD, CMPSN: 3.9 K OHM $, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB3925 |
| Al0R4 78 | 315-0392-00 |  | RES., FXD, CMPSN: 3.9K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB3925 |
| Al0R479 | 315-0752-00 |  | RES., FXD, CMPSN: 7.5 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB7525 |
| Al0R480 | 315-0822-00 |  | RES.,FXD, CMPSN: 8. 2 K OHM, 5\%, 0.25W | 01121 | CB8225 |
| Al0R481 | 321-0191-00 |  | RES., FXD, FILM:953 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G953R0F |
| Al0R482 | 311-1238-00 |  | RES., VAR, NONWIR: 5 K OHM, $10 \%, 0.50 \mathrm{~W}$ | 73138 | 72-27-0 |
| A10R483 | 315-0392-00 |  | RES.,FXD, CMPSN:3.9K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB3925 |
| Al0R484 | 315-0431-00 |  | RES., FXD, CMPSN: 430 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4315 |
| Al0R485 | 315-0431-00 |  | RES., FXD, CMPSN: $4300 \mathrm{HM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4315 |
| Al0R487 | 301-0360-00 |  | RES., FXD, CMPSN: 36 OHM, 5\%,0.5W | 01121 | EB3605 |
| Al0R490 | 315-0241-00 |  | RES., FXD, CMPSN: 240 OHM, 5\%,0.25W | 01121 | CB2415 |
| A10R491 | 315-0201-00 |  | RES., FXD, CMPSN: 200 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB2015 |


| Component No. | Tektronix Part No. | Serial/Model No. Eff Dscont | Name \& Description | Mfr <br> Code | Mfr Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A10R492 | 315-0822-00 |  | RES.,FXD, CMPSN: 8.2 K OHM $, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB8225 |
| A10R493 | 315-0822-00 |  | RES.,FXD,CMPSN: 8.2 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB8225 |
| Al0R494 | 315-0151-00 |  | RES., FXD, CMPSN: 150 0HM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | Cb1515 |
| Al0R495 | 315-0151-00 |  | RES.,FXD,CMPSN: 150 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1515 |
| A10R496 | 315-0124-00 |  | RES., FXD, CMPSN: $120 \mathrm{~K} 0 \mathrm{HM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1245 |
| A10R497 | 315-0241-00 |  | RES. , FXD, CMPSN: $2400 \mathrm{HM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB2415 |
| Al0R501 | 315-0101-00 |  | RES., FXD,CMPSN: 100 0HM, 5\%,0.25W | 01121 | CB1015 |
| A10R503 | 315-0100-00 |  | RES., FXD, CMPSN:10 0HM, 5\%,0.25W | 01121 | CB 1005 |
| A10R504 | 315-0100-00 |  | RES., FXD, CMPSN: 10 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | Cb1005 |
| A10R505 | 315-0434-00 |  | RES., FXD, CMPSN:430K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4345 |
| A10R506 | 315-0434-00 |  | RES., FXD, CMPSN: $430 \mathrm{~K} 0 \mathrm{HM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | C84345 |
| A10R507 | 315-0823-00 |  | RES., FXD, CMPSN: $82 \mathrm{~K} 0 \mathrm{HM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB8235 |
| A10R508 | 315-0823-00 |  | RES., FXD, CMPSN: 82 K 0HM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB8235 |
| AlOR511 | 311-1646-00 |  | RES. , VAR, NONWIR: TRMR, 2 M OHM, 0.5 W | 01121 | E4A205 |
| A10R512 | 311-1646-00 |  | RES. , VAR, NONWIR : TRMR, 2 M OHM, 0.5 W | 01121 | E4A205 |
| Al0R513 | 315-0102-00 |  | RES.,FXD, CMPSN: $1 \mathrm{~K} 0 \mathrm{OHM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1025 |
| A10R514 | 315-0102-00 |  | RES. , FXD, CMPSN: 1 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1025 |
| A10R517 | 315-0103-00 |  | RES.,FXD, CMPSN: 10 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | Cb1035 |
| A10R518 | 315-0102-00 |  | RES., FXD, CMPSN: 1 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1025 |
| Al0R519 | 315-0113-00 |  | RES, , FXD, CMPSN: 11 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1135 |
| A10R5 25 | 315-0274-00 |  | RES. , FXD, CMPSN: $270 \mathrm{~K} 0 \mathrm{HM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB 2745 |
| Al0R526 | 315-0274-00 |  | RES.,FXD,CMPSN: 270 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB2745 |
| Al0R527 | 315-0473-00 |  | RES.,FXD, CMPSN:47K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4735 |
| Al0R528 | 315-0473-00 |  | RES., FXD, CMPSN:47K 0HM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4735 |
| A10R603 | 315-0512-00 |  | RES.,FXD, CMPSN: 5.1 K OHM $, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5 125 |
| A10R605 | 315-0512-00 |  | RES., FXD, CMPSN: 5.1 K OHM $, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5125 |
| A10R607 | 315-0512-00 |  | RES., FXD, CMPSN: 5.1 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5125 |
| Al0R608 | 315-0512-00 |  | RES., EXD, CMPSN: $5.1 \mathrm{~K} 0 \mathrm{HM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5125 |
| Al0R610 | 315-0512-00 |  | RES. , FXD, CMPSN: 5.1 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5125 |
| Al0R611 | 315-0682-00 |  | RES., FXD, CMPSN: 6.8 K OHM $, 5 \%, 0.25 \mathrm{~W}$ | 01121 | cb6825 |
| Al0R612 | 315-0163-00 |  | RES., FXD, CMPSN: 16K 0HM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1635 |
| Al0R614 | 315-0203-00 |  | RES., FXD, CMPSN: 20 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB2035 |
| A10R615 | 315-0621-00 |  | RES. , FXD, CMPSN: 620 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB6215 |
| Al0R618 | 315-0221-00 |  | RES, , FXD, CMPSN: 220 OHM, 5\%, 0.25W | 01121 | CB2215 |
| Al0R619 | 315-0621-00 |  | RES., FXD, CMPSN: 620 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB6215 |
| A10R620 | 315-0102-00 |  | RES., FXD, GMPSN: 1 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1025 |
| Al0R622 | 315-0302-00 |  | RES., FXD,CMPSN: 3 K OHM, 5\%,0.25W | 01121 | Cb3025 |
| Al0R623 | 315-0681-00 |  | RES., FXD, CMPSN: 680 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB6815 |
| Al0R637 | 321-0322-00 |  | RES. , FXD, FILM: 22.1 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G22101F |
| A10R638 | 321-0319-00 |  | RES., FXD,FILM: 20.5 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G20501F |
| AlOR639 | 315-0153-00 |  | RES. , FXD, CMPSN: 15 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1535 |
| Al0R640 | 315-0512-00 |  | RES., FXD, CMPSN: 5.1 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5125 |
| A10R642 | 315-0222-00 |  | RES., FXD, CMPSN: 2.2 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB2225 |
| Al0R649 | 315-0512-00 |  | RES., FXD, CMPSN: $5.1 \mathrm{~K} 0 \mathrm{HM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5125 |
| A10R651 | 315-0201-00 |  | RES., FXD, CMPSN: 200 OHM, 5\%, 0.25 W | 01121 | CB2015 |
| A10R666 | 315-0820-00 |  | RES., FXD, CMPSN: 82 OHM , $5 \%, 0.25 \mathrm{~W}$ | 01121 | Cb8205 |
| Al0R668 | 315-0820-00 |  | RES. , FXD, CMPSN: 82 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB8205 |
| A10R670 | 315-0100-00 |  | RES. , FXD , CMPSN: 10 0HM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1005 |
| Al0R673 | 315-0681-00 |  | RES., FXD, CMPSN:680 OHM, 5\%,0.25W | 01121 | Cb6815 |
| A10R674 | 315-0621-00 |  | RES., FXD, GMPSN: 620 0HM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB6215 |
| Al0R701 | 321-0235-00 |  | RES., FXD, FILM: $2.74 \mathrm{~K} 0 \mathrm{OM}, 1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G27400F |
| Al0R702 | 315-0392-00 |  | RES., FXD, CMPSN: 3.9 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB3925 |
| Al0R703 | 315-0154-00 |  | RES. , FXD, CMPSN: 150 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1545 |
| A10R704 | 315-0621-00 |  | RES., FXD, GMPSN: 620 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB6215 |
| A10R705 | 315-0752-00 |  | RES.,FXD, CMPSN: 7.5 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB7525 |
| A10R706 | 315-0202-00 |  | RES., FXD, CMPSN: $2 \mathrm{~K} 0 \mathrm{HM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB2025 |
| Al0R707 | 315-0132-00 |  | RES., FXD,CMPSN: 1.3 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1325 |


| Component No. | Tektronix Part No. | Serial/Model No. Eff Dscont | Name \& Description | Mfr <br> Code | Mfr Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A10R708 | 321-0271-00 |  | RES.,FXD,FILM: 6.49 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G64900F |
| A10R709 | 311-1560-00 |  | RES., VAR, NONWIR: 5 K OHM, $20 \%, 0.50 \mathrm{~W}$ | 73138 | 91-82-0 |
| Al0R711 | 315-0272-00 |  | RES., FXD, CMPSN: 2.7 K OHM, $5 \%, 0.2 \mathrm{~F}$ W | 01121 | CB2725 |
| Al0R712 | 315-0512-00 |  | RES., FXD, CMPSN: 5.1 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5125 |
| Al0R745 | 315-0221-00 |  | RES., FXD, CMPSN: 220 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB2215 |
| Al0R746 | 321-0134-00 |  | RES., FXD,FILM: 243 OHM, 1\%,0.125W | 91637 | MFF1816G243R0F |
| A10R748 | 321-0230-00 |  | RES., FXD, FILM: 2.43 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G24300F |
| Al0R749 | 321-0271-00 |  | RES.,FXD, FILM 6.49 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G64900F |
| Al0R751 | 321-0180-00 |  | RES., FXD, FILM: 732 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G732ROF |
| Al0R752 | 311-1560-00 |  | RES. ,VAR, NONWIR : 5 K OHM, $20 \%, 0.50 \mathrm{~W}$ | 73138 | 91-82-0 |
| Al0R753 | 321-0217-00 |  | RES.,FXD,FILM:1.78K OHM, 1\%,0.125W | 91637 | MFFl816G17800F |
| Al0R754 | 315-0100-00 |  | RES., FXD, CMPSN: 10 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1005 |
| Al0R756 | 315-0681-00 |  | RES., FXD, CMPSN: $6800 \mathrm{HM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB6815 |
| Al0R757 | 315-0103-00 |  | RES.,FXD, CMPSN:10K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1035 |
| A10R758 | 311-1559-00 |  | RES., VAR, NONWIR: 10 K OHM, $20 \%, 0.50 \mathrm{~W}$ | 73138 | 91-81-0 |
| A10R760 | 315-0681-00 |  | RES., FXD, CMPSN: 680 OHM , $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB6815 |
| Al0R761 | 321-0180-00 |  | RES.,FXD, FILM: 732 OHM, 1\%,0.125W | 91637 | MFF1816G732R0F |
| Al0R762 | 321-0216-00 |  | RES.,FXD, FILM $: 1.74 \mathrm{~K}$ OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G17400F |
| Al0R763 | 321-0217-00 |  | RES., FXD,FILM:1.78K OHM, 1\%,0.125W | 91637 | MFF1816G17800F |
| Al0R765 | 321-0204-00 |  | RES.,FXD,FILM:1.3K OHM, 1\%,0.125W | 91637 | MFF1816G13000F |
| Al0R766 | 321-0271-00 |  | RES., FXD, FTLM 6.49 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G64900F |
| Al0R768 | 321-0154-00 |  | RES., FXD, FILM: 392 OHM, 1\%,0.125W | 91637 | MFF1816G392R0F |
| Al0R771 | 321-0182-00 |  | RES., FXD, FILM: 768 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G768R0F |
| Al0R772 | 315-0273-00 |  | RES.,FXD, CMPSN: 27 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB2735 |
| Al0R775 | 323-0312-00 |  | RES.,FXD, FILM: 17.4 K OHM, $1 \%, 0.50 \mathrm{~W}$ | 91637 | MFF1226G17401F |
| Al0R776 | 321-0189-00 |  | RES., FXD, FILM: 909 OHM, 1\%,0.125W | 91637 | MFF1816G909R0F |
| Al0R777 | 315-0470-00 |  | RES. , FXD, CMPSN: 47 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4705 |
| Al0R778 | 315-0101-00 |  | RES., FXD, CMPSN: 100 OHM, 5\%,0.25W | 01121 | CB1015 |
| Al0R779 | 315-0273-00 |  | RES., FXD, CMPSN: 27 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB 2735 |
| A10R780 | 321-0209-00 |  | RES., FXD, FLLM: 1.47 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G14700F |
| Al0R781 | 321-0201-00 |  | RES., FXD, FILM: 1.21 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G12100F |
| AlOR782 | 315-0273-00 |  | RES., FXD, CMPSN: 27 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB2735 |
| Al 0R785 | 323-0312-00 |  | RES.,FXD, FILM: $17.4 \mathrm{~K} 0 \mathrm{HM}, 1 \%, 0.50 \mathrm{~W}$ | 91637 | MFF1226G17401F |
| Al0R786 | 321-0189-00 |  | RES .,FXD, FILM:909 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G909R0F |
| Al0R787 | 315-0470-00 |  | RES., FXD, CMPSN: 47 0HM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4705 |
| Al0R788 | 315-0101-00 |  | RES., FXD, CMPSN: 100 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1015 |
| Al0R789 | 315-0273-00 |  | RES., FXD, CMPSN: 27 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB2735 |
| Al0R792 | 321-0265-00 |  | RES , FXD, FILM: 5.62K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G56200F |
| Al0R793 | 321-0382-00 |  | RES.,FXD, FILM:93.1K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G93101F |
| A10R796 | 315-0100-00 |  | RES.,FXD, CMPSN: 10 OHM, 5\%,0.25W | 01121 | CB1005 |
| A10R797 | 315-0100-00 |  | RES. , FXD, CMPSN: 10 OHM , 5\%,0.25W | 01121 | CB1005 |
| Al0R798 | 315-0100-00 |  | RES.,FXD, CMPSN: 10 OHM , 5\%,0.25W | 01121 | CB1005 |
| Al0R799 | 315-0100-00 |  | RES. , FXD , CMPSN: 10 OHM, 5\%,0.25W | 01121 | CB1005 |
| Al0R801 | 301-0472-00 |  | RES., FXD, CMPSN:4.7K OHM , $5 \%, 0.50 \mathrm{~W}$ | 01121 | EB4725 |
| Al0R802 | 301-0472-00 |  | RES.,FXD,CMPSN:4.7K OHM, 5\%,0.50W | 01121 | EB4725 |
| Al0R803 | 315-0102-00 |  | RES.,FXD, CMPSN: 1 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1025 |
| A10R809 | 315-0134-00 |  | RES., FXD, CMPSN: 130 K OHM $, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1345 |
| Al0R810 | 315-0103-00 |  | RES., FXD, CMPSN: $10 \mathrm{~K} 0 \mathrm{HM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1035 |
| Al0R811 | 315-0683-00 |  | RES., FXD , CMPSN: 68 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB6835 |
| Al0R812 | 315-0182-00 |  | RES., FXD, CMPSN: 1.8 K OHM $, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1825 |
| Al0R813 | 315-0473-00 |  | RES., FXD, CMPSN: 47 K OHM , $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4735 |
| Al0R814 | 321-0394-00 |  | RES., FXD , FILM : 124 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G12402F |
| Al0R816 | 321-0118-00 |  | RES.,FXD, FILM: 165 OHM , $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G165R0F |
| Al0R817 | 321-0157-00 |  | RES.,FXD, FILM:422 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G422R0F |
| Al0R820 | 315-0102-00 |  | RES.,FXD,CMPSN:1K OHM,5\%,0.25W | 01121 | CB1025 |
| Al0R821 | 315-0104-00 |  | RES.,FXD, CMPSN: 100 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1045 |
| Al0R822 | 315-0511-00 |  | RES., FXD, CMPSN:510 OHM, 5\%,0.25W | 01121 | CB5115 |


| Component No. | Tektronix Part No. | Serial/Model No. Eff Dscont | Name \& Description | Mfr Code | Mfr Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Al0R825 | 315-0104-00 |  | RES., FXD , CMPSN: $100 \mathrm{~K} 0 \mathrm{MM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1045 |
| A10R826 | 315-0102-00 |  | RES., FXD, CMPSN: 1K OHM, 5\%,0.25W | 01121 | CB1025 |
| A10R827 | 321-0379-00 |  | RES., FXD, FILM: 86.6 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G86601F |
| A10R828 | 321-0291-00 |  | RES.,FXD,FILM: 10.5 K OHM, 1\%,0.125W | 91637 | MFFl816G10501F |
| Al0R829 | 321-0197-00 |  | RES.,FXD,FILM: 1.1 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G11000F |
| Al0R830 | 315-0123-00 |  | RES., FXD, CMPSN: 12 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1235 |
| A10R831 | 315-0431-00 |  | RES., FXD, CMPSN: 430 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4315 |
| A10R834 | 315-0304-00 |  | RES., FXD, CMPSN: 300 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB3045 |
| A10R835 | 315-0395-00 |  | RES. ,FXD, CMPSN: $3.9 \mathrm{M} 0 \mathrm{HM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB3955 |
| Al0R836 | 315-0821-00 |  | RES., FXD, CMPSN: 820 0HM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB8215 |
| A10R837 | 315-0302-00 |  | RES., FXD, CMPSN: 3K OHM , 5\%, 0.25W | 01121 | CB3025 |
| A10R839 | 315-0472-00 |  | RES.,FXD, CMPSN: 4.7 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4725 |
| Al0R840 | 321-0241-00 |  | RES., FXD, FILM:3.16K OHM, 1\%,0.125W | 91637 | MFF1816G31600F |
| Al0R841 | 321-0149-00 |  | RES., FXD, FILM:348 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G348R0F |
| Al0R842 | 321-0261-00 |  | RES.,FXD, FILM: 5.11K 0HM, 1\%,0.125W | 91637 | MFF1816G51100F |
| Al0R844 | 321-0230-00 |  | RES., FXD,FILM: 2.43 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G24300F |
| Al0R845 | 321-0221-00 |  | RES., FXD,FILM:1.96K OHM, 1\%,0.125W | 91637 | MFF1816G19600F |
| Al0R846 | 321-0332-00 |  | RES.,FXD,FILM: 28 K 0HM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G28001F |
| A10R847 | 315-0102-00 |  | RES. , FXD, CMPSN: 1 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1025 |
| A10R849 | 315-0270-00 |  | RES. ,FXD, CMPSN: 27 OHM, 5\%,0.25W | 01121 | CB2705 |
| A10R850 | 315-0471-00 |  | RES., FXD, CMPSN: 470 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4715 |
| Al0R851 | 315-0222-00 |  | RES., FXD, CMPSN: 2. 2 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB2225 |
| Al0R852 | 315-0104-00 |  | RES., FXD, CMPSN: 100 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1045 |
| Al0R854 | 315-0180-00 | XB012543 | RES., FXD, CMPSN: 18 OHM, 5\%,0.25W | 01121 | CB1805 |
| Al0R856 | 315-0470-00 |  | RES. , FXD, CMPSN: 47 OHM, 5\%, 0.25W | 01121 | CB4705 |
| A10R860 | 311-1558-00 |  | RES .,VAR, NONWIR : 20 K 0HM, 20\%,0.50W | 73138 | 91-80-0 |
| Al OR861 | 315-0203-00 |  | RES.,FXD, CMPSN: 20 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB2035 |
| Al0R863 | 315-0474-00 |  | RES.,FXD, CMPSN:470K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4745 |
| Al0R864 | 315-0472-03 |  | RES.,FXD, CMPSN:4.7K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4725 |
| Al0R865 | 315-0470-03 |  | RES.,FXD, CMPSN: 47 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4705 |
| Al0R867 | 315-0511-02 |  | RES., FXD, CMPSN: 510 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5115 |
| Al0R868 | 315-0226-01 |  | RES.,FXD, CMPSN: 22 M OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB2265 |
| A10R870 | 311-1555-00 |  | RES.,VAR, NONWIR: 100 K OHM, $20 \%, 0.5 \mathrm{~W}$ | 73138 | 91-77-0 |
| A10R871 | 315-0471-00 |  | RES., FXD, CMPSN: 470 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4715 |
| A10R872 | 315-0102-00 |  | RES., FXD, CMPSN: 1K OHM,5\%,0.25W | 01121 | CB1025 |
| A10R873 | 315-0513-00 |  | RES., FXD, CMPSN: $51 \mathrm{~K} 0 \mathrm{HM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5135 |
| Al0R874 | 315-0433-00 |  | RES. , FXD , CMPSN: 43K OHM , 5\%,0.25W | 01121 | CB4335 |
| A10R875 | 311-1550-00 |  | RES.,VAR, NONWIR: 2 M OHM, $20 \%, 0.50 \mathrm{~W}$ | 73138 | 91-72-0 |
| A10R876 | 315-0471-00 |  | RES., FXD, CMPSN: 470 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4715 |
| Al0R877 | 315-0183-00 |  | RES.,FXD, CMPSN: 18 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1835 |
| Al0R878 | 301-0105-00 |  | RES., FXD, CMPSN: 1 M OHM, $5 \%, 0.50 \mathrm{~W}$ | 01121 | EB1055 |
| A10R879 | 301-0105-00 |  | RES., FXD, CMPSN: 1M OHM, 5\%,0.50W | 01121 | EB1055 |
| A10R880 | 301-0105-00 |  | RES., FXD, CMPSN: 1 M OHM, $5 \%, 0.50 \mathrm{~W}$ | 01121 | EB1055 |
| A10R881 | 301-0105-00 |  | RES. , FXD, GMPSN: 1 M OHM, $5 \%, 0.50 \mathrm{~W}$ | 01121 | Eb1055 |
| Al0R882 | 301-0105-00 |  | RES., FXD, CMPSN: 1 M OHM, $5 \%, 0.50 \mathrm{~W}$ | 01121 | EB1055 |
| A10R883 | 311-1933-00 |  | RES. , VAR, NONWIR: PNL, 5M OHM, $10 \%, 0.50 \mathrm{~W}$ | 01121 | 17M095 |
| Al0R884 | 301-0105-00 |  | RES., FXD, CMPSN: 1 M OHM, $5 \%, 0.50 \mathrm{~W}$ | 01121 | EB1055 |
| A10R886 | 315-0471-00 |  | RES. , FXD , CMPSN: 470 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4715 |
| Al0R887 | 311-1555-00 |  | RES. ,VAR, NONWIR : 100 K OHM, 20\%, 0.5 W | 73138 | 91-77-0 |
| Al0R911 | 301-0184-00 | B010100 B021199X | RES.,FXD, CMPSN: $180 \mathrm{~K} 0 \mathrm{HM}, 5 \%, 0.50 \mathrm{~W}$ | 01121 | EB1845 |
| Al0R912 | 315-0104-00 | B010100 B021199X | RES. ,FXD, CMPSN: 100 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1045 |
| Al0R914 | 301-0184-00 | B010100 B021199X | RES.,FXD, CMPSN: 180 K OHM $, 5 \%, 0.50 \mathrm{~W}$ | 01121 | EB1845 |
| Al0R915 | 321-0230-00 | B010100 B021199X | RES.,FXD, FILM: 2.43 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G24300F |
| Al0R916 | 315-0223-00 | B010100 B021199X | RES.,FXD, CMPSN: $22 \mathrm{~K} 0 \mathrm{HM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB2235 |
| Al0R917 | 315-0154-00 | B010100 B021199X | RES.,FXD, CMPSN: $150 \mathrm{~K} 0 \mathrm{HM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1545 |
| Al0R918 | 315-0753-00 | B010100 B021199X | RES., FXX, CMPSN: 75 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB7535 |
| Al0R920 | 301-0105-00 | B010100 B021199X | RES.,FXD, CMPSN: 1 M OHM, $5 \%, 0.50 \mathrm{~W}$ | 01121 | EB1055 |


| Component No. | Tektronix Part No. | Serial/Mo Eff | del No. Dscont | Name \& Description | Mfr Code | Mir Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A 10R925 | 315-0510-00 | B010100 | B021199X | RES.,FXD, CMPSN: 51 OHM, 5\%,0.25W | 01121 | CB5105 |
| A10R926 | 301-0471-00 | B010100 | B021199X | RES. , FXD, CMPSN: 470 OHM, $5 \%, 0.50 \mathrm{~W}$ | 01121 | EB4715 |
| A10R940 | 315-0470-00 |  |  | RES. , FXD, CMPSN: 47 OHM, 5\%,0.25W | 01121 | CB4705 |
| A10R941 | 308-0677-00 |  |  | RES. , FXD, WW: 1 OHM, 5\%, 2 W | 75042 | BWH-1 R000J |
| Al0R942 | 315-0470-00 |  |  | RES. , FXD, CMPSN: 47 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4705 |
| A10R945 | 321-0234-00 |  |  | RES. FFXD, FILM: 2.67 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G26700F |
| A10R946 | 311-1248-00 |  |  | RES.,VAR, NONWIR: 500 OHM, 10\%,0.50W | 73138 | 72-23-0 |
| Al0R947 | 321-0304-00 |  |  | RES., FXD, FILM : 14.3 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816C14301F |
| A10R948 | 315-0102-00 |  |  | RES., FXD, CMPSN: 1R OHM , 5\%,0.25W | 01121 | CB1025 |
| A10R950 | 315-0102-00 |  |  | RES.,FXD, CMPSN: 1 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1025 |
| Al0R951 | 301-0472-00 |  |  | RES., FXD, CMPSN: 4.7K OHM, 5\%,0.50W | 01121 | EB4725 |
| A10R952 | 311-1 562-00 | B010100 | B021199X | RES.,VAR, NONWIR : 2 K OHM, $20 \%, 0.50 \mathrm{~W}$ | 73138 | 91-84-0 |
| A10R953 | 315-0361-00 | B010100 | B021199 | RES. , FXD, CMPSN: 360 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB3615 |
| Al0R953 | 315-0203-00 | B021 200 |  | RES., FXD, CMPSN: 20 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB2035 |
| A10R954 | 315-0102-00 |  |  | RES. , FXD, CMPSN: 1K 0HM, 5\%,0.25W | 01121 | CB1025 |
| A10R956 | 301-0622-00 |  |  | RES . , FXD, CMP SN: 6. 2K OHM , 5\%,0.50W | 01121 | EB6225 |
| Al0R990 | 315-0101-00 |  |  | RES., FXD, CMPSN: 100 OHM, 5\%,0.25W | 01121 | CB1015 |
| A10R992 | 315-0682-03 |  |  | RES., FXD, CMPSN:6.8K OHM, 5\%,0.25 W | 01121 | CB6825 |
| A10R994 | 315-0682-03 |  |  | RES., FXD, CMPSN: 6.8K OHM, 5\%,0.25 W | 01121 | CB6825 |
| Al0R995 | 315-0101-03 |  |  | RES. , FXD, CMPSN: 100 OHM , 5\%, 0. 25 W | 01121 | CB1015 |
| A10RT356 | 307-0125-00 |  |  | RES. ,THERMAL: 500 OHM, 10\%, 25 DEG C | 50157 | 2D1595 |
| A10S 901 | 260-1 849-00 |  |  | SWITCH, PUSH: DPDT, 4A, 250VAC, W/BRKT | 31918 | OBD |
| A10T448 | 120-1401-00 |  |  | XFMR, TRIG: | 54937 | OBD |
| A10T925 | 120-1384-00 | B010100 | B021199X | TRANSFORMER, RF:TOROID, 2 WINDS | 80009 | 120-1384-00 |
| Al0T940 | 120-1348-00 |  |  | XFMR, PWR, SDN $\&$ SU: HIGH VOLTAGE | 80009 | 120-1348-00 |
| Al0T942 | 120-1347-00 |  |  | TRANSFORMER,RF:DRIVER SATURATING POT CORE | 80009 | 120-1347-00 |
| A10TP444 | 214-0579-00 |  |  | TERM, TEST POINT: BRS CD PL | 80009 | 214-0579-00 |
| Al0TP 500 | 214-0579-00 |  |  | TERM, TEST POINT: BRS CD PL | 80009 | 214-0579-00 |
| A10TP501 | 214-0579-00 |  |  | TERM, TEST POINT: BRS CD PL | 80009 | 214-0579-00 |
| A10TP854 | 214-0579-00 | XB019850 |  | TERM, TEST POINT: BRS CD PL | 80009 | 214-0579-00 |
| A10TP915 | 214-0579-00 | B010100 | B021199X | TERM, TEST POINT: BRS CD PL | 80009 | 214-0579-00 |
| A10TP920 | 214-0579-00 | B010100 | B021199X | TERM, TEST POINT: BRS CD PL | 80009 | 214-0579-00 |
| A10TP921 | 214-0579-00 | B010100 | B021199X | TERM, TEST POINT: BRS CD PL | 80009 | 214-0579-00 |
| Al0TP934 | 214-0579-00 |  |  | TERM, TEST POINT: BRS CD PL | 80009 | 214-0579-00 |
| A10TP951 | 214-0579-00 |  |  | TERM, TEST POINT: BRS CD PL | 80009 | 214-0579-00 |
| A10TP952 | 214-0579-00 | XB019850 |  | TERM, TEST POINT: BRS CD PL | 80009 | 214-0579-00 |
| Al0U170 | 156-1 294-00 |  |  | MICROCIRCUIT,LI:FIVE NPN TRANSISTOR ARRAY | 80009 | 156-1294-00 |
| Al0U197 | 156-0048-00 |  |  | MICROCIRCUIT, LI: FIVE NPN TRANSISTOR ARRAY | 02735 | CA3046 |
| Al0U 270 | 156-1294-00 |  |  | MICROCIRCUIT, LI: FIVE NPN TRANSISTOR ARRAY | 80009 | 156-1294-00 |
| A10U305 | 156-0728-00 | B010100 | B019249 | MICROCIRCUIT, DI: QUAD 2-INP POS AND GATES | 27014 | DM74LS09 |
| Al0U305 | 156-0728-02 | B019250 |  | MICROCIRCUIT, DI: QUAD 2 IPUT STATE W/OC | 80009 | 156-0728-02 |
| A10U310 | 156-0721-00 | B010100 | B012542 | MICROCIRCUIT, DI: ST POS-NAND GATES W/TP OUT | 27014 | DM74LS132N |
| A10U310 | 156-0721-02 | B012543 |  | MICROCIRCUIT, DI: QUAD 2-IN NAND SCHMITT TRI | 04713 | SN74LS132NDS |
| Al0U315 | 156-0384-00 | B010100 | B019249 | MICROCIRCUIT, DI: QUAD 2-INPUT NAND GATE | 80009 | 156-0384-00 |
| A10U315 | 156-0384-02 | B019250 |  | MICROCIRCUIT, DI: QUAD 2-INP NAND GATE | 01295 | SN74LS03 |
| A10U317 | 156-0388-00 | B010100 | B019249 | MICROCIRCUIT, DI: DUAL D-TYPE FLIP-FLOP | 80009 | 156-0388-00 |
| A10U317 | 156-0388-03 | B019250 |  | MICROCIRCUIT, DI: DUAL D FLIP-FLOP | 07263 | 74LS74A |
| A10U421 | 156-1294-00 |  |  | MICROCIRCUIT, LI:FIVE NPN TRANSISTOR ARRAY | 80009 | 156-1294-00 |
| A10U460 | 156-0534-00 |  |  | MICROCIRCUIT, LI: DUAL DIFF AMPL, 14 LD DIP | 80009 | 156-0534-00 |
| A10U480 | 156-0205-00 |  |  | MICROCIRCUIT, DI: QUAD 2-INPUT NOR GATE | 04713 | MC10102 (P OR L) |
| A10U 507 | 156-0158-00 |  |  | MICROCIRCUIT,LI:DUAL OPERATIONAL AMPLIFIER | 18324 | MC1458N |
| A10U603 | 156-1611-00 |  |  | MICROCIRCUIT, DI: DUAL D TYPE EDGE-TRIGGERED | 07263 | 74F74 |
| A10U607 | 156-0180-04 | B010100 | B018549 | MICROCIRCUIT, DI: QUAD 2-INPUT NAND GATE | 01295 | SN74S00NP3 |
| A10U607 | 156-0382-02 | B018550 |  | MICROCIRCUIT, DI: QUAD 2-INP NAND GATE | 01295 | SN74LS00 |
| Al0U620 | 156-0875-00 | B010100 | B017149 | MICROCIRCUIT, DI: DUAL 2 WIDE 2INP A01 GATE | 27014 | 4 DM74LS51(N OR J) |
| Al0U620 | 156-0875-02 | B017150 |  | MICROCIRCUIT, DI: dUAL 2-W/2 INP AOI GATES | 01295 | 5 SN74LS51 |
| Al0U640 | 156-0405-00 | B010100 | B011849 | microcircuit, di: dual retrig monostable my | 07263 | 3 9602 (PC OR DC) |


| Component No. | Tektronix Part No. | Serial/Mo Eff | del No. Dscont | Name \& Description | Mfr <br> Code | Mfr Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Al0U640 | 156-1195-00 | B011850 | B020949 | MICROCIRCUIT, DI: DUAL RETRIC MONOSTABLE MV | 80009 | 156-1195-00 |
| Al0U640 | 156-1195-01 | B020950 |  | MICROCIRCUIT, DI: DUAL RETRIG/RESET |  |  |
| A10U825 | 156-0515-00 |  |  | MICROCIRCUIT, DI: TRIPLE 3-CHAN MUX | 80009 | 156-0515-00 |
| A10U835 | 156-1191-00 |  |  | MICROCIRCUIT, LI: DUAL BI-FET OP-AMPL, 8 DIP | 01295 | TL072ACP |
| A10U931 | 156-0885-00 | B010100 | B021199X | MICROCIRCUIT, LI: OPTOELECTRONIC ISOLATOR | 04713 | S0C123A |
| Al0U985 | 156-1263-00 |  |  | MICROCIRCUIT, LI: VOLTAGE REGULATOR | 27014 | LM341P-5.0TB |
| Al0U990 | 152-0791-00 |  |  | SEMICOND DEVICE:V MULTR, 4 KV IN, 8 KV DC OUT | 52306 | CMX554D |
| A10VR483 | 152-0662-00 |  |  | SEMICOND DEVICE:ZENER, $0.4 \mathrm{~W}, 5 \mathrm{~V}, 1 \%$ | 04713 | SZG195 |
| Al0VR644 | 152-0278-00 |  |  | SEMICOND DEVICE:ZENER,0.4W, 3V,5\% | 04713 | SZG35009K20 |
| Al0VR657 | 152-0317-00 |  |  | SEMICOND DEVICE:ZENER,0.25W,6.2V,5\% | 04713 | SZG20012 |
| Al0VR781 | 152-0243-00 |  |  | SEMICOND DEVICE:ZENER, $0.4 \mathrm{~W}, 15 \mathrm{~V}, 5 \%$ | 14552 | TD3810983 |
| Al0VR809 | 152-0127-00 |  |  | SEMICOND DEVICE: ZENER,0.4W, $7.5 \mathrm{~V}, 5 \%$ | 04713 | SZG35009K2 |
| Al0VR847 | 152-0662-00 |  |  | SEMICOND DEVICE:ZENER,0.4W, 5v, $1 \%$ | 04713 | SZG195 |
| Al0VR901 | 307-0456-00 |  |  | RES, V SENSITIVE: | 03508 | M0V-V250LA15A |
| Al0VR913 | 152-0304-00 | B010100 | B021199X | SEMICOND DEVICE:ZENER,0.4W, 20V,5\% | 15238 | Z5411 |
| Al0VR914 | 152-0149-00 | B010100 | B021199X | SEMICOND DEVICE:ZENER,0.4W,10V,5\% | 04713 | SZG35009K3 |
| Al0VR915 | 152-0149-00 | B010100 | B021199X | SEMICOND DEVICE:ZENER,0.4W,10V,5\% | 04713 | SZG35009K3 |
| AlOVR938 | 152-0788-00 | B010100 | B021199X | SEMICOND DEVICE:TRANSIENT SUPPRESSOR | 24444 | 5 KP 45 |
| Al0VR951 | 152-0317-00 |  |  | SEMICOND DEVICE:ZENER,0.25W,6.2V,5\% | 04713 | SZG20012 |
| A10W170 | 131-0566-00 |  |  | BUS CONDUCTOR:DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| A10W196 | 131-0566-00 | XB019850 |  | BUS CONDUCTOR: DUMMY RES, $2.375,22$ AWg | 55210 | L-2007-1 |
| Al0W197 | 131-0566-00 |  |  | BUS CONDUCTOR:DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| Al0W198 | 131-0566-00 |  |  | BUS CONDUCTOR:DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| Al0W199 | 131-0566-00 |  |  | BUS CONDUCTOR:DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| A10W296 | 131-0566-00 | XB019850 |  | BUS CONDUCTOR: DUMMY RES, $2.375,22$ AWG | 55210 | L-2007-1 |
| Al0W297 | 131-0566-00 |  |  | BUS CONDUCTOR: DUMMY RES,2.375,22 AWC | 55210 | L-2007-1 |
| A10W298 | 131-0566-00 |  |  | BUS CONDUCTOR:DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| Al0W299 | 131-0566-00 |  |  | BUS CONDUCTOR: DUMMY RES,2.375,22 AWC | 55210 | L-2007-1 |
| Al0W300 | 131-0566-00 |  |  | BUS CONDUCTOR: DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| Al0W301 | 131-0566-00 |  |  | BUS CONDUCTOR: DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| A10W308 | 131-0566-00 |  |  | BUS CONDUCTOR:DUMMY RES, $2.375,22$ AWG | 55210 | L-2007-1 |
| Al0W 309 | 131-0566-00 |  |  | BUS CONDUCTOR: DUMMY RES, $2.375,22$ AWG | 55210 | L-2007-1 |
| Al0W310 | 131-0566-00 |  |  | BUS CONDUCTOR:DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| Al0w311 | 131-0566-00 |  |  | BUS CONDUCTOR:DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| Al0W312 | 131-0566-00 |  |  | BUS CONDUCTOR:DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| Al0W314 | 131-0566-00 |  |  | BUS CONDUCTOR:DUMMY RES, $2.375,22$ AWG | 55210 | L-2007-1 |
| Al0w315 | 131-0566-00 |  |  | BUS CONDUCTOR: DUMMY RES, 2.375,22 AWG | 55210 | L-2007-1 |
| Al0W380 | 131-0566-00 |  |  | BUS CONDUCTOR: DUMMY RES, 2.375,22 AWG | 55210 | L-2007-1 |
| Al0W392 | 131-0566-00 |  |  | BUS CONDUCTOR: DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| Al0W397 | 131-0566-00 |  |  | BUS CONDUCTOR: DUMMY RES, 2.375,22 AWG | 55210 | L-2007-1 |
| A10w399 | 131-0566-00 |  |  | BUS CONDUCTOR:DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| Al0W418 | 131-0566-00 |  |  | BUS CONDUCTOR: DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| A10W421 | 131-0566-00 |  |  | BUS CONDUCTOR:DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| Al0w422 | 131-0566-00 |  |  | BUS CONDUCTOR: DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| Al0w430 | 131-0566-00 |  |  | BUS CONDUCTOR:DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| A10W431 | 131-0566-00 |  |  | BUS CONDUCTOR:DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| A10W432 | 131-0566-00 |  |  | BUS CONDUCTOR:DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| A10W444 | 131-0566-00 |  |  | BUS CONDUCTOR: DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| A10W447 | 131-0566-00 | B010100 | B019849X | BUS CONDUCTOR: DUMMY RES, $2.375,22$ AWG | 55210 | L-2007-1 |
| Al0W448 | 131-0566-00 |  |  | BUS CONDUCTOR:DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| Al0W470 | 131-0566-00 |  |  | BUS CONDUCTOR:DUMMY RES, 2.375,22 AWG | 55210 | L-2007-1 |
| Al0W472 | 131-0566-00 |  |  | BUS CONDUCTOR: DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| Al0W 507 | 131-0566-00 |  |  | BUS CONDUCTOR:DUMMY RES,2.375,22 AWG | 55210 | 1-2007-1 |
| A10W508 | 131-0566-00 |  |  | BUS CONDUCTOR:DUMMY RES, 2.375,22 AWG | 55210 | L-2007-1 |
| A10W519 | 131-0566-00 |  |  | BUS CONDUCTOR: DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| A 10W564 | 131-0566-00 |  |  | BUS CONDUCTOR:DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| Al0W571 | 131-0566-00 |  |  | BUS CONDUCTOR: DUMMY RES, $2.375,22$ AWC | 55210 | L-2007-1 |


| Component No. | Tektronix Part No. | Serial/Model No. Eff Dscont | Name \& Description |  |  |  | Mfr Code | Mfr Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A10W606 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES,2.375,22 | AWG | 55210 | L-2007-1 |
| A10W616 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES,2.375,22 A | AWG | 55210 | L-2007-1 |
| A10W640 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES,2.375,22 | AWG | 55210 | L-2007-1 |
| A10W646 | 131-0566-00 |  | BUS | CONDUCTOR : DUMMY | RES,2.375,22 | AWG | 55210 | L-2007-1 |
| A10W650 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES,2.375,22 | AWG | 55210 | L-2007-1 |
| A10W652 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES,2.375,22 | AWG | 55210 | L-2007-1 |
| A10W674 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES, 2. 375, 22 | AWG | 55210 | L-2007-1 |
| A10W696 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES, 2.375,22 | AWG | 55210 | L-2007-1 |
| A10W704 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES, 2.375,22 | AWG | 55210 | L-2007-1 |
| Al0W762 | 131-0566-00 | XB019850 | BUS | CONDUCTOR : DUMMY | RES, 2.375,22 | AWG | 55210 | L-2007-1 |
| A10W763 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES, $2.375,22$ | AWG | 55210 | L-2007-1 |
| A10W764 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES,2.375,22 | AWG | 55210 | L-2007-1 |
| A10W835 | 131-0566-00 |  | BUS | CONDUCTOR : DUMMY | RES, 2.375, 22 | AWG | 55210 | L-2007-1 |
| Al0W836 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES, 2.375, 22 | AWG | 55210 | L-2007-1 |
| A10W840 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES, $2.375,22$ | AWG | 55210 | L-2007-1 |
| A10W841 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES,2.375,22 | AWG | 55210 | L-2007-1 |
| Al0W842 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES, 2.375, 22 | AWG | 55210 | L-2007-1 |
| Al0W843 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES,2.375, 22 | AWG | 55210 | L-2007-1 |
| Al0W844 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES, 2. 375, 22 | AWG | 55210 | L-2007-1 |
| Al0W845 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES, 2.375, 22 | AWG | 55210 | L-2007-1 |
| A10W846 | 131-0566-00 |  | BUS | CONDUC TOR : DUMMY | RES, 2.375, 22 | AWG | 55210 | L-2007-1 |
| Al0W847 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES, 2.375,22 | AWG | 55210 | L-2007-1 |
| Al0W854 | 131-0566-00 | B010100 B012542X | BUS | CONDUCTOR: DUMMY | RES, 2.375,22 | AWG | 55210 | L-2007-1 |
| Al0W877 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES, 2. 375, 22 . | AWG | 55210 | L-2007-1 |
| Al0W878 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES, 2. 375, 22 | AWG | 55210 | L-2007-1 |
| Al0W887 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES,2.375,22 | AWG | 55210 | L-2007-1 |
| Al0W964 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES, 2.375, 22 | AWG | 55210 | L-2007-1 |
| A10W965 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES, 2.375,22 | AWG | 55210 | L-2007-1 |
| A10W966 | 131-0566-00 |  | BUS | CONDUCTOR : DUMMY | RES, 2.375, 22 | AWG | 55210 | L-2007-1 |
| A10W967 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES, $2.375,22$ | AWG | 55210 | L-2007-1 |
| A10W968 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES, 2. 375, 22 | AWG | 55210 | L-2007-1 |
| A10W969 | 131-0566-00 |  | BUS | CONDUCTOR : DUMMY | RES, 2.375, 22 | AWG | 55210 | L-2007-1 |
| A10W975 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES, 2.375, 22 | AWG | 55210 | L-2007-1 |
| A10W976 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES, $2.375,22$ | AWG | 55210 | L-2007-1 |
| A10W982 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES, 2.375,22 | AWG | 55210 | L-2007-1 |
| A10W985 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES,2.375,22 | AWG | 55210 | L-2007-1 |
| A10W986 | 131-0566-00 |  | BUS | CONDUCTOR: DUMMY | RES,2.375,22 | AWG | 55210 | L-2007-1 |
| Al0W1010 | 131-1817-00 |  | LINK | , TERM CONNE:22 A | AWG,2.25" LONG |  | 80009 | 131-1817-00 |

THRU
A10W1039 131-1817-00
LINK, TERM CONNE: 22 AWG, $2.25^{\prime \prime}$ LONG
80009 131-1817-00

| Component No. | Tektronix Part No. | Serial/Model No. Eff Dscont | Name \& Description | Mfr <br> Code | Mfr Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Al1 | 670-6867-00 | в010100 B019849 | CKT board assy:front panel | 80009 | 670-6867-00 |
| A11 | 670-6867-01 | в019850 | CKT BOARD ASSY:FRONT PANEL | 80009 | 670-6867-01 |
| A11C101 | 281-0862-00 |  | CAP., FXD, CER DI: $0.001 \mathrm{OF},+80-20 \%, 100 \mathrm{~V}$ | 20932 | 401-ES-100AD102Z |
| AllC 202 | 285-0515-00 |  | CAP. , FXD, PLSTC: $0.022 \mathrm{UF}, 20 \%, 400 \mathrm{~V}$ | 56289 | 192P22304 |
| A11C265 | 281-0773-00 |  | CAP., FXD, CER DI:0.01UF, $10 \%$, 100 V | 04222 | Gc70-1C103K |
| Allc31 3 | 281-0773-00 |  | CAP., FXD, CER DI: $0.01 \mathrm{UF}, 10 \%, 100 \mathrm{~V}$ | 04222 | Gc70-1C103K |
| Al1C314 | 281-0773-00 |  | CAP., FXD, CER DI: $0.01 \mathrm{UF}, 10 \%, 100 \mathrm{~V}$ | 04222 | cc70-1C103K |
| Allc315 | 281-0773-00 |  | CAP.,FXD, CER DI:0.01UF, $10 \%, 100 \mathrm{~V}$ | 04222 | cc70-1C103K |
| Allc402 | 283-0006-00 |  | CAP.,FKD, CER DI: $0.02 \mathrm{UF},+80-20 \%, 500 \mathrm{~V}$ | 72982 | 0841545Z5V00203z |
| Allc403 | 283-0331-00 |  | CAP., FXD, CER DI:43PF, $2 \%, 100 \mathrm{~V}$ | 72982 | 805-505A430G |
| A11C404 | 283-0342-00 |  | CAP., FKD, CER DI: $6.5 \mathrm{SF}, 0.5 \%$, 2000v | 91418 | HV6R5D2024R0 |
| A11C531 | 281-0773-00 |  | CAP, ,FXD, CER DI: $0.01 \mathrm{UF}, 10 \%, 100 \mathrm{~V}$ | 04222 | 6C70-1C103k |
| AllC650 | 281-0773-00 |  | CAP., FXD, CER DI: $0.01 \mathrm{UF}, 10 \%, 100 \mathrm{~V}$ | 04222 | Gc70-1C103K |
| Allc 725 | 290-0745-00 |  | CAP., FXD, ELCTLT: $22 \mathrm{UF},+50-10 \%$, 25 V | 56289 | 502D225 |
| Al1c726 | 281-0775-00 |  | CAP., FXD, CER DI: $0.1 \mathrm{UF}, 20 \%$, 50 V | 04222 | SA205E 104MAA |
| Al 1 CR 536 | 152-0141-02 |  | SEMICOND DEVICE: SILICON, 30V, 150MA | 01295 | 1N4152R |
| All 1 CR 538 | 152-0141-02 |  | SEMICOND DEVICE:SILICON, 30v, 150 MA | 01295 | 1N4152R |
| All 1 CR701 | 152-0141-02 |  | SEMICOND DEVICE:SILICON, 30V,150MA | 01295 | 1N4152R |
| AllCR 702 | 152-0141-02 |  | SEmiCOND DEvice: Silicon, 30V, 150 MA | 01295 | 1N4152R |
| A11CR703 | 152-0141-02 |  | SEMICOND DEVICE:SILICON, 30V, 150MA | 01295 | 1N4152R |
| Al1CR705 | 152-0141-02 |  | SEMICOND DEVICE:SILICON, 30v,150MA | 01295 | 1N4152R |
| Allcrio6 | 152-0141-02 |  | SEMICOND DEVICE:SILICON, 30V,150MA | 01295 | 1N4152R |
| Allds618 | 150-1029-00 |  | LT Emittinc dio:Green, $565 \mathrm{NM}, 35 \mathrm{MA}$ | 53184 | xC209C |
| Al1J1000 | 131-0608-00 |  | TERMINAL, PIN: $0.365 \mathrm{~L} X 0.025 \mathrm{PH}$ BRZ GOLD (QTY 4) | 22526 | 47357 |
| A11J2000 | 131-0787-00 |  | CONTACT, ELEC:0.64 INCH LONG (QTY 10) | 22526 | 47359 |
| Al1R100 | 307-0107-00 |  | RES. , FXD, CMPSN: 5.6 OHM , 5\%,0.25W | 01121 | CB56G5 |
| Al1R101 | 315-0750-00 |  | RES. , FXD, CMPSN: 75 OHM $, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB7505 |
| A11R102 | 315-0105-00 |  | RES. , FXD, CMPSN: 1 M OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1055 |
| A11R190 | 311-2147-00 |  | RES. , VAR, NONWIR: PNL, 5R OHM, 20\%, 0.50W | 01121 | W8615 |
| AllR191 | 321-0257-00 |  | RES., FXD, FILM:4.64K OHM, 1\%,0.125W | 91637 | MFF1816G46400F |
| Allr 200 | 307-0107-00 |  | RES., FXD, CMPSN: 5.6 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB56G5 |
| AllR201 | 315-0750-00 |  | RES., FXD, CMPSN: 75 OHM, $5 \%, 0.25 \mathrm{H}$ | 01121 | CB7505 |
| All R202 | 315-0105-00 |  | RES., FXD, CMPSN: 1 M OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1055 |
| Al1R290 | 311-2147-00 |  | RES. , VAR, NONWIR: PNL, 5K OHM, 20\%, 0.50 W | 01121 | W8615 |
| A11R291 | 321-0257-00 |  | RES., FXD, FILM:4.64R OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G46400F |
| AllR395 | 311-2147-00 |  | RES. , VAR, NONWIR: PNL, 5K OHM, 20\%, 0. 50W | 01121 | W8615 |
| AllR401 | 315-0820-00 |  | RES., FXD, CMPSN: 82 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB8205 |
| A11R402 | 321-0807-00 |  | RES., FKD, FILM:900K OHM, 1\%,0.125W | 91637 | HFFl104F90002F |
| A11R403 | 321-0617-00 |  | RES.,FXD,FILM:111K OHM, 1\%,0.125w | 91637 | MFF1816G11102F |
| A11R404 | 321-0468-00 |  | RES., FXD, FILM: 732 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 24546 | NA4D7323F |
| Al1R405 | 315-0101-00 |  | RES., FXD, CMPSN: 100 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1015 |
| A11R455 | 311-2149-00 |  | RES , , VAR, NONWIR: PNL, 250 OHM, 20\%, 0. 50W | 01121 | W8612 |
| AllR 530 | 315-0124-00 | в010100 в017399 | RES., FXD, CMPSN: 120 K OHM, $5 \mathrm{~F}, 0.25 \mathrm{H}$ | 01121 | CB1245 |
| A11R530 | $315-0114-00$ | в017400 | RES., FXD, CMPSN: 110 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1145 |
| Al1R531 | 315-0124-00 |  | RES. , FXD, CMPSN: 120 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1245 |
| Al1R532 | 315-0434-00 |  | RES., FXD, CMPSN: 430 K OHM $, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4345 |
| Al18536 | 315-0182-00 |  | RES., FXD, CMPSN: 1.8 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | Cb1825 |
| A118537 | 321-0239-00 |  | RES., FXD, FILM 3.01 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | 7 MFF1816C30100F |
| Al1R538 | 321-0126-00 |  | RES., FXD, FILM: 200 OHM, 1\%, 0.125 W | 91637 | 7 MFF1816C200R0F |
| A118557 | 311-2148-00 |  | RES. ,VAR, NONWIR : PNL, 20K OHM, 20\%, 0. 50W | 01121 | W8616 |
| Al1 1845 | 315-0912-00 |  | RES., FXD, CMPSN: 9.1 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | Cb9125 |
| Al1R646 | 315-0123-00 |  | RES. , FXD, CMPSN: 12 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1235 |
| A11R726 | 311-2147-00 |  | RES. , VAR, NONWIR: PNL, 5 K OHM, 20\%, 0.50 W | 01121 | W8615 |
| A11R807 | 311-2147-00 |  | RES., VAR, NONWIR: PNL, 5 K OHM, 20\%,0.50W | 01121 | 1 W8615 |
| All 8808 | 315-0512-00 |  | RES. , FXD , CMPSN: 5.1 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5125 |
| AllR891 | 311-1562-00 |  | RES., VAR, NONWIR: 2 K OHM, 20\%,0.50W | 73138 | 8 91-84-0 |


| Component No. | Tektronix <br> Part No. | Serial/ Model No. <br> Eff | Dscont |
| :--- | :---: | :---: | :--- | :--- | :--- | :--- |

## Replaceable Electrical Parts-2215 Service

| Component No. | Tektronix Part No. | Serial/Model No. Eff Dscont | Name \& Description | Mtr Code | Mfr Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A12 | 670-6868-00 | B010100 в019849 | CKT BOARD ASSY:ATTEN/SWEEP | 80009 | 670-6868-00 |
| A12 | 670-6868-01 | B019850 | CKT Board assy:atten/SWEEP | 80009 | 670-6868-01 |
| Al2C104 | 281-0078-00 |  | CAP., VAR, AIR DI: 1.4-7.3PF, 750 V | 74970 | 189-0503-075 |
| A12C105 | 281-0214-00 |  | CAP., VAR, CER DI:0.5-3PF,400V | 80031 | 2502A0R503VP02F0 |
| A12C107 | 283-0154-00 |  | CAP., FXD, CER DI: $22 \mathrm{PF}, 5 \%, 50 \mathrm{~V}$ | 72982 | 8111b061C0G220J |
| Al2C110 | 281-0078-00 |  | CAP., VAR, AIR DI: 1.4 -7.3PF, 750 V | 74970 | 189-0503-075 |
| A12C111 | 281-0214-00 |  | CAP., VAR, CER DI: 0.5 -3PF, 400 V | 80031 | 2502A0R 503VP02F0 |
| A12C112 | 283-0108-00 |  | CAP., FXD, CER DI: $220 \mathrm{PF}, 10 \%$, 200V | 56289 | 272 Cl 3 |
| A12C119 | 283-0158-00 |  | CAP., FXD, CER DI: $19 \mathrm{~F}, 10 \%$, 50 V | 51642 | 100-050-NP0-109B |
| Al2C121 | 283-0000-00 |  | CAP. , FXD, CER DI $00.001 \mathrm{UF},+100-0 \%, 500 \mathrm{~V}$ | 59660 | 831-519-25U-102P |
| Al2C123 | 281-0775-00 |  | CAP., FXD, CER DI: $0.1 \mathrm{UF}, 20 \%$, 50 V | 04222 | SA205E104MAA |
| A12C1 25 | 283-0330-00 |  | CAP., FXD, CER DI: $100 \mathrm{PF}, 5 \%, 50 \mathrm{~V}$ | 51642 | 150-050-NP0-101J |
| Al2C132 | 290-0808-00 |  | CAP., FXD, ELCTLT: 2.7UF, 10\%, 20V | 56289 | 162D275x9020CD2 |
| Al2C133 | 283-0000-00 |  | CAP., FXD, CER DI: $0.001 \mathrm{UF},+100-0 \%, 500 \mathrm{~V}$ | 59660 | 831-519-25U-102P |
| A12C134 | 283-0000-00 |  | CAP., FXD, CER DI: $0.0010 \mathrm{~F},+100-0 \%, 500 \mathrm{~V}$ | 59660 | 831-519-25U-102P |
| A12C136 | 283-0220-00 |  | CAP , , FXD, CER DI: $0.01 \mathrm{UF}, 20 \%, 50 \mathrm{~V}$ | 72982 | 8121N075×7R0103M |
| A12C137 | 283-0220-00 |  | CAP., FXD, CER DI: $0.01 \mathrm{UF}, 20 \%$, 50V | 72982 | 8121N075X7R0103M |
| A12C139 | 283-0160-00 |  | CAP., FXD, CER DI: $1.5 \mathrm{PF}, 10 \%$, 50 V | 72982 | 8101A058C0K159B |
| A12C140 | 281-0775-00 |  | CAP., FXD, CER DI:0.1UF, $20 \%$, 50 V | 04222 | SA205E104MAA |
| Al2C141 | 283-0175-00 |  | CAP., FXD, CER DI: $10 \mathrm{PF}, 5 \%, 200 \mathrm{~V}$ | 72982 | $8101 \mathrm{B210c0g} 0100 \mathrm{~J}$ |
| Al2C142 | 283-0201-00 |  | CAP.,FXD, CER DI: $27 \mathrm{PF}, 10 \%$, 200V | 72982 | 8101B210x7R0270K |
| Al2C144 | 281-0775-00 |  | CAP., FXD, CER DI:0.1UF, 20\%,50V | 04222 | SA205E104MAA |
| A12C204 | 281-0078-00 |  | CAP.,VAR,AIR DI:1.4-7.3PF,750V | 74970 | 189-0503-075 |
| A12C205 | 281-0214-00 |  | CAP., VAR, CER DI: $0.5-3 \mathrm{PF}, 400 \mathrm{~V}$ | 80031 | 2502A0R503VP02F0 |
| A12C207 | 283-0154-00 |  | CAP., FXD, CER DI: $22 \mathrm{PF}, 5 \%$, 50V | 72982 | 81118061C0g220J |
| A12C210 | 281-0078-00 |  | CAP., VAR, AIR DI: 1.4-7.3PF,750V | 74970 | 189-0503-075 |
| Al2c211 | 281-0214-00 |  | CAP., VAR, CER DI: $0.5-3 \mathrm{PF}, 400 \mathrm{~V}$ | 80031 | 2502A0R503VP02F0 |
| A12C212 | 283-0108-00 |  | CAP., FXD, CER DI: 220PF, 10\%, 200V | 56289 | 272 Cl 3 |
| A12C219 | 283-0158-00 |  | CAP., FXD, CER DI: PFF, $10 \%$,50V | 51642 | 100-050-NP0-109B |
| A12C221 | 283-0000-00 |  | CAP., FXD, CER DI: $0.001 \mathrm{UF},+100-0 \%, 500 \mathrm{~V}$ | 59660 | 831-519-Z5U-102P |
| A12C225 | 283-0330-00 |  | CAP., FXD, CER DI: $100 \mathrm{PF}, 5 \%$, 50 V | 51642 | 150-050-NP0-101J |
| A12C232 | 290-0808-00 |  | CAP., FXD, ELCTLT: $2.7 \mathrm{UF}, 10 \%$, 20V | 56289 | 162D275X9020CD2 |
| A12C233 | 283-0000-00 |  | CAP., FXD, CER DI: $0.001 \mathrm{UF},+100-0 \%, 500 \mathrm{~V}$ | 59660 | 831-519-Z5U-102P |
| A12C234 | 283-0000-00 |  | CAP. , FXD, CER DI: $0.001 \mathrm{UF},+100-0 \%, 500 \mathrm{~V}$ | 59660 | 831-519-Z5U-102P |
| A12C236 | 283-0220-00 |  | CAP., FXD, CER DI:0.01UF, 20\%,50V | 72982 | 8121N075X7R0103M |
| A12C237 | 283-0220-00 |  | CAP., FXD, CER DI: $0.01 \mathrm{UF}, \mathbf{2 0 \% , 5 0 \mathrm { V }}$ | 72982 | $8121 \mathrm{~N} 075 \times 7 \mathrm{R} 0103 \mathrm{M}$ |
| A12C239 | 283-0160-00 |  | CAP., FXD, CER DI: $1.5 \mathrm{PF}, 10 \%$, 00 V | 72982 | 8101A058C0K159B |
| A12C241 | 283-0175-00 |  | CAP.,FXD, CER DI: $10 \mathrm{PF}, 5 \%, 200 \mathrm{~V}$ | 72982 | 8101b210c060100J |
| A12C242 | 283-0201-00 |  | CAP. , FXD, CER DI: $27 \mathrm{PF}, 10 \%$, 200V | 72982 | 8101B210x7R0270K |
| A12C244 | 281-0775-00 |  | CAP., FXD, CER DI:0.1UF, 20\%,50V | 04222 | SA205E104MAA |
| A12C625 | 283-0631-00 |  | CAP., FXD, MICA D: 95PF, 1\%,100V | 00853 | D151E950F0 |
| A12C626 | 281-0202-00 |  | CAP., VAR, PLSTC: $1.5-5.5 \mathrm{PF}, 100 \mathrm{~V}$ | 80031 | 2807C1R406MM02F |
| A12C628A, B, C, D | 295-0194-00 |  | Cap Set, Matched: 2 ea 1.OUF, 1.5\%,50V | 90201 | TTX $100+100$ |
| Al2C630 | 281-0811-00 |  | CAP., FXD, CER DI: $10 \mathrm{PF}, 10 \%, 100 \mathrm{~V}$ | 72982 | 8035D2AADC1G100K |
| A12C632 | 283-0158-00 |  | CAP., FXD, CER DI: $1 \mathrm{PF}, 10 \%, 50 \mathrm{~V}$ | 51642 | 100-050-NP0-109B |
| A12C636 | 283-0024-00 |  | CAP., FXD, CER DI: $0.1 \mathrm{UF},+80-20 \%, 50 \mathrm{~V}$ | 72982 | 8121 N0832500104Z |
| A12C675 | 283-0631-00 |  | CAP., FXD, MICA D: 95PF, 1\%,100V | 00853 | D151E950F0 |
| A12C676 | 281-0202-00 |  | CAP., VAR, PLSTC: 1.5-5.5PF, 100V | 80031 | 2807C1R406MM02F |
| Al2C677 | 281-0775-00 |  | CAP., FXD, CER DI: $0.1 \mathrm{UF}, 20 \%$, 50 V | 04222 | SA205E104MAA |
| A12C679 | 281-0775-00 |  | CAP., FXD, CER DI: $0.1 \mathrm{UF}, 20 \%$, 50 V | 04222 | SA205E104MAA |
| A12C680 | 281-0811-00 |  | CAP., FXD, CER DI: $10 \mathrm{PF}, 10 \%, 100 \mathrm{~V}$ | 72982 | 8035D2AADC1G100K |
| A12C720 | 281-0763-00 |  | CAP., FXD, CER DI: $47 \mathrm{PF}, 10 \%, 100 \mathrm{~V}$ | 72982 | 8035D9AADC1G470K |
| A12C732 | 281-0756-00 |  | CAP., FXD, CER DI: 2.2 PF, $0.5 \%, 200 \mathrm{~V}$ | 12969 | CGB2R2DFN |
| A12C734 | 281-0151-00 |  | CAP., VAR, CER DI: $1-3 \mathrm{PF}, 100 \mathrm{~V}$ | 72982 | 518-600A1-3 |
| A12C736 | 281-0756-00 |  | CAP., FXD, CER DI: $2.2 \mathrm{2PF}, 0.5 \%, 200 \mathrm{~V}$ | 12969 | CGR2R2DFN |
| A12C738 | 283-0023-00 |  | CAP., FXD, CER DI: 0.1 l | 91418 | MX0104Z1205R5 |
| A12C741 | 283-0023-00 |  | CAP., FXD, CER DI:0.1UF, +80-20\%, 12V | 91418 | MX010421205R5 |
| Al2Cr119 | 152-0246-00 |  | SEMICOND DEVICE:SW, SI, 40V, 200MA | 03508 | DE140 |


| Component No. | Tektronix Part No. | Serial/Model No. Eff Dscont | Name \& Description | Mfr Code | Mfr Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Al2CR219 | 152-0246-00 |  | SEMICOND DEVICE; $5 \mathrm{~W}, \mathrm{SI}, 40 \mathrm{~V}, 200 \mathrm{MA}$ | 03508 | DE 140 |
| A12CR626 | 152-0245-00 |  | SEMICOND DEVICE:SILICON, 10NA AT 5V | 12969 | NDP539 |
| A12CR630 | 152-0141-02 |  | SEMICOND DEVICE:SILICON, 30V, 150 MA | 01295 | 1N4152R |
| A12CR676 | 152-0245-00 |  | SEMTCOND DEVICE:SILICON, 10NA AT 5V | 12969 | NDP539 |
| Al2CR680 | 152-0141-02 |  | SEMICOND DEVICE:SILICON, 30V, 150MA | 01295 | 1 N4152R |
| Al 2P1010 | 131-0608-00 |  | TERMINAL,PIN: 0.365 L X 0.025 PH BRZ GOLD (QTY 4) | 22526 | 47357 |
| Al 2P 2000 | 136-0328-02 |  | SOCKET,PIN TERM:HORIZONTAL (QTY OF 10) | 00779 | 86282-2 |
| Al2P2010 | 131-0608-00 |  | TERMINAL,PIN:0.365 L X 0.025 PH BRZ GOLD (QTY 4) | 22526 | 47357 |
| Al2P6000 | 131-0608-00 |  | TERMINAL, PIN:0.365 L X 0.025 PH BRZ GOLD (QTY 10) | 22526 | 47357 |
| Al 2P 7000 | 131-0608-00 |  | TERMINAL, PIN:0.365 L X 0.025 PH BRZ GOLD (QTY 7) | 22526 | 47357 |
| A12Q122 | 151-1124-00 |  | TRANSISTOR: JFE, N-CHAN, SI, SEL | 22229 | F2942 |
| A12Q125 | 151-0711-00 |  | TRANSISTOR: SILICON, NPN | 04713 | SPS8224 |
| A12Q133 | 151~0711-00 |  | TRANSISTOR:SILICON, NPN | 04713 | SPS8224 |
| A12Q134 | 151-0712-00 |  | TRANSISTOR: SILICON, NPN | 04713 | SPS8223 |
| Al2Q1 39 | 151-0216-00 |  | TRANSISTOR: SILICON, PNP | 04713 | SPS8803 |
| A12Q222 | 151-1124-00 |  | TRANSISTOR: JFE, N-CHAN, SI, SEL | 22229 | F2942 |
| A12Q225 | 151-0711-00 |  | TRANSISTOR: SILICON, NPN | 04713 | SPS8224 |
| A12Q233 | 151-0711-00 |  | TRANSISTOR: SILICON, NPN | 04713 | SPS8224 |
| A12Q234 | 151-0712-00 |  | TRANS ISTOR: SILICON, NPN | 04713 | SPS8223 |
| Al 2 Q239 | 151-0216-00 |  | TRANSISTOR: SILICON, PNP | 04713 | SPS8803 |
| Al2Q629 | 151-0188-00 |  | TRANSISTOR:SILICON, PNP | 04713 | SPS6868K |
| Al2Q630A, B | 151-1042-00 |  | SEMICOND DVC SE:MATCHED PAIR FET | 01295 | SKA5390 |
| A12Q631 | 151-0190-00 |  | TRANSISTOR: SILICON, NPN | 07263 | S032677 |
| A12Q634 | 151-0736-00 |  | TRANSISTOR: SILICON, NPN | $04713$ | SPS8317 |
| A12Q680A, B | 151-1042-00 |  | SEMICOND DVC SE:MATCHED PAIR FET | 01295 | SKA5390 |
| Al2Q681 | 151-0190-00 |  | TRANSISTOR: SILICON, NPN | 07263 | S032677 |
| Al2Q684 | 151-0736-00 |  | TRANSISTOR: SILICON, NPN | 04713 | SPS8317 |
| Al2Q720 | 151-0190-00 |  | TRANSISTOR: SILICON, NPN | 07263 | S032677 |
| A12Q730 | 151-0712-00 |  | TRANSISTOR:S ILICON, NPN | 04713 | SPS8223 |
| A12Q731 | 151-0712-00 |  | TRANS ISTOR: SILICON, NPN | 04713 | SPS8223 |
| Al2Q736 | 151-0711-00 |  | TRANSISTOR: SILICON, NPN | 04713 | SPS8224 |
| Al2R103 | 315-0240-00 |  | RES.,FXD, CMPSN: 24 OHM, 5\%,0.25W | 01121 | CB2405 |
| Al 2R105 | 321-0807-01 |  | RES., FXD, FILM $: 900 \mathrm{~K}$ OHM, $0.5 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G90002D |
| Al2R106 | 317-0330-00 |  | RES . , FXD, CMPSN: 33 OHM, $5 \%, 0.125 \mathrm{~W}$ | 01121 | BB3305 |
| Al2R107 | 321-1389-01 |  | RES., FXD, FILM: 111 K OHM, $0.5 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816Gl1102D |
| Al2R108 | 315-0620-00 |  | RES., FXD, CMPSN: 62 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB6205 |
| Al2R110 | 315-0101-00 |  | RES., FXD , CMPSN: 100 OHM $, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1015 |
| Al 2R111 | 321-0790-01 |  | RES., FXD, FILM:990K OHM, $0.5 \%, 0.125 \mathrm{~W}$ | 91637 | HFFl104G99002D |
| A12R112 | 315-0120-00 |  | RES., FXD, CMPSN: 12 OHM, 5\%,0.25W | 01121 | CB1205 |
| Al2R114 | 321-1289-01 |  | RES., FXD, FILM: $10.1 \mathrm{~K} \mathrm{OHM}, 0.5 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G10101D |
| Al2R115 | 315-0910-00 |  | RES. , FXD, CMPSN: 91 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB9105 |
| Al2R116 | 321-0385-04 |  | RES., FXD, FILM: $100 \mathrm{~K} 0 \mathrm{HM}, 0.1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816D10002B |
| Al2R117 | 321-0807-04 |  | RES., FXD, FILM:900K 0HM, $0.1 \%, 0.125 \mathrm{~W}$ | 24546 | NC55C9003B |
| Al2R119 | 315-0107-00 |  | RES., FXD, CMPSN: 100 M OHM , $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1075 |
| Al2R120 | 315-0104-00 |  | RES. , FXD, CMPSN: 100K OHM $, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1045 |
| Al2R121 | 315-0435-00 |  | RES., FXD, CMPSN: 4. 3M OHM , $5 \%, 0.25 \mathrm{~W}$ | 01121 | 1 CB4355 |
| Al2R122 | 301-0122-00 |  | RES., FXD, CMPSN:1.2K OHM, $5 \%, 0.50 \mathrm{~W}$ | 01121 | EB1225 |
| Al2R123 | 315-0100-00 |  | RES. ,FXD, CMPSN: 10 OHM , 5\%,0.25W | 01121 | 1 CB1005 |
| Al2R124 | 315-0102-00 |  | RES., FXD, CMPSN: 1 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1025 |
| Al2R125 | 321-0131-00 |  | RES.,FXD, FILM: 226 OHM, 1\%,0.125W | 91637 | 7 MFF1816G226R0F |
| Al2R126 | 321-0126-00 |  | RES., FXD, FILM: 200 OHM, 1\%,0.125W | $91637$ | 7 MFF1816G200R0F |
| Al2R127 | 315-0470-00 |  | RES., FXD , CMPSN: 47 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | 1 CB4705 |


| Component No. | Tektronix Part No. | Serial/Model No. Eff Dscont | Name \& Description | Mrr Code | Mfr Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A12R128 | 315-0102-00 |  | RES.,FXD,CMPSN: 1 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1025 |
| Al2R130 | 315-0182-00 |  | RES., FXD, CMPSN: 1.8 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1825 |
| Al2R131 | 321-0254-00 |  | RES.,FXD,FILM:4.32K OHM, 1\%,0.125W | 91637 | MFF1816G43200F |
| A12R132 | 321-0229-00 |  | RES.,FXD,FILM: 2.37K 0HM,1\%,0.125W | 91637 | MFF1816G23700F |
| A12R133 | 315-0101-00 |  | RES., FXD, CMPSN: $100 \mathrm{OHM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1015 |
| A12R134 | 315-0680-00 |  | RES., FXD, CMPSN: 68 OHM , $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB6805 |
| Al2R135 | 315-0472-00 |  | RES., FXD, CMPSN: 4.7 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4725 |
| A12R136 | 307-0106-00 |  | RES., FXD, CMPSN:4.7 0HM , 5\%, 0.25 W | 01121 | cb4765 |
| A12R137 | 315-0100-00 |  | RES., FXD, CMPSN: 10 OHM, 5\%,0.25W | 01121 | CB1005 |
| Al2R138 | 311-1559-00 |  | RES., VAR, NONWIR: 10 K OHM, $20 \%, 0.50 \mathrm{~W}$ | 73138 | 91-81-0 |
| A12R139 | 307-0710-00 |  | RES NTWK, FXD, FI:ATTENUATOR | 80009 | 307-0710-00 |
| Al2R140 | 315-0132-00 |  | RES., FXD, CMPSN: 1.3 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1325 |
| A12R141 | 311-2133-00 |  | RES. ,VAR, NONWIR:PNL, 500 OHM, $20 \%, 0.50 \mathrm{~W}$ | 12697 | CM41766 |
| A12R142 | 315-0304-00 |  | RES., FXD, CMPSN: 300 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB3045 |
| A12R143 | 315-0301-00 |  | RES. , FXD , CMPSN: 300 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB3015 |
| A12R144 | 315-0111-00 |  | RES. , FXD, CMPSN: 110 OHM , 5\%, 0.25 W | 01121 | CB1115 |
| A12R145 | 311-1562-00 |  | RES., ,VAR, NONWIR: 2 K OHM, 20\%,0.50W | 73138 | 91-84-0 |
| A12R 146 | 311-0607-00 |  | RES., VAR, NONWIR:10K 0 HM, $10 \%, 0.50 \mathrm{~W}$ | 73138 | 82-25-2 |
| Al2R147 | 315-0393-00 |  | RES., FXD, CMPSN: 39 K OHM $, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB3935 |
| A12R148 | 315-0111-00 |  | RES., FXD, CMPSN:110 OHM,5\%,0.25W | 01121 | CB1115 |
| Al2R149 | 315-0101-00 |  | RES., FXD, CMPSN: 100 OHM, 5\%,0.25W | 01121 | CB1015 |
| A12R203 | 315-0240-00 |  | RES.,FXD, CMPSN: 24 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB2405 |
| Al2R205 | 321-0807-01 |  | RES., FXD, FILM:900K $0 \mathrm{HM}, 0.5 \%, 0,125 \mathrm{~W}$ | 91637 | MFF1816G90002D |
| A12R206 | 317-0330-00 |  | RES., FXD, CMPSN: 33 OHM, $5 \%, 0.125 \mathrm{~W}$ | 01121 | BB3305 |
| Al2R207 | 321-1389-01 |  | RES.,FXD,FILM:111K OHM, 0.5\%,0.125W | 91637 | MFF1816G11102D |
| Al2R208 | 315-0620-00 |  | RES., FXD, CMPSN: 62 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB6205 |
| A12R210 | 315-0101-00 |  | RES., FXD, CMPSN: 100 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1015 |
| A12R211 | 321-0790-01 |  | RES.,FXD,FILM:990K 0HM, $0.5 \%, 0.125 \mathrm{~W}$ | 91637 | HFFl104G99002D |
| Al2R212 | 315-0120-00 |  | RES., FXD, CMPSN: 12 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1205 |
| A12R214 | 321-1289-01 |  | RES.,FXD, FILM: 10.1 K OHM,0.5\%,0.125W | 91637 | MFF1816G10101D |
| A12R215 | 315-0910-00 |  | RES., FXD, CMPSN: 91 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB9105 |
| A12R216 | 321-0385-04 |  | RES.,FXD,FILM: 100 K OHM, $0.1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816D10002B |
| A12R217 | 321-0807-04 |  | RES.,FXD,FILM:900K OHM, $0.1 \%, 0.125 \mathrm{~W}$ | 24546 | NC55C9003B |
| Al2R219 | 315-0107-00 |  | RES., FXD, CMPSN: 100 M 0HM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1075 |
| A12R220 | 315-0104-00 |  | RES., FXD,CMPSN: $100 \mathrm{~K} 0 \mathrm{OM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1045 |
| Al2R221 | 315-0435-00 |  | RES., FXD, CMPSN: 4.3 M OHM $, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4355 |
| Al2R222 | 301-0122-00 |  | RES., FXD, CMPSN: 1.2 K OHM, $5 \%, 0.50 \mathrm{~W}$ | 01121 | EB1225 |
| A12R225 | 321-0131-00 |  | RES., FXD, FILM: 226 OHM, 1\%,0.125 W | 91637 | MFF1816G226R0F |
| Al2R226 | 321-0126-00 |  | RES., FXX, FILM:200 ОHM, 1\%,0.125W | 91637 | MFF1816G200R0F |
| A12R227 | 315-0470-00 |  | RES.,FXD,CMPSN:47 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4705 |
| A12R231 | 321-0254-00 |  | RES., FXD, FILM 4.32 K OHM , $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G43200F |
| A12R232 | 321-0229-00 |  | RES., FXD, FILM:2.37K 0 HM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFFI816G23700F |
| Al2R233 | 315-0101-00 |  | RES., FXX, CMPSN: 100 OHM , $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1015 |
| Al2R234 | 315-0680-00 |  | RES., FXD, CMPSN: 68 ОНM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB6805 |
| A12R235 | 315-0472-00 |  | RES. , FXD, CMPSN: 4.7 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4725 |
| A12R236 | 307-0106-00 |  | RES., FXD, CMPSN:4.7 ОHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4765 |
| A12R237 | 315-0100-00 |  | RES., FXD, CMPSN: 10 OHM, 5\%,0.25W | 01121 | CB1005 |
| A12R238 | 311-1559-00 |  | RES., VAR, NONWIR: 10 K OHM, $20 \%, 0.50 \mathrm{~W}$ | 73138 | 91-81-0 |
| Al2R239 | 307-0710-00 |  | RES NTWK, FXD, FI: Attenuator | 80009 | 307-0710-00 |
| A12R240 | 315-0132-00 |  | RES., FXD, CMPSN: 1.3 K ОНM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1325 |
| A12R241 | 311-2133-00 |  | RES., VAR, NONWIR: PNL, 500 OHM, 20\%, 0, 50W | 12697 | CM4 1766 |
| A12R242 | 315-0304-00 |  | RES., FXD, CMPSN: 300 R OHM , $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB3045 |
| A12R243 | 315-0301-00 |  | RES., FXD, CMPSN: 300 0HM, 5\%,0.25W | 01121 | CB3015 |
| A12R244 | 315-0111-00 |  | RES., FXD, CMPSN: 110 OHM, 5\%,0.25W | 01121 | CB1115 |
| Al2R245 | 311-0609-00 |  | RES.,VAR, NONWIR: 2 K OHM, $10 \%$,0.50W | 73138 | 82-26-1 |
| A12R246 | 311-0607-00 |  | RES., VAR, NONWIR: 10 K OHM, $10 \%, 0.50 \mathrm{~W}$ | 73138 | 82-25-2 |
| A12R247 | 315-0393-00 |  | RES., FXD, CMPSN: 39 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB 3935 |


| Component ${ }^{\text {No. }}$ | Tektronix Part No. | Serial/Model No. Eff Dscont | Name \& Description | Mfr Code | Mfr Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Al2R248 | 315-0111-00 |  | RES., FXD, CMPSN: $1100 \mathrm{HM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1115 |
| A12R249 | 315-0101-00 |  | RES., FXD, CMPSN: 100 OHM, 5\%,0.25 | 01121 | CB1015 |
| Al2R625 | 322-0519-01 |  | RES. , FXD, FILM: 2.49 M OHM $, 0.5 \%, 0.25 \mathrm{~W}$ | 91637 | hFF143G24903D |
| Al2R626 | 307-0780-00 |  | RES NTWK, FXD, FI: TIMING | 80009 | 307-0780-00 |
| Al 2 R627 | 315-0241-00 |  | RES., FXD, CMPSN: 240 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB2415 |
| Al 2 R 628 | 315-0100-00 |  | RES. , FXD, GMPSN: 10 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1005 |
| A12R629 | 311-2151-00 |  | RES. , VAR, NONWIR: PNL, 500 OHM, $20 \%, 0.5 W$, DFST | 12697 | OBD |
|  |  |  | (PART OF S734) |  |  |
| Al 2R630 | 315-0510-00 |  | RES., FXD, CMPSN: 51 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5105 |
| A12R631 | 301-0242-00 |  | RES., FXD, CMPSN:2.4K OHM, 5\%,0.50W | 01121 | EB2425 |
| Al2R632 | 315-0114-00 |  | RES.,FXD,CMPSN: $110 \mathrm{~K} 0 \mathrm{HM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1145 |
| A12R633 | 321-0232-00 |  | RES.,FXD,FILM: 2.55 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G25500F |
| Al 2 R634 | 321-0232-00 |  | RES.,FXD,FILM: 2.55 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G25500F |
| Al 2R635 | 315-0102-00 |  | RES., FXD, CMPSN: 1 K 0HM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1025 |
| Al2R636 | 307-0107-00 |  | RES., FXD, CMPSN: 5.6 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB56G5 |
| A12R676 | 307-0780-00 |  | RES NTWK, FXD, FI: TIMING | 80009 | 307-0780-00 |
| Al2R677 | 307-0107-00 |  | RES.,FXD,CMPSN: 5.6 OHM, $5 \%, 0,25 \mathrm{~W}$ | 01121 | CB56G5 |
| Al2R678 | 315-0100-00 |  | RES.,FXD, CMPSN: 10 OHM, 5\%, 0.25W | 01121 | CB1005 |
| A12R679 | 307-0107-00 |  | RES., FXD, CMPSN: 5.6 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB56G5 |
| Al2R680 | 315-0510-00 |  | RES., FXD, CMPSN: 51 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5105 |
| Al2R681 | 301-0242-00 |  | RES.,FXD, CMPSN: 2.4K OHM, 5\%,0.50W | 01121 | EB2425 |
| Al2R682 | 311-1248-00 |  | RES., VAR, NONWIR: 500 OHM, $10 \%, 0.50 \mathrm{~W}$ | 73138 | 72-23-0 |
| A12R683 | 321-0228-00 |  | RES., FXD, FILM: 2.32 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G23200F |
| A12R684 | 321-0232-00 |  | RES.,FXD, FILM: 2.55 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G25500F |
| Al2R685 | 315-0102-00 |  | RES., FXD, CMPSN:1K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1025 |
| A12R686 | 315-0682-00 |  | RES., FXD, cMPSN: 6.8 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB6825 |
| Al2R691 | 315-0470-00 |  | RES. , FXD, CMPSN: 47 OHM , 5\%, 0.25 W | 01121 | CB4705 |
| Al2R720 | 315-0221-00 |  | RES.,FXD,CMPSN: 220 OHM $, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB2215 |
| A12R721 | 315-0101-00 |  | RES., FXD, CMPSN: 100 OHM, 5\%,0.25W | 01121 | CB1015 |
| Al2R722 | 315-0152-00 |  | RES., FXD, CMPSN: 1.5 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1525 |
| A12R723 | 315-0682-00 |  | RES., FXD, CMPSN:6.8K ОНм, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB6825 |
| A12R724 | 315-0392-00 |  | RES., FXD, CMPSN:3.9K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB3925 |
| Al2R725 | 315-0822-00 |  | RES., FXD, CMPSN: 8.2 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB8225 |
| Al2R728 | 315-0822-00 |  | RES.,FXD,CMPSN: 8.2 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB8225 |
| Al2R729 | 321-0159-00 |  | RES.,FXD,FILM:442 OHM, 1\%,0.125 | 91637 | MFF1816G442R0F |
| A12R730 | 315-0561-00 |  | RES.,FXD, CMPSN: 560 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5615 |
| Al2R731 | 315-0911-00 |  | RES., FXD, CMPSN: 910 0HM, 5\%,0.25W | 01121 | CB9115 |
| Al 2 R 732 | 321-0206-00 |  | RES., FXD, FILM: 1.37 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G13700F |
| A12R733 | 311-1562-00 |  | RES., VAR, NONWIR: 2 K OHM, $20 \%, 0.50 \mathrm{~W}$ | 73138 | 91-84-0 |
| Al2R734 | 321-0295-00 |  | RES.,FXD,FILM:11.5K $0 \mathrm{HM}, 1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G11501F |
| A12R736 | 315-0272-00 |  | RES., FXD, CMPSN:2.7K OHM, 5\%,0.25W | 01121 | CB2725 |
| A12R737 | 315-0362-00 |  | RES., FXD, CMPSN:3.6K ОHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | Св3625 |
| Al2R738 | 315-0100-00 |  | RES. , FXD , CMPSN: 10 OHM, 5\%, 0. 25 W | 01121 | CB1005 |
| Al2R739 | 315-0560-00 |  | RES. , FXD, CMPSN: 56 OHM , $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5605 |
| Al2R741 | 315-0100-00 |  | RES. , FXD, GMPSN: 10 OHM, 5\%, 0.25 W | 01121 | CB1005 |
| Al 2RT144 | 307-0125-00 |  | RES., THERMAL: 500 OHM, $10 \%, 25$ DEG C | 50157 | 2 D 1595 |
| Al2RT244 | 307-0125-00 |  | Res., thermal: 500 Ohm, $10 \%, 25$ deg C | 50157 | 2 D 1595 |
| A12S734 | ----- ---- |  | (PART OF R629) |  |  |
| A12S105A, B | 260-2025-00 |  | SWITCH, ROTARY: VERTICAL ATTENUATOR | 80009 | 260-2025-00 |
| A12S205A, B | 260-2025-00 |  | SWITCH, ROTARY: VERTICAL ATTENUATOR | 80009 | 260-2025-00 |
| Al2S630A, B , C | 260-2023-00 |  | SWITCH, ROTARY:TME/DIV A/B | 80009 | 260-2023-00 |
| Al 2 U 120 | 156-1551-00 |  | MICROCIRCUIT, LI : OPNL AMPL | 02735 | 90593 |
| Al 2 U145 | 155-0227-00 |  | MICROCIRCUIT, LI: VERTICAL PREAMP | 80009 | 155-0227-00 |
| A12U220 | 156-1551-00 |  | MICROCIRCUIT, LI: OPNL AMPL | 02735 | 90593 |
| A120245 | 155-0227-00 |  | microcircuit, li : VErtical preamp | 80009 | 155-0227-00 |
| A12VR122 | 152-0168-00 |  | SEMICOND DEVICE:ZENER, $0.4 \mathrm{~W}, 12 \mathrm{~V}, 5 \%$ | 04713 | SZG35009K4 |
| Al2VR130 | 152-0217-00 |  | SEMICOND DEVICE:ZENER,0.4W,8.2V,5\% | 04713 | SZG20 |


| Component No. | Tektronix Part No. | Serial/Model No. Eff Dscont | Name \& | Mtr Code | Mfr Part Numb |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A12VR222 | 152-0168-00 |  | SEMICOND DEVICE:ZENER, $0.4 \mathrm{~W}, 12 \mathrm{~V}, 5 \%$ | 04713 | SZG35009K4 |
| A12VR629 | 152-0647-00 |  | SEMICOND DEVICE:ZENER, $0.4 \mathrm{~W}, 6.8 \mathrm{~V}, 5 \%$ | 04713 | SZG35014k3 |
| A12W116 | 131-0566-00 |  | bus Conductor: DUMMY RES, $2.375,22$ AWG | 55210 | L-2007-1 |


| Component No. | Tektronix Part No. | Serial/Model No. Eff Dscont | Name \& Description | Mfr Code | Mir Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A13 | 670-6869-00 |  | CKT BOARD ASSY:ALTERNATE SWEEP | 80009 | 670-6869-00 |
| Al3C554 | 281-0862-00 |  | CAP., FXD , CER DI: $0.001 \mathrm{UF},+80-20 \%, 100 \mathrm{~V}$ | 20932 | 401-ES-100AD1022 |
| Al3C556 | 281-0773-00 |  | CAP., FXD, CER DI:0.01UF, $10 \%$, 100 V | 04222 | Gc70-1c103k |
| A13C566 | 281-0615-00 |  | CAP., FXD, CER DI: $3.9 \mathrm{PF},+/-0.5 \mathrm{PF}, 200 \mathrm{~V}$ | 59660 | 374001C0J0399D |
| A13C584 | 281-0773-00 |  | CAP., FXD, CER DI: $0.01 \mathrm{UF}, 10 \%, 100 \mathrm{~V}$ | 04222 | GC70-1C103K |
| A13C585 | 281-0775-00 |  | CAP., FXD, CER DI:0.1UF, $20 \%$, 50 V | 04222 | SA205E104MAA |
| A13C657 | 281-0615-00 |  | CAP., FXD, CER DI: $3.9 \mathrm{PF},+/-0.5 \mathrm{SF}, 200 \mathrm{~V}$ | 59660 | 374001C0J0399D |
| A13C659 | 290-0776-00 | XB010685 | CAP., FXD, ELCTLT: 22UF, $+50-10 \%$, 10V | 55680 | 10ULA22V-T |
| A13C664 | 281-0786-00 |  | CAP., FXD, CER DI: $150 \mathrm{PF}, 10 \%, 100 \mathrm{~V}$ | 72982 | 8035D2AADX5P151K |
| A13C690 | 281-0770-00 |  | CAP., FXD, CER OI:0.001UF, 20\%, 100V | 72982 | 8035D9AADX5R102M |
| Al 3C693 | 281-0775-00 |  | CAP., FXD, CER DI:0.1UF, 20\%, 50V | 04222 | SA205E104MAA |
| A13CR662 | 152-0141-02 |  | SEMICOND DEVICE: SILICON, 30v,150MA | 01295 | 1 N 4152 R |
| Al3CR669 | 152-0141-02 |  | SEMICOND OEVICE:SILICON, 30V, 150 MA | 01295 | 1N4152R |
| A13CR670 | 152-0141-02 |  | SEMICOND DEVICE:SILICON, 30V, 150 MA | 01295 | 1N4152R |
| Al3CR671 | 152-0141-02 |  | SEMICOND DEvICE:SILICON, 30V,150MA | 01295 | 1N4152R |
| Al3CR672 | 152-0141-02 |  | SEMICOND DEVICE:SILICON, 30V,150MA | 01295 | 1N4152R |
| A13Q573 | 151-0435-00 |  | TRANSISTOR:SILICON, PNP | 04713 | SPS8335 |
| A13Q592 | 151-0199-00 |  | TRANSISTOR:SILICON, PNP | 04713 | SPS6866K |
| A13Q593 | 151-0199-00 |  | TRANSISTOR:SILICON, PNP | 04713 | SPS6866K |
| A130662 | 151-0190-00 |  | TRANSISTOR:SILICON, NPN | 07263 | 5032677 |
| A130664 | 151-0190-00 |  | TRANS ISTOR:SILICON,NPN | 07263 | 5032677 |
| A130690 | 151-0190-00 |  | TRANSISTOR:SILICON, NPN | 07263 | S032677 |
| Al3R552 | 315-0510-00 |  | RES. , FXD, CMPSN: 51 OHM, 5\%, 0. 25W | 01121 | CB5105 |
| A13R553 | 315-0472-00 |  | RES., FXD, CMPSN: 4.7 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4725 |
| A13R554 | 315-0391-00 |  | RES., FXD, CMPSN: 390 OHM, 5\%, 0.25 W | 01121 | CB3915 |
| Al 3R555 | 315-0302-00 |  | RES. , FXD, CMPSN: 3K OHM , 5\%,0.25W | 01121 | CB3025 |
| Al3R556 | 315-0100-00 |  | RES. , FXD, CMPSN: 10 OHM, 5\%,0.25W | 01121 | CB1005 |
| Al3R 560 | 321-0207-00 |  | RES., FXD, FILM: 1.4 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF 1816 Gl 14000 F |
| Al3R561 | 321-0197-00 |  | RES., FXD, FILM: 1.1 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G11000F |
| Al 3R 562 | 321-0203-00 |  | RES.,FXD, FILM: $1.27 \mathrm{X} 0 \mathrm{HM}, 1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G12700F |
| Al3R563 | 321-0201-00 |  | RES., FXD, FILM: $1.21 \mathrm{~K} 0 \mathrm{HM}, 1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G12100F |
| Al3R564 | 315-0242-00 |  | RES. , FKD, CMPSN: 2.4 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB2425 |
| A13R566 | 315-0101-00 |  | RES . , FKd, CMPSN: 100 OHM, 5\%,0.25W | 01121 | CB1015 |
| A13R567 | 315-0821-00 |  | RES. , FXD, CMPS $\mathrm{N}: 820$ OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB8215 |
| Al3R569 | 315-0123-00 |  | RES., FKO,CMPSN: 12K 0HM, 5\%,0.25W | 01121 | CB1235 |
| Al3R571 | 315-0104-00 |  | RES., FXD, CMPSN: 100K OHM , 5\%,0.25W | 01121 | CB1045 |
| A13R573 | 315-0102-00 |  | RES., FXI, CMPSN: 1K OHM , 5\%, 0.25W | 01121 | CB1025 |
| Al3R574 | 315-0185-00 |  | RES., FXD,CMPSN: 1.8 MM OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1855 |
| A13R575 | 315-0512-00 |  | RES. , FXD, CMPSN: 5.1K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5125 |
| Al3R579 | 315-0511-00 |  | RES. , FXD, CMPSN: 510 OHM, 5\%, 0.25W | 01121 | CB5115 |
| A13R581 | 321-0191-00 |  | RES., FXD, FILM:953 0HM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G953R0F |
| Al 3R582 | 315-0820-00 |  | RES. , FXD, CMPSN: 82 OHM, 5\%,0.25W | 01121 | CB8205 |
| Al3R 584 | 301-0470-00 |  | RES. , FXD , CMPSN: 47 OHM, 5\%, 0.50W | 01121 | EB4705 |
| Al3R585 | 315-0511-00 |  | RES. , FXD, CMPSN: 510 0HM, 5\%,0.25W | 01121 | CB5115 |
| Al3R587 | 315-0511-00 |  | RES., FXD, CMPSN: 510 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5115 |
| Al3R590 | 315-0511-00 |  | RES. , FXD, CMPSN: 510 OHM , $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5115 |
| A13R591 | 315-0361-00 |  | RES. , FXD, CMPSN: $3600 \mathrm{HM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB3615 |
| A1 3R593 | 315-0242-00 |  | RES., FXD, CMPSN: 2.4 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB2425 |
| Al3R594 | 315-0361-00 |  | RES. , FXD, CMPSN: 360 OHM, 5\%,0.25W | 01121 | CB3615 |
| Al3R641 | 315-0102-00 |  | RES., FXD, CMPSN: 1K ОHM, 5\%,0.25W | 01121 | CB1025 |
| Al 3R644 | 315-0752-00 | B010100 B010684 | RES., FXD, CMPSN:7.5K OHM, 5\%,0.25W | 01121 | CB7525 |
| Al3R644 | 315-0102-00 | B010685 | RES., FXD, CMPSN: 1 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1025 |
| Al 3R648 | 315-0331-00 |  | RES. , FXD, CMPSN: 330 OHM, 5\% , 0.25 W | 01121 | CB3315 |
| A1 3R650 | 315-0392-00 |  | RES., FXD, CMPSN: $3.9 \mathrm{~K} 0 \mathrm{HM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB3925 |
| A13R652 | 315-0162-00 |  | RES., FXD, CMPSN: 1.6 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1625 |
| Al3R653 | 321-0271-00 |  | RES., FXD, FILM: 6.49 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G64900F |
| Al3R654 | 311-1238-00 |  | RES.,VAR, NONWIR:5K 0HM, 10\%,0.50W | 73138 | 72-27-0 |


|  | Tektronix | Serial/Model No. |  | Mfr |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Component No. | Part No. | Eff Dscont | Name \& Description | Code | Mfr Part Number |
| A13R655 | 321-0304-00 | B010100 B017399 | RES., FXD, FILM 14.3 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G14301F |
| A13R655 | 321-0294-00 | B017400 | RES., FXD,FILM:11.3K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G11301F |
| A13R656 | 315-0332-00 |  | RES.,FXD, CMPSN: 3.3K OHM, 5\%,0.25W | 01121 | CB3325 |
| Al3R657 | 315-0473-00 |  | RES., FXD, CMPSN: 47 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4735 |
| A13R658 | 315-0473-00 |  | RES.,FXD, CMPSN:47K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4735 |
| A13R659 | 311-1237-00 |  | RES.,VAR, NONWIR: 1 K OHM, $10 \%, 0.50 \mathrm{~W}$ | 32997 | 3386X-T07-102 |
| A13R660 | 315-0471-00 |  | RES , FXD , CMPSN: 470 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB4715 |
| Al 3R661 | 321-0307-00 |  | RES.,FXD, FILM:15.4K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G15401F |
| Al 3R662 | 315-0162-00 |  | RES.,FXD, CMPSN: 1.6 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1625 |
| Al 3R663 | 315-0102-00 |  | RES. ,FXD, CMPSN: 1K OHM , $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1025 |
| Al3R664 | 315-0392-00 |  | RES.,FXD, CMPSN:3.9K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB3925 |
| A 13 R 665 | 315-0392-00 |  | RES.,FXD,CMPSN:3.9K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB3925 |
| A13R667 | 315-0622-00 |  | RES. , FXD, CMPSN: 6. 2 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB6225 |
| Al3R669 | 315-0102-00 |  | RES.,FXD, CMPSN: 1K OHM, 5\%,0.25W | 01121 | CB1025 |
| A13R671 | 315-0302-00 |  | RES.,FXD, CMPSN: 3K OHM, 5\%,0.25W | 01121 | CB3025 |
| Al3R672 | 315-0302-00 |  | RES., FXD, CMPSN: 3 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB3025 |
| Al3R675 | 315-0102-00 |  | RES , , FXD, CMPSN: 1 K OHM , $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1025 |
| Al3R687 | 321-0322-00 |  | RES.,FXD,FILM:22.1K OHM, 1\%,0.125W | 91637 | MFF1816G22101F |
| Al3R688 | 321-0319-00 |  | RES., FXD, FILM: 20.5 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G20501F |
| Al3R689 | 315-0153-00 |  | RES., FXD, CMPSN: 15K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1535 |
| A13R690 | 315-0512-00 |  | RES., FXD, CMPSN:5.1K OHM, 5\%,0.25W | 01121 | CB5125 |
| A13R693 | 315-0512-00 |  | RES., FXD, CMPSN: 5.1K OHM, 5\%,0.25W | 01121 | CB5125 |
| A13R694 | 315-0512-00 |  | RES., FXD, CMPSN: 5.1 K OHM , 5\%,0.25W | 01121 | CB5125 |
| Al3R695 | 315-0512-00 |  | RES., FXD, CMPSN: 5.1K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5125 |
| A13R696 | 315-0621-00 |  | RES . FXD, CMPSN: 620 OHM, 5\%,0.25W | 01121 | CB6215 |
| Al3R697 | 315-0621-00 |  | RES., FXD, CMPSN: 620 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB6215 |
| A13U555 | 156-1349-00 |  | MICROCIRCUIT, LI: DUAL INDEP DIFF AMPL | 02735 | CA3054 |
| Al3U585 | 156-0205-00 |  | MICROCIRCUIT, DI: QUAD 2-INPUT NOR GATE | 04713 | MC10102 (P OR L) |
| Al3U648 | 156-1381-00 |  | MICROCIRCUIT, LI: XSTR ARRAY | 02735 | CA3096AE-17 |
| Al3U665 | 156-0382-00 | B010100 B019249 | MICROCIRCUIT, DI: QUAD 2-INPUT NAND GATE | 01295 | SN74LS00(N OR J) |
| A13U665 | 156-0382-02 | B019250 | MICROCIRCUIT, DI : QUAD 2-INP NAND GATE | 01295 | SN74LS00 |
| A130670 | 156-0382-00 | B010100 B019249 | MICROCIRCUIT, DI: QUAD 2-INPUT NAND GATE | 01295 | SN74LSOO(N OR J) |
| A13U670 | 156-0382-02 | B019250 | MICROCIRCUIT, DI: QUAD 2-INP NAND GATE | 01295 | SN74LS00 |
| Al3U690 | 156-0385-00 | B010100 B019249 | MICROCIRCUIT, DI: HEX. INVERTER | 80009 | 156-0385-00 |
| A13U690 | 156-0385-02 | B019250 | MICROCIRCUIT, DI: HEX INVERTER | 01295 | SN74LS04 |
| Al3U693 | 156-0480-00 | B010100 B01 2542 | MICROCIRCUIT, DI: QUAD 2-INPUT AND GATE | 01295 | SN74LS08(N OR J) |
| Al3U693 | 156-0480-02 | B012543 | MICROCIRCUIT, DI: QUAD 2 INP \& GATE | 01295 | SN74LS08NP3 |
| A13U696 | 156-1611-00 |  | MICROCIRCUIT, DI: DUAL D TYPE EDGE-TRIGGERED | 07263 | 74F74 |
| Al3VR584 | 152-0195-00 | B010100 B012542 | SEMICOND DEVICE:ZENER,0.4W,5.1V,5\% | 04713 | SZ11755 |
| Al3VR 584 | 152-0662-00 | B012543 | SEMICOND DEVICE:ZENER,0.4W,5V,1\% | 04713 | SZG195 |
| Al3W556 | 131-0566-00 |  | BUS CONDUCTOR: DUMMY RES, 2.375, 22 AWG | 55210 | L-2007-1 |
| Al 3W661 | 131-0566-00 |  | BUS CONDUCTOR: DUMM | 55210 | L-2007-1 |
| A13W662 | 131-0566-00 |  | BUS CONDUCTOR: DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| Al 3W665 | 131-0566-00 |  | BUS CONDUCTOR: DUMMY RES, $2.375,22$ AWG | 55210 | L-2007-1 |
| A13W670 | 131-0566-00 |  | BUS CONDUCTOR: DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| Al 3W671 | 131-0566-00 |  | BUS CONDUCTOR: DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| A13W672 | 131-0566-00 |  | BUS CONDUCTOR: DUMMY RES, $2.375,22$ AWG | 55210 | L-2007-1 |
| Al 3W689 | 131-0566-00 |  | BUS CONDUCTOR: DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| A13W690 | 131-0566-00 |  | BUS CONDUCTOR: DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| Al 3W692 | 131-0566-00 |  | BUS CONDUCTOR: DUMMY RES,2.375, 22 AWG | 55210 | L-2007-1 |
| A13W693 | 131-0566-00 |  | BUS CONDUCTOR:DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| Al 3W694 | 131-0566-00 |  | BUS CONDUCTOR:DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| A13W695 | 131-0566-00 |  | BUS CONDUCTOR: DUMMY RES,2.375,22 AWG | 55210 | L-2007-1 |
| Al3W1001 | 131-0589-00 |  | TERMINAL, PIN: $0.46 \mathrm{~L} \times 0.025 \mathrm{SQ}$ | 22526 | 48283-029 |


| Component No. | Tektronix <br> Part No. | Serial/Model No. Eff Dscont | Name \& Description | Mfr Code | Mfr Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A18 | 670-7706-00 | XB022000 | CKT ROARD ASSY: PREREGULATOR | 80009 | 670-7706-00 |
| A18C903 | 285-1192-00 | XB022000 | CAP., FXD, PPR DI $0.0022 \mathrm{UF}, 20 \%, 250 \mathrm{VAC}$ | 000FG | PME 2714422 |
| A18C904 | 285-1192-00 | хв022000 | CAP., FXD, PPR DI: $0.0022 \mathrm{UF}, 20 \%, 250 \mathrm{VAC}$ | 000Fg | PME271Y422 |
| A18C905 | 285-1250-00 | xb022000 | CAP., FXD, PPR DI: $0.14 \mathrm{~F}, 20 \%, 250 \mathrm{VAC}$ | 19701 | $719 \mathrm{JlGG104M251SB}$ |
| A18C907 | 285-1192-00 | xB022000 | CAP., FXD, PPR DI: $0.0022 \mathrm{UF}, 20 \%, 250 \mathrm{VAC}$ | 000FG | PME2714422 |
| A18C908 | 285-1192-00 | XB022000 | CAP., FXD, PPR DL: $0.0022 \mathrm{JF}, 20 \%, 250 \mathrm{VAC}$ | 000FG | PME2714422 |
| A18C909 | 290-0978-00 | XB022000 | CAP. , FXD, ELCTLT: 75UF, $+50-10 \%, 450 \mathrm{~V}$ | 56289 | 17 D 1149 |
| Al8C910 | 283-0335-00 | xB022000 | CAP., FXD, CER DI: $0.10 \mathrm{~F}, 20 \%, 600 \mathrm{~V}$ | 51642 | UC4710025U824NPS |
| A18C913 | 290-0770-00 | xB022000 | CAP. , FXD, ELCTLT: $1000 \mathrm{~F},+50-10 \%, 25 \mathrm{~V}$ | 56289 | 502D230 |
| A18C920 | 281-0852-00 | XB022000 | CAP., FXD, CER DI: $1800 \mathrm{PF}, 10 \%$, 100 VDC | 04222 | gCiolicis2kat. |
| A18C921 | 281-0775-00 | XB022000 | CAP., FXD, CER DI $: 0.1 \mathrm{UF}, 20 \%$, 50 V | 04222 | SA205E104MAA |
| A18C923 | 281-0772-00 | xB022000 | CAP., FXD, CER DI: $0.0047 \mathrm{UF}, 10 \%, 100 \mathrm{~V}$ | 04222 | GC701C472K |
| A18C925 | 281-0820-00 | XB022000 | CAP.,FXD, CER DI: 680PF, 10\%,50V | 12969 | CGB 681KDX |
| A18C929 | 281-0809-00 | XB022000 | CAP., FXD, CER DI: $200 \mathrm{PF}, 5 \%$, 100 V | 72982 | 8013T2ADDC 1G201J |
| A18C933 | 285-0932-00 | XB022000 | CAP. , FXD, PLSTC: 1UF, 10\%,400V | 14752 | A-1478 |
| A18C934 | 290-0831-00 | хв022000 | CAP., FXD, ELCTLT: $470 \mathrm{UF},+50-10 \%, 50 \mathrm{~V}$ | 55680 | 50ULA470 |
| A18C935 | 283-0208-00 | xb022000 | CAP., FXD, CER DI $0.0 .22 \mathrm{UF}, 10 \%, 200 \mathrm{~V}$ | 72982 | 8151 N 230 C 224 K |
| A18CR904 | 152-0750-00 | XB022000 | SEMICOND DEVICE:RECT BRIDGE,600V,3A | 80009 | 152-0750-00 |
| A18CR913 | 152-0061-00 | XB022000 | SEMICOND DEVICE:SILICON,175V,100MA | 07263 | FDH2161 |
| A18CR931 | 152-0061-00 | XB022000 | SEMICOND DEvice: SILICON, $175 \mathrm{v}, 100 \mathrm{MA}$ | 07263 | FDH2161 |
| A18CR933 | 152-0661-00 | XB022000 | SEMICOND DEVICE: RECT, SI, 600V, 3A, FAST | 04713 | MR856 |
| A18E933 | 276-0640-00 | Xb022000 | CORE, EM: TOROID, FERRITE, 0.375 OD X 0.187 | 00779 | 1-480304-0 |
| A18L937 | 108-0422-00 | xb022000 | COIL, RF:FIXED, 82UH | 80009 | 108-0422-00 |
| A18L938 | 108-0422-00 | xb022000 | COIL, RF:FIXED, 82UH | 80009 | 108-0422-00 |
| Al8P801 | 131-1048-00 | XB022000 | TERM.QIK DISC:CKT BD MT, $0.11 \times 0.02$ | 00779 | 61134-1 |
| A18P802 | 131-1048-00 | xb022000 | TERM.QIK DISC:CKT BD MT, $0.11 \times 0.02$ | 00779 | 61134-1 |
| A18P803 | 131-1048-00 | XB022000 | TERM.QIK DISC:CKT BD MT, $0.11 \times 0.02$ | 00779 | 61134-1 |
| A10P804 | 131-1048-00 | XB022000 | TERM.QIK DISC:CKT BD MT, $0.11 \times 0.02$ | 00779 | 61134-1 |
| A18Q915 | 151-0164-00 | xb022000 | TRANSISTOR: SILICON, PNP | 01295 | SKB 3334 |
| A18Q917 | 151-0432-00 | XB022000 | TRANSISTOR: SILICON, NPN | 80009 | 151-0432-00 |
| A18Q931 | 151-0164-00 | xb022000 | TRANSISTOR: SILICON, PNP | 01295 | SKB3334 |
| A18Q933 | 151-1152-00 | xB022000 | TRANSISTOR:MOSFE, N-CHANNEL, SI , T0-220 | 04713 | STP3002 |
| A18Q935 | 151-0506-00 | xB022000 | SCR:SILICON | 03508 | C106B2X283 |
| A18R903 | 315-0512-00 | xB022000 | RES., FXD, CMPSN: 5.1K OHM, 5\%,0.25W | 01121 | CB5125 |
| A18R904 | 315-051 2-00 | XB022000 | RES., FXD, CMPSN: 5.1 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5125 |
| A18R907 | 315-0561-00 | хв022000 | RES. , FXD, CMPSN: 560 OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5615 |
| A18R908 | 315-0561-00 | XB022000 | RES. , FKD, CMPSN: 560 OHM, $5 \%, 0.25 \mathrm{H}$ | 01121 | CB5615 |
| A18R911 | 303-0154-00 | xB022000 | RES., FXD, CMPSN: 150 K OHM, $5 \%, 1 \mathrm{~W}$ | 01121 | GB1545 |
| A18R912 | 315-0104-00 | XB022000 | RES., FXD, CMPSN: $100 \mathrm{~K} 0 \mathrm{HM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | Cb1045 |
| A18R913 | 315-0104-00 | XB022000 | RES. , FXD, CMPSN: 100K OHM, 5\%,0.25w | 01121 | CB1045 |
| A18R914 | 315-0104-00 | XB022000 | RES. , FXD, CMPSN: 100 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1045 |
| A18R916 | 315-0302-00 | xB022000 | RES., FXD, CMPSN: 3 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB3025 |
| A18R917 | 315-0512-00 | хв022000 | RES., FXD, CMPSN: 5.1K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB5125 |
| A18R920 | 315-0203-00 | xB022000 | RES. , FKD, CMPSN: 20K OHM , 5\%, 0.25 W | 01121 | CB 2035 |
| A18R921 | 321-0289-00 | Xb022000 | RES. , FXD, FILM: $10 \mathrm{~K} 0 \mathrm{HM}, 1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF $1816 \mathrm{Gl0001F}$ |
| A18R922 | 321-0379-00 | xB022000 | RES., FXD, FILM: 86.6 K OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF 1816G86601F |
| A18R923 | 315-0154-00 | xB022000 | RES. , FXD, CMPSN: 150K OHM $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1545 |
| A18R925 | 315-0682-00 | xb022000 | RES., FXD, CMPSN: 6.8 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB6825 |
| A18R927 | 315-0103-00 | xb022000 | RES. , FXD, CMPSN: 10 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1035 |
| A18R928 | 315-0391-00 | Xb022000 | RES., FXD, CMPSN: 390 OHM, 5\%,0.25W | 01121 | CB3915 |
| A18R929 | 315-0103-00 | xB022000 | RES., FKD, CMPSN: 10 K OHM, $5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1035 |
| A18R931 | 315-0302-00 | xB022000 | RES.,FXD,CMPSN: 3K OHM, $5 \boldsymbol{z}, 0.25 \mathrm{~W}$ | 01121 | CB3025 |
| A18R933 | 308-0843-00 | xb022000 | RES. , FXD WW: 0.2 OHM, 5\%, 1.0W | 91637 | RSIAR2000JT/R |
| A18R934 | 308-0441-00 | xB022000 | RES., FXD, WW: 3 OHM, 5\%, 3W | 91637 | $7 \mathrm{CW} 2 \mathrm{~B}-3 \mathrm{R} 00 \mathrm{~J}$ |
| A18R935 | 315-0121-00 | XB022000 | RES. , FXD, CMPSN: $1200 \mathrm{HM}, 5 \%, 0.25 \mathrm{~W}$ | 01121 | CB1215 |
| A18R936 | 315-0470-00 | XB022000 | RES. ,FXD, CMPSN: 47 OHM, 5\%,0.25W | 01121 | CB4705 |
| A18R937 | 301-0822-00 | XB022000 | RES., FXD, CMPSN: 8.2 K OHM, $5 \%, 0.50 \mathrm{~W}$ | 01121 | 1 E88225 |


| Component No . | Tektronix Part No. | Serial/Model No. Eff Dscont | Name \& Description | Mfr Code | Mfr Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A18RT901 | 307-0350-00 | xB022000 | RES., THERMAL: 7.5 OHM, $10 \%, 3.9 \% / \mathrm{DEG}$ C | 15454 | 75d.J7R5R0220ss |
| A18RT902 | 307-0350-00 | XB022000 | RES., THERMAL: 7.5 OHM, $10 \%, 3.9 \% / \mathrm{DEG}$ C | 15454 | 75DJ7R5R0220SS |
| A18T901 | 120-1449-00 | xb022000 | XFMR, COM MODE: | 02113 | P104 |
| A18T907 | 120-1441-00 | хв022000 | TRANSFORMER,RF:POT CORE | 09969 | OBD |
| A18T933 | 120-1439-00 | XB022000 | TRANSFORMER,RF: ENERGY STORAGE | 20462 | OBD |
| A18U920 | 156-1627-00 | XB022000 | MICROCIRCUIT, LI: POWER WIDTH MODULATED CONT | 01295 | TL494ACN |
| A18VR917 | 152-0166-00 | XB022000 | SEMICOND DEVICE:ZENER,0.4W,6.2V,5\% | 04713 | SZ11738 |
| A18VR935 | 152-0255-00 | xb022000 | SEMICOND DEVICE:ZENER,0.4W,51V,5\% | 80009 | 152-0255-00 |


| Component No. | Tektronix Part No. | Serial/M Eff | del No. Dscont | Name \& Description | Mfr Code | Mfr Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A19 | 670-7498-00 | B010100 | B021999X | CKT BOARD ASSY: GURRENT LTMIT | 80009 | 670-7498-00 |
| A19C938 | 285-1222-00 | B010100 | B021999 X | CAP., FXD, PLSTC: $0.068 \mathrm{UF}, 20 \%, 250^{\circ}$ | 000FG | PME 271 M568 |
| A19CR932 | 152-0782-00 | B010100 | B021999X | SEMICOND DEVICE:RECTIFIER, SILIC. ${ }^{\text {, } 600 \mathrm{~V}}$ | 05828 | GP20J-009 |
| A19CR934 | 152-0782-00 | B010100 | B021999X | SEMICOND DEVICE: RECTIFIER, SILICON, 600V | 05828 | GP20J-009 |
| A19CR935 | 152-0141-02 | B010100 | B021999X | SEMICOND DEVICE:SILICON, 30V,150MA | 01295 | 1N4152R |
| A19P801 | 131-1048-00 | B010100 | B021999X | TERM.QIK DISC:CKT BD MT, $0.11 \times 0.02$ | 00779 | 61134-1 |
| A19P802 | 131-1048-00 | B010100 | B021999X | TERM.QIK DISC:CKT BD MT, 0.11 X 0.02 | 00779 | 61134-1 |
| A19P803 | 131-1048-00 | B010100 | B021999X | TERM.QIK DISC:CKT BD MT, $0.11 \times 0.02$ | 00779 | 61134-1 |
| A19P804 | 131-1048-00 | B010100 | B021999X | TERM.QIK DISC:CKT BD MT, $0.11 \times 0.02$ | 00779 | 61134-1 |
| A19Q933 | 151-0736-00 | B010100 | B021999X | TRANS ISTOR:SILICON, NPN | 04713 | SPS8317 |
| A19Q938 | 151-1141-00 | B010100 | B021999X | TRANS ISTOR: SILICON, N-CHANNEL, FET | 81483 | TRF9523 |
| Al9R933 | 301-0203-00 | B010100 | B021999X | RES. , FXD, CMPSN: 20K OHM, 5\%,0,50W | 01121 | EB2035 |
| A19R935 | 321-0165-00 | B010100 | B015899 | RES.,FXD,FILM:511 OHM, 1\%,0.125W | 91637 | MFF1816G511R0F |
| A19R935 | 321-0140-00 | B015900 | B021999X | RES., FXD, FILM: 280 OHM, 1\%,0.125W | 91637 | MFF1816G280R0F |
| A19R936 | 321-0193-00 | B010100 | B015899 | RES.,FXD, FILM: 1K OHM , $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G10000F |
| A19R936 | 321-0152-00 | B015900 | B021999X | RES.,FXD, FILM: 374 OHM, $1 \%, 0.125 \mathrm{~W}$ | 91637 | MFF1816G374R0F |
| A19R937 | 308-0710-00 | B010100 | B015899 | RES., FXD, WW: 0.27 OHM, 10\%, 1W | 75042 | BW20-R2700J |
| Al9R937 | 308-0843-00 | B015900 | B02 1999X | RES., FXD WW:0.2 OHM, 5\%, 1.0W | 91637 | RS1AR2000JT/R |
| Al9R938 | 301-0203-00 | B010100 | B021999X | RES , FXD , CMPSN: 20K OHM, 5\%,0.50W | 01121 | EB2035 |
| A19R939 | 308-0123-00 | B010100 | B021999X | RES. , FXD, WW: 20 OHM, 5\%, 5W | 05347 | C56-20R0.J |
| A19RT935 | 307-0125-00 | B010100 | B021999X | RES., THERMAL: 500 0HM, $10 \%, 25$ DEG C | 50157 | 2 D 1595 |
| A19VR933 | 152-0268-00 | B010100 | B015899 | SEMICOND DEVICE:ZENER, $0.4 \mathrm{~W}, 56 \mathrm{~V}, 5 \%$ | 80009 | 152-0268-00 |
| Al9VR933 | 152-0286-00 | B015900 | B021999X | SEMICOND DEVICE:ZENER, $0.4 \mathrm{~W}, 75 \mathrm{~V}, 5 \%$ | 80009 | 152-0286-00 |
| Al9VR934 | 152-0149-00 | B010100 | B021999X | SEMICOND DEVICE:ZENER,0.4W,10V,5\% | 04713 | SZG35009K3 |


| Component No. | Tektronix Part No. | Serial/Model No. <br> Eff Dscont | Name \& Description | Mfr <br> Code | Mfr Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | CHASSIS PARTS |  |  |
| C401 | 281-0787-00 |  | CAP., FXD, CER DI: 15PF, 5\%,500V | 72982 | $0314021 \mathrm{COGO150才}$ |
| DL 350 | 119-1392-00 |  | DELAY LINE, ELEC: 100 NANO SEC, 150 OHM | 80009 | 119-1392-00 |
| F901 | 159-0021-00 | B010100 B021999 | FUSE, CARTRIDCE : 3AG, 2A, 250V, FAST-BLOW | 71400 | AGC 2 |
| F901 | 159-0019-00 | B022000 | FUSE, CARTRIDGE: 3AG,1A, 250V, SLOW BLOW | 71400 | MDLI |
| F937 | 159-0032-00 | B010100 B021999X | FUSE, CARTRIDGE: 3AG, $0.5 \mathrm{~A}, 250 \mathrm{~V}$, SLOW-BLOW | 71400 | MDL 1/2 |
| FL9001 | 119-1541-00 | XB022000 | FILTER, RFI: 1A, 250VAC | 05245 | 1EF1 |
| J1001 | 131-0126-00 |  | CONNECTOR, RCPT, : BNC, FEMALE | 77820 | 9663-1 NT-34 |
| J2001 | 131-0126-00 |  | CONNECTOR, RCPT, : BNC, FEMALE | 77820 | 9663-1 NT-34 |
| J4001 | 131-0955-00 |  | CONN, RCPT, ELEC: BNC, FEMALE | 13511 | 31-279 |
| J8001 | 131-0955-00 |  | CONN, RCPT, ELEC: BNC, FEMALE | 13511 | 31-279 |
| L925 | 108-1096-00 | B010100 B021999X | COIL, RF : FIXED, 16MH, 25\% | 54937 | 5002282 |
| Q938 | 151-1141-00 | B010100 B021999X | TRANSISTOR:SILICON, N-CHANNEL, FET | 81483 | IRF9523 |
| R647 | 311-2146-00 |  | RES., VAR, NONWIR: 50 OHM, 20\%,0.5W | 12697 | CM41773 |
| R658 | 311-1183-00 | B010100 B016999 | RES.,VAR, WW: PNL, 2K OHM, $5 \%, 2 \mathrm{~W}$ | 02111 | 534-9514 |
| R658 | 311-1183-01 | B017000 | RES., VAR, WW : PNL, 2K OHM, 5\%, 2W | 32997 | 3540S-91-202 |
| v870 | 154-0838-00 |  | ELECTRON TUBE:CRT,T4652-31-2 | 80009 | 154-0838-00 |

symbols
Graphic symbols and class designation letters are
based on ANSI Standard Y32:-1975. Logic symbology is based on ANSI Y32.14-1973 in
terms of positive logic. Logic symbols depict the logic terms of positive logic. Logic symbols depict the eogic
function performed and may differ trom the manutac-

The overline on a signal name indicates that the signal
performs its intended function when it is in the low state.
Abbreviations are based on ANSI Y.1.-1972.
Other ANSI standards that are used in the preparation
of diagrams by Tektronix, Inc. are:
—— The information and special symbols below may appear in this manual
Assembly Numbers and Grid Coordinates
Each assembly in the instrument is assigned an
assembly number (e.g., AR20. The assembly number assembly number (e.g., A20). The assembly number
appearson the circuit board outine on the diagram, in the
athers.

 Replaceable Electrical Parts listis sarranged by assemblies
in numerical sequence; the components are listed by


 Electrical
Engineering.
erican National Stan American National Standard Institute New York, Hew Yoarkay 10018 Component Values Electrical components shown on the diagrams are in
the following units unless noted otherwise: Capacitors = Values one or greater arein picofarads ( $\mathrm{P} F$ ) Resistors $=0 \mathrm{Ofms}(\Omega)$.
 lustrated ithe lookup table will list the diagram number of
other iligarams that the circuitry of the circuit board
anpears ppears on.


| Color | $\begin{gathered} \text { SIGNIFICANT } \\ \text { FIGURES } \end{gathered}$ | RESISTORS |  | CAPACITORS ${ }^{\text {MULTIPLIER }}$ Tolerance |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | MULTIPLER | Tolerance |  |  |  |  |
| BLACK | 0 | 1 | --- | 1 | +20\% | 12 pF | 4 vDC |
| вrown | 1 | 10 | $\pm 1 \%$ | 10 | $\pm 1 \%$ | $\pm 0.1 \mathrm{pF}$ | 6 vDC |
| Red | 2 | $10^{2}$ or 100 | $\pm 2 \%$ | $10^{2}$ or 100 | $\pm 2 \%$ | --- | 10 voc |
| orange | 3 | $10^{3}$ or 1 K | +3\% | $10^{3}$ or 1000 | +3\% | --- | 15 VDC |
| YELLOW | 4 | $10^{4}$ or 10 K | $\pm 4 \%$ | $10^{4}$ or 10.000 | +100\%-9\% | --- | 20 VDC |
| Grem | 5 | $10^{\circ}$ or 100 K | t2\% | $10^{5}$ or 10,000 | +5\% | $\pm 0.5 \mathrm{pF}$ | 25 VDC |
| BLUE | 6 | $10^{6}$ or 1 M | $\pm 2 \%$ | $10^{6}$ or $1,000,000$ | --- | --- | 35 voc |
| VIolet | 7 | -- | $\pm 1 / 10 \%$ | - | --- | --- | 50 vDC |
| gray | 8 | -- | --- | $10^{-2}$ or 0.01 | +80\%-20\% | $\pm 0.25 \mathrm{pF}$ | --.. |
| WHITE | 9 | -- | --- | $10^{-1}$ or 0.1 | $\pm$ | $\pm 1 \mathrm{pF}$ | 3 VDC |
| GOLD | - | $10^{-1}$ or 0.1 | +5\% | -- | --- | --- | --- |
| SILVER | - | $10^{-2}$ or 0.01 | $\pm 10 \%$ | -- | -- | --- | --- |
| None | - | --- | $\pm 20 \%$ | -- | $\pm 10 \%$ | $\pm 1 \mathrm{pF}$ | --- |



EEAD CONEIGURATIONS AND CASE STYLES ARE TYYICALL, EUT MAY VARY DUE TO VENDOR CHANGES OA



## TEST WAVEFORM AND VOLTAGE SETUPS

## WAVEFORM MEASUREMENTS


#### Abstract

On the left-hand pages preceding the schematic diagrams are test waveform illustrations that are intended to aid in troubleshooting the instrument. To test the instrument for these waveforms, make the initial control settings as follows:


Changes to the control settings for specific waveforms are noted at the beginning of each set of waveforms. Input signals and hookups required are also indicated, if needed, for each set of waveforms.

## DC VOLTAGE MEASUREMENTS

Typical voltage measurements, located on the schematic diagram, were obtained with the instrument operating under the conditions specified in the Waveform Measurement setup. Control-setting changes required for specific voltages are indicated on each waveform page. Measurements are referenced to chassis ground with the exception of the Preregulator and Inverter voltages on diagram 9. Those voltages are referenced as indicated on the schematic diagram.

## RECOMMENDED TEST EOUIPMENT

Test equipment listed in Table 4-1 in the "Performance Check Procedure" section 4 of this manual, meets the required specifications for testing this instrument.

Horizontal

| POSITION | Midrange |
| :--- | :--- |
| X10 MAG | Off (button in) |
| HORIZONTAL MODE | A |
| A and B SEC/DIV | .5 ms |
| SEC/DIV Variable | CAL detent |
| B DELAY TIME POSITION 5.0 |  |

## Trigger

| SLOPE (both | + |
| :--- | :--- |
| A LEVEL | Midrange |
| MODE | AUTO |
| A \& B INT | VERT MODE |
| A SOURCE | INT |
| B LEVEL | RUN AFTER DELAY-CW |
| VAR HOLDOFF | Min (fully ccw) |

## POWER SUPPLY ISOLATION PROCEDUR


The power distribution diagram is divided into circuit boards. Each power supply fead to a circuit board is
dicated by the schematic diagram number on which the voltage appears. The sche arm appears. he schematic diagram grici location If a power supply comes up atiter liting one of the main jumpers stom the power supply to isolate that supply, it
is very probabole that a shortexist it ithe circuitry on that supply ine. By lifting iumpers tarther down the line, the Typical resistance values to ground from the regulated supplies output as measured at the supply test points


```
$+5v
```




Resistance values significantly lower may indicate shorted components in the load. Values will vary between
Always set the Power switch to off before soldering or unsoldering service jumpers or other components
and before attempting to measure component resistance values.





|  | Scemen | cincur | Scemen | crame | Stem | ciremir | Sctem | cincer | Sctem |  | Sctem |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | ${ }_{8}^{5}$ | ${ }_{\substack{\text { Pemo } \\ \text { Reoma } \\ \hline}}$ | ${ }_{7}^{10}$ | ${ }_{\substack{\text { R.120 } \\ \text { R120 }}}^{\text {a }}$ |  |  |  | ${ }_{\substack{\text { Rese } \\ \text { Reat }}}$ |  |
|  |  |  | ${ }_{8}^{8}$ |  | 7 | ${ }_{\text {den }}^{\substack{12122}}$ |  |  |  |  |  |
| colit |  |  | ¢ |  | ${ }_{8}^{8}$ |  |  |  |  |  |  |
|  |  | (tar | 7 |  | $\stackrel{8}{7}$ |  |  | ${ }_{\substack{\text { ne32 } \\ \text { R232 }}}^{\text {203 }}$ |  |  |  |
|  |  |  | 7 |  | ? |  |  |  |  |  |  |
|  |  |  | ${ }_{8}^{8}$ | - | + |  |  |  |  |  |  |
|  |  |  | ! |  | , |  |  | ${ }_{\substack { 12328 \\ \begin{subarray}{c}{223{ 1 2 3 2 8 \\ \begin{subarray} { c } { 2 2 3 } }\end{subarray}}$ |  |  |  |
|  |  |  | S | ${ }_{\text {cose }}^{\substack{022 \\ 0225}}$ | ; | ${ }_{\substack{\text { Ra36 } \\ \text { R138 }}}^{\text {and }}$ |  |  |  | ${ }_{\substack{8,372 \\ 783}}^{2}$ |  |
|  |  |  |  |  |  |  |  |  |  |  |  |
|  |  |  | + | ${ }_{\text {cose }}^{0239}$ | ! | ${ }_{\substack{\text { Ralas } \\ \text { R, } 120}}$ |  |  |  | ${ }_{\substack{\text { aras } \\ \text { ar3 }}}^{\text {a }}$ |  |
|  |  |  | ; |  | ${ }_{5}^{5}$ | ${ }_{\substack{\text { Rela } \\ \text { Rila }}}^{\text {Rat }}$ |  |  |  |  |  |
|  |  |  | , |  | ${ }_{10}$ | ${ }_{\substack { \text { Pa, } \\ \begin{subarray}{c}{1,43{ \text { Pa, } \\ \begin{subarray} { c } { 1 , 4 3 } }\end{subarray}}$ |  |  |  |  |  |
| coicter |  |  | 1 |  | ${ }^{10}$ |  |  |  |  | cis |  |
|  |  |  | ${ }_{8}^{8}$ |  | 7 | $\underbrace{\text { and }}_{\substack{\text { P.148 } \\ \text { Rise }}}$ |  |  |  | coss |  |
|  |  |  | $\stackrel{5}{5}$ |  | ? |  |  |  | 5 | cinc |  |
|  |  |  | , |  | ; |  |  | ${ }_{\substack { \text { re33 } \\ \begin{subarray}{c}{\text { Re34 }{ \text { re33 } \\ \begin{subarray} { c } { \text { Re34 } } }\end{subarray}}$ | 7 | cise |  |
|  |  | ${ }_{\substack{\text { a }}}^{\text {penio. }}$ | , | ${ }_{\substack{\text { R10\% } \\ \text { R10\% }}}^{\text {Ros }}$ | , |  |  | ${ }_{\substack{\text { Re36 } \\ \text { Rese }}}^{\text {Rem }}$ | ? | vall |  |
|  |  |  | 10 | ${ }_{\text {fand }}^{\text {falin }}$ | 1 |  |  |  | 8 |  |  |
|  | ${ }_{8}^{8}$ |  | ${ }_{5}^{5}$ | ${ }_{\substack{\text { R.114 } \\ \text { R1, } \\ \hline}}$ | + |  |  |  | 边 | w 34 |  |
|  | ${ }_{8}^{5}$ |  |  | ${ }_{\text {R }}^{\substack{\text { R1116 } \\ \text { R17 }}}$ |  | ${ }_{\text {a }}^{\substack{2217}}$ |  | ${ }_{\substack{\text { Re8 } \\ \text { Re8 }}}$ | $\stackrel{1}{7}$ |  |  |

| A , B, C , D, E , F ,

5


Figure 9.6. Crrcuit view of $\mathbf{A 1 2}$-Atemuator/ Sweep board



CH $1 \&$ CH 2 Attenuators

|  |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| cemer cimer | summ | Roano | cemers | soctem | gamis | crame | satem | Reant | cemer | Soction | Somb |
| $\underset{\substack{\text { ciol } \\ \text { cian } \\ \text { and }}}{ }$ |  |  |  |  |  |  |  | $\underbrace{}_{\substack { \text { jec } \\ \begin{subarray}{c}{\text { ack } \\ \text { cic }{ \text { jec } \\ \begin{subarray} { c } { \text { ack } \\ \text { cic } } }\end{subarray}}$ | sisiol | ${ }^{28}$ | ${ }^{38}$ |
|  |  |  |  |  |  |  |  |  |  |  |  |
| assemalval2 |  |  |  |  |  |  |  |  |  |  |  |
| cememe | satmo | (oano | ${ }^{\text {couman }}$ | Schen | comb | cemer | somen |  | cemat | Scamm | Eamon |
|  |  |  |  |  |  |  |  |  |  |  |  |

chass moumre pars




8


## 

## 

|  |  | cincur sctem | Crinur sctem |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| ${ }_{\text {coiel }}$ | ${ }^{\text {c6io }}$ | ${ }^{\text {c775 }}$ | O8880 | ${ }^{680} 0$ | ${ }^{12270}$ |
| ${ }_{\text {c173 }}$ | ${ }_{\text {cis }}$ | ${ }^{\text {con }}$ | ${ }_{\text {cien }}$ | ${ }^{20706}$ | ${ }^{10273}$ |
| come | cose |  | 速 | coict |  |
| $\substack{\begin{subarray}{c}{\text { cise } \\ \text { cies } \\ \text { cies }} }} \end{subarray}$ |  | cose |  |  |  |
| cicle | ceas |  | doll | (07\% |  |
| ${ }_{\substack{\text { c23 } \\ \text { c23 }}}^{2}$ |  |  |  | -0775 |  |
|  |  | $\underbrace{}_{\substack{\text { crinc } \\ \text { crin }}}$ |  | cose |  |
| (ent | coicicie | coicter |  | cois |  |
| coict | coict | (carse |  |  | ${ }^{\substack{2029 \\ 2029}}$ |
| coict | cinc |  |  |  |  |
|  |  | ${ }_{\substack{\text { chas } \\ \text { crate }}}^{\substack{\text { cas }}}$ |  | ${ }_{\text {a }}^{\text {asfo }}$ |  |
| cose | $\xrightarrow{\substack { c 73 \\ \begin{subarray}{c}{7 \\ 7{ c 7 3 \\ \begin{subarray} { c } { 7 \\ 7 } }\end{subarray}}$ |  |  | (os) |  |
| coicce | coictic |  |  |  |  |
| cis | coictice |  |  | (oas | $\underbrace{\text { and }}_{\substack{\text { and } \\ \text { anc }}}$ |
| cis | circi |  | ${ }_{\text {Prosem }}$ | cosa | ${ }_{\substack{\text { majo } \\ \text { Rase }}}$ |
| coicce | cose |  |  | ${ }_{\substack{\text { R.151 } \\ \text { R152 }}}$ | ${ }_{\text {and }}^{\text {Ra3i }}$ |
|  | cos |  | ${ }_{\substack{\text { Pergio. } \\ \text { Pr10.4 }}}$ |  |  |
| cisic | ${ }^{\text {cosio }}$ | ${ }_{\text {corlo }}^{\text {carle }}$ |  |  |  |
| cis |  |  |  | ${ }_{\substack{\text { R1, } 162 \\ \text { R13 }}}$ |  |
|  |  |  | cosem |  |  |
|  | $\underbrace{\text { a }}_{\substack{\text { cise } \\ \text { case } \\ \text { case }}}$ |  | - | ${ }_{\text {and }}^{\text {And }}$ | ${ }_{\substack{\text { an23 } \\ \text { R234 }}}^{\text {and }}$ |
| cose | cose |  |  |  |  |
|  | ceat | ${ }_{\substack{\text { cras3 } \\ \text { crast }}}^{\text {cea }}$ |  | ${ }^{\text {Ratir }}$ | ${ }_{\substack{\text { a33 } \\ \text { R33 }}}^{\text {ar }}$ |
| ${ }_{\substack{\text { cais } \\ \text { cas }}}^{\text {cais }}$ | ${ }_{\substack{\text { cas } \\ \text { cas }}}^{\text {cas }}$ |  | ${ }_{\text {cose }}^{\substack{\text { O288 } \\ 027}}$ | ${ }_{\text {R }}^{\text {R178 }}$ | ${ }_{\text {R1323 }}^{\text {R332 }}$ |
| coick | $\underbrace{\text { ata }}_{\substack{\text { cas } \\ \text { cas } \\ \text { cas }}}$ |  | - |  |  |
| cise | coicl |  |  | $\underbrace{}_{\substack{\text { R1, } \\ \text { R185 }}}$ |  |
| cois | ${ }_{\text {cese }}^{\text {cose }}$ | crea |  | $\underbrace{\text { deg }}_{\substack{\text { R1,87 } \\ \text { R1, }}}$ |  |
|  | ${ }_{\substack{\text { ce71 } \\ \text { cri3 }}}^{\text {a }}$ | ${ }_{\text {coicle }}^{\text {cras }}$ | ${ }_{\substack{\text { asiso } \\ \text { asion }}}$ | ${ }_{\substack{\text { R192 } \\ \text { R192 }}}$ | ${ }_{\substack{\text { aja4 } \\ \text { ajas }}}^{\text {and }}$ |
| ${ }_{\substack{\text { case } \\ \text { case }}}^{\text {cas }}$ |  |  | ${ }_{\substack{0370 \\ 937}}^{\text {aid }}$ | ${ }_{\substack{\text { Reli, } \\ \text { R1, }}}$ | $\underbrace{}_{\substack{\text { Ras } \\ \text { Ras }}}$ |
| cose | ${ }_{\text {core }}^{\substack{\text { ara }}}$ |  |  | ${ }_{\substack{\text { Reler } \\ \text { R1,9 }}}$ | $\xrightarrow[\substack{\text { Raso } \\ \text { R35 }}]{ }$ |
| cismo | cick |  |  | ${ }_{\substack{\text { and } \\ \text { R25 } \\ \text { R25 }}}$ |  |
| (exms | ${ }_{\text {cole }}^{\substack{\text { cit } \\ \text { ali }}}$ |  | ${ }_{\text {a }}^{\substack{\text { asid } \\ \text { ata }}}$ |  |  |
| ${ }_{\text {cise }}^{\text {cise }}$ | ceice |  | ${ }_{\substack{\text { arata } \\ \text { arate }}}^{\text {ate }}$ | ${ }_{\substack{\text { arse } \\ \text { R258 }}}^{\text {a }}$ |  |
| cicle | cens |  | ${ }_{\text {a }}^{\text {atas }}$ | ${ }_{\text {a }}^{\substack{\text { aras } \\ \text { R2e }}}$ | ${ }_{\text {cose }}^{\text {Rasi }}$ |
|  | cose |  |  |  |  |
| (case |  | Sossid |  |  | $\underbrace{\text { and }}_{\substack{\text { and } \\ 8308}}$ |
|  | ${ }_{c}^{\text {car }}$ |  |  |  |  |





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| Channel switch a vertical output |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Assemiva 10 |  |  |  |  |  |  |  |  |  |  |  |
| Comer |  |  | cheme |  |  | Crear seme |  |  | comer |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  |
| assemilvall |  |  |  |  |  |  |  |  |  |  |  |
| cinco | Socmen | gamo |  | Somem | bean | $\left.\right\|_{\text {couma }} ^{\text {numer }}$ | Scemem | Lemme | cieme | Loamon | Sano |
|  |  |  |  |  |  | cos | cid |  | comen |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  |
| Chassis mounteo parts |  |  |  |  |  |  |  |  |  |  |  |
| cimern | Soumb |  | cememer | Scticm | , mation |  | somen | Somo | cemer cher | $\left.\right\|_{\text {coume }} ^{\text {Soumo }}$ | Loanton |
| 0050 | \% | cmass | Rse | " | cmsss | ase | \% | crusss |  |  |  |




## 2215 CONTROL SETTINGS

## DC Voltages

horizontal mode
A TRIGGER MODE
AUTO INTENSITY
A SEC/DIV
AUTO

AC Waveforms
VERTICAL MODE
HORIZONTAL MODE
A TRIGGER MODE
A TRIGGER SOURCE
A \& B INT
CH 1 VOLTS/DIV
A TRIGGER LEVEL
CH 1 INPUT
AC-GND-DC
CH 1
A
AUTO
INT
CH 1
1 V
Midrange
$1-\mathrm{kHz}$ sine wove, 4 V P-P
DC



## 2215 CONTROL SETTINGS



3826-12

AUTO INTENSITY \& Z AXIS

| Assemaly aio |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | scome |  | cincur | ( sctem | , 8oano | $\underset{\substack{\text { cricurar } \\ \text { Numeer }}}{ }$ | sctim | goano | Citarema |  | , boano |
|  |  |  |  |  |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  |
| Assembir Al1 |  |  |  |  |  |  |  |  |  |  |  |
|  | ${ }_{\text {semem }}^{\text {scommon }}$ | ${ }_{\text {Leand }}^{\text {Locano }}$ | cincur | Scctem | $\xrightarrow{\text { boang }}$ | cincer cime | ${ }_{\text {Solen }}^{\text {Socalon }}$ | boano |  | Scoten |  |
| ${ }_{\substack{\text { rapo } \\ \text { neos }}}$ | ${ }_{64}^{6 A}$ | ${ }_{\text {iA }}^{\text {iA }}$ | ${ }^{5330}$ | ${ }^{84}$ | ${ }^{24}$ | Wiome | ${ }_{\text {¢ }}^{\substack{88 \\ 68}}$ | ${ }_{4}^{4 A}$ |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  |
| Chassis mounted parts |  |  |  |  |  |  |  |  |  |  |  |
| cincer |  | $\xrightarrow{\text { boano }}$ Localon | $\underbrace{\substack{\text { creur } \\ \text { Nuser }}}_{\text {checur }}$ | $\xrightarrow{\text { scotem }}$ Locaton | $\xrightarrow{\text { boanion }}$ Localon | $\underset{\substack{\text { cricur } \\ \text { Numer }}}{\text { and }}$ | sconem | $\xrightarrow{\text { boanton }}$ | cincent | (icctem | $\xrightarrow{\text { boano }}$ |
| sa0\% | ${ }^{\circ}$ | chassis |  |  |  |  |  |  |  |  |  |

$\underset{\text { - See Parts. List for }}{\text { senal number ranges. }}$


```
    2 2 1 5 ~ C O N T R O L ~ S E T T I N G S ~
DC Voltages
\begin{tabular}{ll} 
HORIZONTAL MODE & A \\
A TRIGCER MODE & AUTO \\
AC-GND-DC & GND
\end{tabular}
AC Waveforms
    HORIZONTAL MODE
    VAR HOLDOFF
    A TRIGGER MODE AUTO
    AC-GND-DC (both)
    MIN (fully CCw)
    GND
```

Horizontal POSITION start of trace at extreme left of vertical line.



| timing switch |  |  |  | (8) |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| assmenv $\mathrm{Alu}^{\text {an }}$ |  |  |  |  |  |
| Semurn | summ | como |  | soumon | Somot |
| jame 7 | " | ${ }^{30}$ | ${ }^{3020} 8$ | " | ${ }^{30}$ |
|  |  |  |  |  |  |
| Assmenivalz |  |  |  |  |  |
| Smeur | somm | 8amo | Semern | ${ }_{\text {a }}^{\text {comb }}$ | Somo |
|  |  |  |  |  |  |
|  |  |  |  |  |  |




Figure 9－10．A19－Current Limit board．


DC Voltages

（25）


A19－CURRENT LIMIT BOARD

| citaut | Sters | citareme | SMemem |
| :---: | :---: | :---: | :---: |
| ${ }_{\text {cias }}^{\text {cas }}$ | \％ | ${ }_{\text {a }}^{\text {ang }}$ | ， |
| coicce | $\stackrel{9}{9}$ |  | \％ |
|  | \％ |  | \％ |
|  | $\stackrel{9}{9}$ | 䧲超 | \％ |
|  | \％ | ${ }_{\text {veas3 }}^{\text {veas }}$ | \％ |


 $2 8 \longdiv { \substack { \text { Probe ground } \\ \text { LEAD on TP915 } } }$

AC Waveforms
WARNING


 $29)_{\mathrm{OV}}$
$30 \square \square \square \square \prod_{\substack{\text { PROBE OROUND } \\ \text { LEAD ON TP934 }}}^{-80 \mathrm{~V}}$ 31 $\bigsqcup \square \square \square \square \bigsqcup_{\substack{\text { Probe orvound } \\ \text { LEAD ON TP934 }}}^{-+ \text {Bov }}$



[^6]POWER SUPPLY，PROBE ADJUST \＆CRT $\stackrel{\rightharpoonup}{9}$

| Assembir | VA10 |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| cen creur | Scten | $\xrightarrow{\text { Batano }}$ Lecton |  | Schan | $\xrightarrow{\text { gocano }}$ | cincour | cock | 80apo | citar | Scticm | gono |
| ${ }^{\text {cata }}$ | ${ }_{88}^{48}$ | $\underbrace{}_{\substack{\text { sk } \\ \text { sFe }}}$ | ${ }_{\text {cren }}^{\text {crear }}$ |  | ${ }^{86}$ | ${ }_{\text {Re87 }}^{\text {Re75 }}$ | ${ }_{4}^{3 N}$ | ${ }_{4}$ | ${ }_{\text {Trosal }}^{\text {Tras }}$ | ${ }_{5}^{\text {¢ }}$ | ${ }_{10}^{80}$ |
| cicce |  | 1 | ${ }^{\text {cosem }}$ | $\underbrace{}_{\substack{\text { st } \\ \text { st } \\ \text { d }}}$ |  |  | ${ }^{50}$ | ${ }_{6}^{\prime \prime}$ | ）391 | ${ }_{5 E}$ |  |
| ceic |  | \％ |  | ¢ |  |  | ， |  | ciccis | $\underset{\substack{64 \\ 24}}{\text { cit }}$ | ${ }_{60}^{61}$ |
|  | $\underset{\substack{2 N \\ 3 N}}{\substack{\text { N }}}$ | 出 | cress | 5 | ${ }_{9}$ |  | ${ }_{4}^{4 L}$ |  | veon | ${ }^{38}$ | ${ }_{\substack{\text { sk }}}$ |
| cick | ¢ | 品 | ¢isser | $\underset{\substack{2 \times \\ 2 \times 2}}{\substack{2 \times}}$ | ${ }_{5}^{51}$ |  | cick |  |  | ¢ |  |
| cick | ${ }_{4}^{4}$ | ${ }_{5}{ }^{\text {sH}}$ |  |  |  | $\underbrace{}_{\substack{\text { Rage } \\ \text { Re87 }}}$ | ${ }_{\substack{4 N \\ 40}}^{40}$ | ＂11 | Vmast | ${ }_{\substack{3, 35}}$ | ${ }_{\text {lok }}^{\text {giok }}$ |
|  | ${ }_{\substack{28 \\ 6 c}}$ |  | 1972 | ${ }^{6}$ | 1 OH |  | coc |  | Watr | ${ }_{5}^{58}$ | 5 |
| cils |  |  |  |  |  |  |  |  |  |  | ¢ |
| coicle |  | com |  | ${ }_{\text {cos }}^{\substack{4 \\ 4 \sim}}$ | ${ }_{\text {iff }}^{\text {if }}$ |  | $\substack{45 \\ 50}_{\substack{45 \\ 50}}$ | $\underbrace{}_{\substack{\text { gx } \\ \text { gx }}}$ |  | ${ }_{3}^{2}$ | 岃 |
| ${ }_{\text {coser }}^{\text {casi }}$ | ${ }_{\substack{46 \\ 36}}^{46}$ | 100 | （ex | ${ }_{\substack{3 N \\ 5 \times \\ \\ \text { N }}}$ | 岕 |  | cois |  |  | ${ }_{3}^{31}$ | ， |
| cos | ${ }_{\substack{36 \\ 46}}^{46}$ | \％os |  | con | ， |  |  | \％ |  |  | 96 |
| cos | ， | ¢ | cosme | $\underbrace{\substack{3 A}}_{\substack{3, 38}}$ | 旡 |  | $\underbrace{46}_{\substack{46 \\ 3_{8}}}$ | （100 | ${ }_{\substack{\text { wese } \\ \text { wess }}}$ | 尤 | ${ }_{96}^{49}$ |
| come | ¢！ | ${ }_{\substack{06 \\ 86}}^{96}$ | ${ }^{\text {peos5．2 }}$ | ${ }_{20}$ | ${ }^{\text {® }}$ |  | ${ }_{4}^{3 F}$ | ${ }_{\text {cox }}^{100}$ |  |  | ${ }_{54}^{96}$ |
| （eam | ${ }_{6}^{61}$ | $\xrightarrow{106}$ |  | ${ }^{36}$ | ${ }_{\text {¢\％}}^{\text {gk }}$ |  | ${ }_{3}^{4 F}$ | \％ok |  |  | \％ |
| cosm | 21 | ¢ |  | $\begin{gathered} 36 \\ 36 \\ 36 \\ \hline \end{gathered}$ |  |  |  | ¢ge |  | ${ }_{\sim}^{\text {g }}$ | ${ }_{8}$ |
| ${ }_{\text {cose }}$ | ${ }_{2}^{2 J}$ | ${ }_{64}$ | cos |  | ${ }_{\text {\％}}^{0}$ |  | ${ }_{\substack{46 \\ 36 \\ 46}}^{4}$ | ${ }_{\text {g\％}}$ |  |  |  |
|  | ${ }_{\substack{3 \\ 4 \times \\ 4 \\ \\ \\ \hline}}$ | \％ | cios | $\substack{35 \\ 46}_{46}$ | ${ }_{\text {OK}}^{\substack{\text { Oox }}}$ |  | 边21 | cont |  |  |  |
| coict | $\underbrace{}_{\substack { 2 \times \\ \begin{subarray}{c}{2 \times{ 2 \times \\ \begin{subarray} { c } { 2 \times } } \\{2 \times}\end{subarray}}$ | ¢ |  | ${ }_{\substack{48 \\ 58}}^{48}$ |  | ع995 | ${ }^{2 J}$ | ${ }_{6}{ }^{\text {ct }}$ |  |  | ${ }_{\substack{\text { ef }}}^{\text {ef }}$ |
|  |  |  |  | ${ }_{3}^{48}$ | ¢ | 5501 | ${ }^{2 A}$ | ${ }^{\text {sk }}$ | Wex70．1． | 号 | ${ }_{5}^{5 \%}$ |
| crean | ${ }_{\substack{48 \\ 60}}$ | ¢ | $\underbrace{}_{\substack{\text { Ref } \\ \text { Reas } \\ \text { Ream }}}$ | ${ }_{4}^{4}$ | ${ }^{4 k}$ |  |  |  |  | $\stackrel{81}{n}$ |  |
|  | cos | ${ }_{8}^{6 \times}$ |  | ¢ | ${ }_{8}$ | ${ }_{\text {Tosa }}$ | ${ }_{4}^{4 \mathrm{H}}$ | ${ }^{81}$ |  | 奀 | ${ }_{5}^{51}$ |
|  | $\underbrace{}_{\substack{56 \\ 3.6 \\ 3.6}}$ | 旡近 |  | ${ }^{2}$ | $\stackrel{\text { nı }}{\text { n／}}$ |  | $\underset{\substack{\text { \％}}}{\substack{\text { c }}}$ | ${ }_{\substack{106 \\ 108}}^{108}$ |  |  |  |
|  | ${ }_{\substack{3 \\ 3 \\ 3 \\ \hline}}$ | ${ }_{76}^{76}$ |  | ${ }_{3}$ | ${ }_{4}^{4}$ | （Tome | ${ }_{4}$ | ${ }_{\substack{88 \\ 88}}^{\text {80，}}$ |  |  |  |

POWER SUPPLY, PROBE ADJUST \& CRT -9 (CONT)

| cincur | Sctem | $\xrightarrow{\text { Roafob }}$ | citar | Scten | ${ }_{\text {boano }}^{\text {boation }}$ |  | Scten | , moano |  | ${ }_{\text {sechen }}^{\text {coction }}$ | ${ }_{\text {bocano }}^{\text {boation }}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| ${ }^{\text {c331 }}$ | ${ }^{80}$ | 10 |  | ${ }^{3 \mathrm{~s}}$ | ${ }_{24}^{18}$ |  |  |  | 0535 | ${ }^{\text {sc }}$ | ${ }^{10}$ |
|  | ${ }_{\substack{80 \\ 80}}$ | 10 | cex poose | "10 | ${ }^{24}$ |  |  |  | (wions |  | ${ }_{\substack{4 a_{8} \\ 48}}^{4}$ |
| P1200. 1 | ${ }_{\text {®E }}$ | ${ }_{8}$ |  |  |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  |
| Assembila Al9 |  |  |  |  |  |  |  |  |  |  |  |
|  | $\xrightarrow{\text { scomen }}$ Locatom | , boaro | $\underset{\substack{\text { arcuir } \\ \text { Number }}}{\text { coser }}$ | Sctem | ${ }_{\text {L }}^{\text {Locano }}$ |  | ${ }_{\text {Lemen }}^{\text {Scomm }}$ | - boanion | cinctir | Sctem |  |
| ${ }^{\text {c988 }}$ | ${ }^{26}$ | ${ }^{18}$ |  |  | ${ }^{18}$ |  |  |  | ${ }^{\text {ar935 }}$ | ${ }^{\text {F }}$ | ${ }^{28}$ |
| cos | $\underbrace{}_{\substack { 2 E \\ \begin{subarray}{c}{\text { KE }{ 2 E \\ \begin{subarray} { c } { \text { KE } } } \\{\text { 2F }}\end{subarray}}$ |  |  | ${ }_{\substack{26 \\ 16}}^{26}$ | ${ }_{\substack{18 \\ 18}}^{18}$ |  |  |  | ${ }_{\text {vegs }}^{\text {veas }}$ | ${ }_{\text {VE }}^{1 /}$ | ${ }_{\substack{26 \\ 26}}^{2}$ |
|  |  |  | ${ }_{\substack{\text { a } \\ 0938 \\ 0939}}$ | $\underset{\substack{16 \\ 16}}{\substack{\text { cid }}}$ | ${ }_{28}^{28}$ |  |  |  |  |  |  |
| Chassis mounted parts |  |  |  |  |  |  |  |  |  |  |  |
| cincur | Scitem | ${ }_{\text {brafo }}^{\text {Boarow }}$ | qrecur | Scike | ${ }^{\text {Brafap }}$ |  | Scten |  | circur | Sctem | ${ }^{\text {Soado }}$ |
| ${ }^{580}$ | ${ }^{24}$ | crassis | ${ }^{1225}$ | ${ }^{10}$ | ${ }^{\text {chasssis }}$ |  |  |  |  |  | $\xrightarrow[\substack{\text { chasssis } \\ \text { Chassis }}]{\text { ase }}$ |
| und | ${ }^{38}$ |  |  | $\begin{gathered} \text { on } \\ \text { sim } \\ \hline 0.0 \end{gathered}$ | $\left\lvert\, \begin{gathered} \text { chassis } \\ \text { chassis } \\ \text { Chatssis } \end{gathered}\right.$ |  | $\begin{gathered} 4 N \\ \left.\begin{array}{c} 3 N \\ 3 N \end{array}\right) \\ \hline 20 \end{gathered}$ |  | - | ${ }^{1} \times$ |  |




Figure 9.11. A13-Alt sweep board


A13-ALT SWEEP BOARD

|  | satal |  |  | amam |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | , mas |  |  |  |  |  |

2215 CONTROL SETTINGS


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## ALTERNATE B SWEEP

| ASSEMBLY A10 |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| CIRCUIT NUMBER | $\begin{aligned} & \text { SCHEM } \\ & \text { LOCATION } \end{aligned}$ | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATION } \\ & \hline \end{aligned}$ | CIRCUIT NUMBER | $\begin{aligned} & \text { SCHEM } \\ & \text { LOCATION } \end{aligned}$ | BOARD <br> LOCATION | CIRCUIT NUMBER | $\begin{aligned} & \text { SCHEM } \\ & \text { LOCATION } \end{aligned}$ | 80ARD LOCATION | CIRCUIT NUMBER | SCHEM LOCATION | BOARD LOCATION |
| C564 | 8C | 8A | R607 | 2 C | 7 F | W1001.25 | 2 C | 8A | W4000-15 | 3D | 8 F |
| C569 | 90 | 7A | R649 | 1 C | 10C | W1001-26 | 2 C | 8A | W4000-16 | 3D | 8 F |
| C601 | 2C | 9 B | R651 | 3 D | 9 F | W1001-27 | 2 C | 8A | W4000-17 | 6 D | 8 F |
| C608 | 3 C | 8G | R673 | 6 C | 8 F | W1001-28 | 2 C | 8A | W4000-19 | 1D | 8 F |
| C648 | 1 C | 8G | R674 | 60 | 8 F | W1001-29 | 2 C | 8A | W4000-20 | 3D | 8 F |
| C651 | 1 C | 7A |  |  |  | W1001-30 | 7A | 8A | W4000-21 | 2D | 9 F |
| C658* | 40 | 10E | VR657 | 3 D | 9 E | W4000-1 | 8 D | 6G | W4000-22 | 5 D | 9 F |
|  |  |  |  |  |  | W4000-2 | 70 | 6 F | W6001-2* $\ddagger$ | $\ddagger$ 6C | 8 F |
| P6001-2" $\ddagger$ | 6 C | 8 F | W564 | 80 | 8G | W4000-4 | 9 D | 7F | W6001-3* $\ddagger$ | $\pm$ 6C | 8 F |
| P6001-3* $\ddagger$ | 6 C | 8 F | W571 | 90 | 8G | W4000-6 | 2 D | 7 F | W6001-8* $\ddagger$ | $\ddagger$ 5C | 8F |
| P6001-8* $\ddagger$ | 5 C | 8 F | W606 | 30 | 7 F | W4000-10 | 2D | 7F |  |  |  |
| P7055-1 | 4 C | 10F | W650 | 4D | 10F | W4000-12 | 2D | 8 F |  |  |  |
| P7055-2 | 4 C | 10F | W1001-17 | 1 C | 6 6 | W4000-13 | 10 | 8 F |  |  |  |
| P7055-3 | 3 C | 10F | W1001-18 | 9 C | 7A | W4000-14 | 4D | 8 F |  |  |  |
| Partial A10 also shown on diagrams 2, 3, 4, 5, 6, 7 and 9. |  |  |  |  |  |  |  |  |  |  |  |
| ASSEMBLY 411 |  |  |  |  |  |  |  |  |  |  |  |
| CIRCUIT <br> NUM8ER | SCHEM <br> LOCATION | $\begin{aligned} & \text { 8OARD } \\ & \text { LOCATION } \end{aligned}$ | CIRCUIT <br> NUMBER | $\begin{aligned} & \text { SCHEM } \\ & \text { LOCATION } \end{aligned}$ | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATION } \end{aligned}$ | CIRCUIT NUM8ER | $\begin{aligned} & \text { SCHEM } \\ & \text { LOCATION } \end{aligned}$ | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATION } \end{aligned}$ | CIRCUIT <br> NUM8ER | $\begin{aligned} & \text { SCHEM } \\ & \text { LOCATION } \end{aligned}$ | BOARD LOCATION |
| C650 | 2 C | 1 E | $\mathbf{S 6 5 0}$ | 2A | 2 E | W1000-18 | 9 C | 4 C | $\begin{aligned} & \text { W1000-29 } \\ & \text { W1000-30 } \end{aligned}$ | $\begin{aligned} & 2 \mathrm{C} \\ & 7 \mathrm{C} \end{aligned}$ | $\begin{aligned} & 4 D \\ & 4 D \end{aligned}$ |
|  |  |  |  |  |  | W1000-25 | 2C | 4D |  |  |  |
| R557 | 9 C | 1F | W636 W638 | 9 C8 C | 1 F1 F | W1000-26 | 2 C | 40 |  |  |  |
|  |  |  |  |  |  | W1000-27 | 2 C | 4 D |  |  |  |
| S564 | 7 C | 1E | W1000-17 | 1C | 4 C | W1000-28 | 2 C | 4D |  |  |  |
| Partial All afso shown on diagrams 1, 2, 3, 4, 5, 6, 7, 8 and 9. |  |  |  |  |  |  |  |  |  |  |  |
| ASSEMBLY A12 |  |  |  |  |  |  |  |  |  |  |  |
| CIRCUIT NUMBER | SCHEM LOCATION | BOARD Location | CIRCUIT NUMBER | $\begin{aligned} & \text { SCHEM } \\ & \text { LOCATION } \end{aligned}$ | BOARD <br> Location | CIRCUIT NUMBER | $\begin{aligned} & \text { SCHEM } \\ & \text { LOCATION } \end{aligned}$ | BOARD LOCATION | CIRCUIT <br> NUMBER | SCHEM LOCATION | BOARD location |
| c680 | 58 |  | $\begin{aligned} & \text { P6000-2 } \\ & \text { P6000-3 } \\ & \text { P6000-8 } \end{aligned}$ | $\begin{aligned} & 6 C \\ & 6 C \\ & 5 C \end{aligned}$ | $\begin{aligned} & 4 \mathbf{F} \\ & 3 \mathbf{F} \\ & \mathbf{3 F} \end{aligned}$ | 0680A <br> 0680B <br> 0681 | $\begin{aligned} & 5 B \\ & 6 B \\ & 5 B \end{aligned}$ | $\begin{aligned} & \text { 3E } \\ & 3 E \\ & 3 F \end{aligned}$ | R680 R681 | $\begin{aligned} & \text { 5B } \\ & 5 B \end{aligned}$ | $\begin{aligned} & 3 F \\ & 2 F \end{aligned}$ |
| CR676 | 68 | 3 E |  |  |  |  |  |  |  |  |  |
| CR680 | 68 | 3F |  |  |  |  |  |  |  |  |  |
| Partial A12 also shown on diagrams 7, 5, 7 and 8. |  |  |  |  |  |  |  |  |  |  |  |

alternate b sweep (i0) (cont)

| Assembly at3 |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| cicter | Sctem | (coaro |  | $\xrightarrow{\text { cremen }}$ | $\xrightarrow{\text { boano }}$ Lochon |  | Scten |  |  | ( scten | , 8oono |
|  |  |  |  |  |  |  |  |  |  |  |  |
| Chastis mounte pants |  |  |  |  |  |  |  |  |  |  |  |
| ctick | ( | ${ }_{\substack{\text { gafion } \\ \text { Coafow }}}$ | $\underbrace{\substack{\text { creur } \\ \text { Numer }}}_{\text {crect }}$ | chten | ${ }_{\text {goano }}^{\text {goaton }}$ | cincur | (en | gatao | circur | sccmen |  |
| ${ }^{\text {Re68 }}$ | ${ }^{4}$ | chasss |  |  |  |  |  |  |  |  |  |

See Parts List for



A10-MAIN BOARD ADJUSTMENT LOCATIONS


A12-ATTENUATOR/SWEEP AND A13-ALT SWEEP boards ADJUSTMENT LOCATIONS

$$
\begin{aligned}
& \text { SPECIFIC NOTES } \\
& \text { Set initiol front-panel controls os follows: }
\end{aligned}
$$












NOTE




Verify the -2kV supply of pin 2 of the ort socket.
The vol toge should be between -1 geve ond -21 evev.
warning
The Preregulator ond Invert ter circults have a flooting






## 



$\underset{\substack{\text { VERTICALY } \\ \text { off CENTER }}}{ }$





# REPLACEABLE <br> MECHANICAL PARTS 

## PARTS ORDERING INFORMATION

Replacement parts are available from or through your local Tektronix, Inc. Field Office or representative

Changes to Tektronix instruments are sometimes made to accommodate improved components as they become available, and to give you the benefit of the latest circuit improvements developed in our engineering department. It is therefore important, when ordering parts, to include the following information in your order: Part number, instrument type or number, serial number, and modification number if applicable.

If a part you have ordered has been replaced with a new or improved part, your local Tektronix, Inc. Field Office or representative will contact you concerning any change in part number.

Change information, if any, is located at the rear of this manual

SPECIAL NOTES AND SYMBOLS
X000 Part first added at this serial number
00X Part removed after this serial number

## FIGURE AND INDEX NUMBERS

Items in this section are referenced by figure and index numbers to the illustrations.

| $"$ | INCH |
| :--- | :--- |
| $\#$ | NUMBER SIZE |
| ACTR | ACTUATOR |
| ADPTR | ADAPTER |
| ALIGN | ALIGNMENT |
| AL | ALUMINUM |
| ASSEM | ASSEMBLED |
| ASSY | ASSEMBLY |
| ATTEN | ATTENUATOR |
| AWG | AMERICAN WIRE GAGE |
| BD | BOARD |
| BRKT | BRACKET |
| BRS | BRASS |
| BRZ | BRONZE |
| BSHG | BUSHING |
| CAB | CABINET |
| CAP | CAPACITOR |
| CER | CERAMIC |
| CHAS | CHASSIS |
| CKT | CIRCUIT |
| COMP | COMPOSITION |
| CONN | CONNECTOR |
| COV | COVER |
| CPIG | COUPLING |
| CRT | CATHODE RAY TUBE |
| DEG | DEGREE |
| DWR | DRAWER |

## INDENTATION SYSTEM

This mechanical parts list is indented to indicate item relationships. Following is an example of the indentation system used in the description column.

> 12345
> Name \& Description
> Assembly and/or Component
> Attaching parts for Assembly and/or Component
> - - * - -

> Detail Part of Assembly and/or Component
> Attaching parts for Detail Part
> -- - *--
> Parts of Detail Part
> Attaching parts for Parts of Detail Part
$\qquad$

Attaching Parts always appear in the same indentation as the item it mounts, while the detail parts are indented to the right. Indented items are part of, and included with, the next higher indentation. The separation symbol--- - -- indicates the end of attaching parts.

Attaching parts must be purchased separately, unless otherwise specified.

## ITEM NAME

In the Parts List, an Item Name is separated from the description by a colon (i). Because of space limitations, an Item Name may sometimes appear as incomplete. For further Item Name identification, the U.S. Federal Cataloging Handbook H6-1 can be utilized where possible.

## ABBREVIATIONS

| ELCTRN | ELECTRON | IN | INCH | SE | SINGLE END |
| :---: | :---: | :---: | :---: | :---: | :---: |
| ELEC | ELECTRICAL | INCAND | INEANDESCENT | SECT | SECTION |
| ELCTLT | ELECTROLYTIC | INSUL | INSULATOR | SEMICOND | SEMICONDUCTOR |
| ELEM | ELEMENT | INTL | INTERNAL | SHLD | SHIELD |
| EPL | ELECTRICAL PARTS LIST | LPHLDR | LAMPHOLDER | SHLDR | SHOULDERED |
| EQPT | EOUIPMENT | MACH | MACHINE | SKT | SOCKET |
| EXT | EXTERNAL | MECH | MECHANICAL | SL | SLIDE |
| FIL | FILLISTER HEAD | MTG | MOUNTING | SLFLKG | SELF-LOCKING |
| FLEX | FLEXIBLE | NIP | NIPPLE | SLVG | SLEEVING |
| FLH | FLAT HEAD | NON WIRE | NOT WIRE WOUND | SPR | SPAING |
| FLTR | FILTER | OBD | ORDER BY DESCRIPTION | SQ | SQUARE |
| FR | FRAME or FRONT | OD | OUTSIDE DIAMETER | SST | ESS |
| FSTNR | FASTENEA | OVH | OVAL HEAD | STL | STEEL |
| FT | FOOT | PH BRZ | PHOSPHOR BRONZE | SW | SWITCH |
| EXD | FIXED | PL | PLAIN or PLATE | T | TUBE |
| GSKT | GASKET | PLSTC | PLASTIC | TERM | TERMINAL |
| HDL | HANDLE | PN | PAFT NUMBER | THD | THREAD |
| HEX | HEXAGON | PNH | PAN HEAD | THK | THICK |
| HEX HD | HEXAGONAL HEAD | PWR | POWER | TNSN | TENSION |
| HEX SOC | HEXAGONAL SOCKET | RCPT | RECEPTACLE | TPG | TAPPING |
| HLCPS | HELICAL COMPRESSION | RES | RESISTOR | TRH | TRUSS HEAD |
| HLEXT | HELICAL EXTENSION | RGD | RIGID | $V$ | VOLTAGE |
| HV | HIGH VOLTAGE | RLF | REL.IEF | VAR | VARIABLE |
| IC | INTEGRATED CIRCUIT | RTNR | RETAINER | W/ | WITH |
| ID | INSIDE DIAMETER | SCH | SOCKET HEAD | WSHR | WASHER |
| IDENT | IDENTIFICATION | SCOPE | OSCiLLOSCOPE | XFMR | TRANSFORMER |
| IMPLR | IMPELLER | SCR | SCREW | XSTR | TRANSISTOR |


| Mfr. Code | Manufacturer | Address | City, State, Zip |
| :---: | :---: | :---: | :---: |
| S3629 | PANEL COMPONENTS CORP. | 2015 SECOND ST. | BERKELEY, CA 94170 |
| 00779 | AMP, INC. | P O B0X 3608 | HARRISBURG, PA 17105 |
| 01536 | CAMCAR DIV OF TEXTRON INC. SEMS PRODUCTS UNIT | 1818 CHRISTINA ST. | ROCKFORD, IL 61108 |
| 02768 | ILLINOIS TOOL WORKS, INC., FASTEX DIV. | 195 ALgONQUIN ROAD | DES PLAINES, IL 60016 |
| 05129 | KILO ENGINEERING COMPANY | 2015 D | LA VERNE, CA 91750 |
| 05820 | WAKEFIELD ENGINEERING, INC. | AUDUBON ROAD | WAKEFIELD, MA 01880 |
| 12327 | FREEWAY CORPORATION | 9301 ALLEN DRIVE | CLEVELAND, OH 44125 |
| 13103 | THERMALLOY COMPANY, INC. | 2021 W VALLEY VIEW LANE P O BOX 34829 | DALLAS, TX 75234 |
| 16428 | BELDEN CORP. | P. O. BOX 1331 | RICHMOND, IN 47374 |
| 22526 | BERG ELECTRONICS, INC. | YOUK EXPRESSWAY | NEW CUMBERLAND, PA 17070 |
| 23050 | PRODUCT COMPONENTS CORP | 30 LORRAINE AVE. | MT VERNON, NY 10553 |
| 24931 | SPECLALITY CONNECTOR CO., INC. | 2620 ENDRESS PLACE | GREENWOOD, IN 46142 |
| 28520 | HEYMAN MFG. CO. | 147 N. MICHIGAN AVE. | KENILWORTH, NJ 07033 |
| 71279 | CAMBRIDGE THERMIONIC CORP. | 445 CONCORD AVE. | CAMBRIDGE, MA 02138 |
| 71400 | BUSSMAN MFG., DIVISION OF MCGRAWEDISON CO. | 2536 W. UNIVERSITY ST. |  |
| 73743 | FISCHER SPECIAL MFG. CO. | 446 MORGAN ST. | CINCINNATI, OH 45206 |
| 77250 | PHEOLL MANUFACTURING CO., DIVISION OF ALLIED PRODUCTS CORP. | $5700 \mathrm{~W} . \mathrm{ROOSEVELT}$ RD. | CHICAGO, IL 60650 |
| 78189 | ILLINOIS TOOL WORKS, INC. SHAKEPROOF DIVISION | ST. CHARLES ROAD | ELGIN, IL 60120 |
| 79807 | WROUGHT WASHER MFG. CO. | 2100 S. O BAY ST. | MILWAUKEE, WI 53207 |
| 80009 82389 | TEKTRONIX, INC. | P O BOX 500 5555 N ELSTON AV | BEAVERTON, OR 97077 |
| 83385 | CENTRAL SCREW CO. | 2530 CRESCENT DR. | CHICAGO, IL 60630 BROADVIEW, IL 60153 |
| 89663 | REESE, J. RAMSEY, INC, | 71 MURRAY STREET | NEW YORK, NY 10007 |
| 93907 | TEXTRON INC. CAMCAR DIV | 60018 TH AVE | ROCKFORD, IL 61101 |
| 95987 | WECKESSER CO., INC. | 4444 WEST IRVING PARK RD. | CHICAGO, IL 60641 |

Fig. \&

| Index No. | Tektronix Part No. | Serial/Model No. Eff Dscont | Qty | 12345 Name \& Description | Mfr Code | Mfr Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1-1 | 334-5001-00 |  | 1 | Marker, ident mid caution | 80009 | 334-5001-00 |
| -2 | 200-2538-00 |  | 1 | COVER, REAR: PLASTIC (attaching parts) | 80009 | 200-2538-00 |
| -3 | 211-0691-00 |  | 2 | SCREW, MACHINE: 6-32 X 0.625, PNH | 01536 | ObD |
| -4 | 390-0790-06 |  | 1 | CABINET, SCOPE:W/FEET | 80009 | 390-0790-00 |
| -5 | 348-0659-00 |  | 2 | . FOOT,CABINET:BLACK POLYuRETHANE | 80009 | 348-0659-00 |
| -6 | 213-0882-00 |  | 1 | SCREW, TAPPING: 6 - $32 \times 0.437$ TYPE C | 01536 | OBD |
| -7 | 334-4170-00 |  | 1 | MARKER, IDENT: | 80009 | 334-4170-00 |
| -8 | 367-0289-00 |  | 1 | HANDLE, CARRYING: 13.855 ,SST <br> (attaching parts) | 80009 | 367-0289-00 |
| -9 | 212-0144-00 |  | 2 | SCREW, TPG, TF: $8-16 \times 0.562 \mathrm{~L}$ | 93907 | 225-38131-012 |
| -10 | 334-5002-00 |  | 1 | Plate, ident:mid tektronix | 80009 | 334-5002-00 |
| -11 | 426-1765-00 |  | 1 | FRAME, CRT: <br> (AtTACHING PARTS) | 80009 | 426-1765-00 |
| -12 | 211-0690-00 |  | 2 | SCREW, MACHINE: 6-32 ${ }^{\text {a }} 0.875$, PNH, STL | 01536 | OBD |
| -13 | 337-2775-00 |  | 1 | SHLD, implosion: | 80009 | 337-2775-00 |
| -14 | 366-1833-00 |  | 7 | KNOB: GRAY, 0.25 ID $\times 0.392 \times 0.3920 \mathrm{D}$ | 80009 | 366-1833-00 |
| -15 | 366-1701-00 | в010100 в010404 | 1 | KNOB:GY, 0.127 ID X0.392 OD X 0.4 | 80009 | 366-1701-00 |
|  | 366-1701-01 | B010405 | 1 | KNOB: GY, 0.127 ID X0. 392 OD X 0.4 | 80009 | 366-1701-01 |
| -16 | ----- ----- |  | 1 | RES, VAR, NONWIR: (SEE R647 REPL) <br> (attaching Parts) |  |  |
| -17 | 210-0583-00 |  | 1 | NUT, PLAIN, HEX: $0.25-32 \times 0.312$ INCH, BRS | 73743 | 2X20317-402 |
| -18 | 210-0940-00 |  | 1 | WASHER,FLAT: 0.25 ID X 0.375 INCH OD, STL | 79807 | OBD |
|  | 210-0021-00 | XB010405 | 1 | WASHER,LOCK:INTL,0.476 ID X $0.60^{\circ} \mathrm{OD}$ STL | 78189 | 1222-01-00-0541C |
| -19 | ----- ----- |  | 2 | CONNECTOR, RCPT, : (SEE J1001, J2001 REPL) |  |  |
| -20 | ----- ----- |  | 1 | CONN, RCPT, ELEC:(SEE J4001 RePL) |  |  |
| -21 | 210-0255-00 |  | 1 | TERMINAL,LUG:0.391 ID,LOCKING, BRS CD PL | 80009 | 210-0255-00 |
| -22 | 384-1575-00 |  | 1 | EXTENS ION, Shaft: Focus W/KNOB, PLASTIC | 80009 | 384-1575-00 |
| -23 | 358-0550-00 |  | 1 | BUSHING, SHAFT:0.15 Id X 0.3INCH OD, PLSTC | 80009 | 358-0550-00 |
| -24 | 136-0387-01 |  | 1 | JACK, TIP: BLACK | 71279 | 450-4252-01-0310 |
| -25 | 366-1031-03 |  | 1 | KNOB: RED--CAL | 80009 | 366-1031-03 |
| -26 | 366-1838-01 |  | 1 | KNOB:Gy, 0.249 Id x0.718 $\times 1.765$ | 80009 | 366-1838-01 |
| -27 | 358-0640-00 |  | 1 | BSHG, MACH THD: 0.5-32 THD X 0.39 ID | 80009 | 358-0640-00 |
| -28 | 366-1405-08 |  | 1 | KNOB: RED, CAL, $0.082 \mathrm{ID} \times 0.4500$ | 80009 | 366-1405-08 |
| -29 | 366-1840-00 |  | 1 | KNOB:GY, TIME/DIV,0.127 ID X 0.855 | 80009 | 366-1840-00 |
| -30 | 366-1850-00 |  | 1 | KNOB:CLEAR, 0.252 Id $\times 1.2$ od $\times 0.383$ | 80009 | 366-1850-00 |
| -31 | 331-0328-00 |  | 1 | dial, CONTROL: 10 turn For 0.25 dia Shart | 05129 | 461-5-70 |
| -32 | 210-0840-00 |  | 1 | WASHER, FLAT: 0.39 ID X 0.562 INCH OD, STL | 89663 | 644 R |
| -33 |  |  | 1 | RES., VAR, WW: (SEE R658 REPL) |  |  |
| -34 | 333-2679-00 |  | 1 | PANEL, FRONT: | 80009 | 333-2679-00 |
| -35 | 366-2013-00 |  | 2 | PUSH, BUTTON: DIRTY GRAY, 0.134 SQ X 0.480 H | 80009 | 366-2013-00 |
| -36 | 348-0660-00 |  | 4 | CUSHION, CRT: POLYURETHANE | 80009 | 348-0660-00 |
| -37 | 386-4444-00 |  | 1 | SUBPANEL, FRONT: <br> (attaching parts) | 80009 | 386-4444-00 |
| -38 | 213-0881-00 |  | 4 | SCREW, TAPPING: 6-32 $\times 0.25$ TYPE $C$ | 01536 | OBD |
| -39 | 213-0882-00 |  | 2 | SCREW, TAPPING: 6-32 X 0.437 TYPE C | 01536 | OBD |
| -40 | 377-0512-00 |  | 7 | INSERT, KNOB:0.125 ID X $0.663 \mathrm{~L}, \mathrm{AL}$ | 80009 | 377-0512-00 |
| -41 | 129-0836-00 |  | 2 | SPACER, POST:1.207 L, W/0.5-32 THD | 80009 | 129-0836-00 |
|  | 213-0228-00 |  | 2 | . SETSCREW:6-32 X 0.125, STL CD PL, HEX SKT |  |  |
| -42 | 384-1503-00 |  | 2 | EXTENSION SHAFT: $5.4 \mathrm{~L} \times 0.124 \mathrm{DIA}, \mathrm{PlASTIC}$ | 80009 | 384-1503-00 |
| -43 | 384-1323-00 |  | 1 | EXTENSION SHAFT: $6.4 \mathrm{l} \times 0.0810 \mathrm{D}$ SST | 80009 | 384-1323-00 |
| -44 | ----- ----- |  | 1 | CKT BOARD ASSY:ATTEN/SWEEP(SEE A12 REPL) |  |  |
| -45 | 211-0304-00 |  | 2 | SCREW,MACHINE:4-40 X 0.312, PNH <br> - - - * - - | 01536 | OBD |
|  | ----- ----- |  | - | CKT board assy includes: |  |  |
| -46 | ---- ---- |  | 25 | . TERMINAL, PIN:(SEE A12P1010,P2010,P6001, <br> . P7000 REPL) |  |  |
| -47 | 136-0328-02 |  | 10 | SOCKEt, PIN TERM: horizontal | 00779 | 86282-2 |

Fig. \&

| Index | Tektronix | Serial/Model No. |  |  | Mfr |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| No. | Part No. | Eff | Dscont | Qty | 12345 | Name \& Description | Code | Mfr Part Number


| 1-48 | ----- ----- | 1 | . RES, VAR, NONWIR: (SEE A12S734, R629 REPL) (ATTACHING PARTS) |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| -49 | 210-0413-00 | 1 | . NUT, PLAIN, HEX.:0.375-32 X 0.50 INCH, STL | 73743 | 3145-402 |
| -50 | 210-0012-00 | 1 | - WASHER, LOCK: INTL, 0. 375 ID X $0.50^{\prime \prime}$ OD STL - - - * - - - | 78189 | 1220-02-00-0541C |
| -51 | ----- ----- | 1 | . SWITCH, ROTARY: (SEE Al2S630A, B, C REPL) |  |  |
| -52 | ----- --- | 2 | . RES,VAR, NONWIR: (SEE A12R141,R241 REPL) (ATTACHING PARTS) |  |  |
| -53 | 210-0413-00 | 1 | . NUT, PLAIN, HEX.:0.375-32 X 0.50 INCH, STL | 73743 | 3145-402 |
| -54 | 210-0012-00 | 1 | - WASHER,LOCK:INTL,0.375 ID X 0.50" OD STL, | 78189 | 1220-02-00-0541C |
| -55 | ----- ----- | 2 | . SWITCH, ROTARY: (SEE A12S105, S205 REPL) |  |  |
| -56 | 407-2585-00 | 1 | BRACKET,GROUND: CIRCUIT BOARD, BRASS | 80009 | 407-2585-00 |
| -57 | 337-2892-00 | 1 | SHIELD,ELEC:CIRCUIT BOARD <br> (ATTACHING PARTS) | 80009 | 337-2892-00 |
| -58 | 211-0304-00 | 1 | SCREW, MACHINE:4-40 X 0.312, PNH | 01536 | OBD |
| -59 | 129-0906-00 | 1 | SPACER,POST:0.685 L W/4-40 INT \& EXT THD - - - * - - | 80009 | 129-0906-00 |
| -60 | ----- ----- | 1 | cKt board assy:front panel(see all repl) (ATtaching parts) |  |  |
| -61 | 211-0304-00 | 2 | SCREW, MACHINE: 4-40 X 0.312, PNH | 01536 | OBD |
|  | ----- m---n | - | CKT BOARD ASSY INCLUDES: |  |  |
| -62 | ----- ----- | 4 | . TERMINAL, PIN: (SEE Al1J1000 REPL) |  |  |
| -63 | ----- ----- | 10 | . TERMINAL, PIN: (SEE AllJ2001 REPL) |  |  |
| -64 | --- ----- | 9 | . SWITCH, SLIDE: (SEE AllS101, S201,S305,S315, <br> . . S317,S401, $5440,5611,5650$ REPL) |  |  |
| -65 | 361-1081-00 | 1 | - SPACER,LED: PLASTIC | 80009 | 361-1081-00 |
| -66 | - | 1 | , RES,VAR, NONWIR: (SEE AllR455 REPL) |  |  |
| -67 | ----- ----- | 1 | - RES,VAR, NONWIR: (SEE AllR557 REPL) |  |  |
| -68 | ----- ----- | 2 | - SWITCH,SLIDE: (SEE AllS464, S564 REPL) |  |  |
| -69 | ----- ----- | 1 | , SWITCH, PUSH: (SEE AllS264 REPL) |  |  |
| -70 | --->" ----- | 1 | - SWITCH, PUSH: (SEE AllS390 REPL) |  |  |
| -71 | ----- ----- | 5 | - RES,VAR, NONWIR: (SEE AllR190,R290,R395,R726, . . R807 REPL) |  |  |
| -72 | 407-2584-00 | 1 | BRACKET, GROUND: FRONT PANEL, BRASS <br> (ATTACHING PARTS) | 80009 | 407-2584-00 |
| -73 | 211-0304-00 | 2 | SCREW, MACHINE: 4-40 X 0.312, PNH | 01536 | OBD |
| -74 | 343-0089-00 | 1 | CLAMP, LOOP: LARGE | 80009 | 343-0089-00 |
| -75 | 441-1535-00 | 1 | CHASSIS, SCOPE:FRONT MAIN <br> (attaching parts) | 80009 | 441-1535-00 |
| -76 | 213-0881-00 | 2 | SCREW, TAPPING: 6-32 X 0.25 TYPE C | 01536 | OBD |
|  | -- ---- | 1 | TRANSISTOR: (SEE Q938 REPL) (ATTACHING PARTS) |  |  |
|  | 211-0318-00 | 1 | SCREW,MACHINE:4-40 X 0.75, FLH, 100 DEG | 93907 | OBD |
|  | 210-0586-00 | I | NUT, PL, ASSEM WA:4-40 X 0.25 , STL CD PL - - - * - - - | 83385 | OBD |
|  | 342-0582-00 | 1 | INSULATOR, PLATE: TRANSISTOR, CERAMIC | 80009 | 342-0582-00 |
|  | 343-1025-00 | 1 | RETAINER, XSTR: | 80009 | 343-1025-00 |
|  | ----- ----- | 1 | CKT BOARD ASSY: CURRENT LIMIT(SEE A19 REPL) |  |  |
|  | 344-0154-03 | 2 | . CLIP, ELECTRICAL:FUSE, CKT BD MT | 80009 | 344-0154-03 |




Fig. \&

| Index <br> No. | Tektronix <br> Part No. | Serial/Model No. Eff Dscont | Qty | 12345 Name \& Description | Mfr Code | Mfr Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 2-1 | 334-4251-00 |  | 1 | MARKER, IDENT:MKD CAUTION | 80009 | 334-4251-00 |
| -2 | 337-2773-00 |  | 1 | SHIELD, ELEC: POWER SUPPLY,LOWER, PLSTC (AtTACHING PARTS) | 80009 | 337-2773-00 |
| -3 | 211-0304-00 |  | 1 | SCREW, MACHINE:4-40 X 0.312, PNH | 01536 | ObD |
| -4 | 386-4613-00 |  | 1 | SUPPORT, SHIELD: <br> (attachinc parts) | 80009 | 386-4613-00 |
| -5 | 211-0305-00 |  | 2 | SCREW,MACHINE:4-40 X 0.437, PNH | 01536 | OBD |
| -6 | 334-4251-00 |  | 1 | MARKER, IDENT:MKD CAUTION | 80009 | 334-4251-00 |
| -7 | 348-0721-00 |  | 1 | GROMMET, PLASTIC:BLACK POLYSUFONE | 80009 | 348-0721-00 |
| -8 | 348-0555-00 |  | 1 | CROMMET, PLASTIC:SIL GY, U SHAPE,0.52 Id | 80009 | 348-0555-00 |
| -9 | 344-0334-00 |  | 1 | CLIP,CIRCUIT BD:PLASTIC | 80009 | 344-0334-00 |
| -10 | 337-2772-00 |  | 1 | SHIELD, ELEC: POWER SUPPLY,AL (attaching parts) | 80009 | 337-2772-00 |
| -11 | 211-0304-00 | в010100 в012542 | 3 | SCREW, MACHINE:4-40 X 0.312, PNH | 01536 | ObD |
|  | 211-0305-00 | B012543 | 3 | SCREW, MACHINE:4-40 X 0.437, PNH | 01536 | ObD |
| -12 | 211-0303-00 |  | 2 | SCREW, MACHINE:4-40 X 0.25, FLH 100 DEG | 01536 | OBD |
| -13 | 366-1480-03 |  | 1 | PUSH BUTTON: BLACK, OfF | 80009 | 366-1480-03 |
| -14 | 384-1576-00 | в010100 в019849 | 1 | EXTENSION SHAFT: 12.809 L , PLASTIC | 80009 | 384-1576-00 |
|  | 384-1576-01 | B019850 | 1 | EXTENSION SHAFT: 12.809 L,PLASTIC | 80009 | 384-1576-01 |
| -15 | 337-2915-00 |  | 1 | shield, elec:alternate sweep <br> (attaching parts) | 80009 | 337-2915-00 |
| -16 | 211-0304-00 |  | 2 | SCREW,MACHINE:4-40 X 0.312, PNH | 01536 | OBD |
| -17 | 129-0906-00 |  | 2 | SPACER,POST:0.685 L W/4-40 INT \& EXT THD (attaching parts) | 80009 | 129-0906-00 |
| -18 | 210-0586-00 |  | 2 | NUT, PL, ASSEM WA: $4-40 \times 0.25$, STL CD PL $-\quad-*--$ | 83385 | OBD |
| -19 | ----- ----- |  | 1 | CKt board assy : Alternate sweep (see al3 Repl) |  |  |
| -20 | 131-0589-00 |  | 27 | . TERMINAL, PIN:0.46 L X 0.025 SQ | 22526 | 48283-029 |
| -21 | - |  | 1 | CKT BOARD ASSY:MAIN(SEE AIO REPL) |  |  |
| -22 | 213-0882-00 |  | 3 | SCREW, TAPPING: 6-32 X 0.437 TYPE C | 01536 | OBD |
| -23 | 211-0302-00 |  | 2 | SCREW, MACHINE: $4-40 \times 0.75$, PNH | 01536 | OBD |
|  | ----- ----- |  | - | CKT board assy includes: |  |  |
| -24 | --------------- |  | $3$ | - TERMINAL, PIN: (SEE A10P1011,P2011,P6001, . P7001, P7055 REPL) |  |  |
| -25 | ----- ----- |  | 1 | . RES,VAR, NONWIR: (SEE AlOR883 REPL) (ATTACHING PARTS) |  |  |
| -26 | 220-0495-00 |  | 1 | . NUT, PLAIN, HEX.:0.375-32 X 0.438 INCH BRS | 73743 | OBD |
| -27 | 337-2945-00 |  | 1 | . Shield, elec: POTENTIOMETER | 80009 | 337-2945-00 |
| -28 | 361-1047-00 |  | 1 | . SPACER, Var res: $0.3 \times 0.615 \times 0.55$ | 80009 | 361-1047-00 |
| -29 | 214-0498-00 | в010100 8019549 | 2 | . HEAT SINK,XSTR:T0-18,AL BLACK ANODIZED | 05820 | 201-AB |
|  | 214-3414-00 | B019550 | 2 | . HEAT SINK,XSTR:T0-92/T0-18,ALUMINUM | 13103 | 2224B |
| -30 | 337-2922-00 |  | 1 | . Shield, elec: horizontal amplifier | 80009 | 337-2922-00 |
| -31 | 200-2735-00 |  | 1 | . COVER, POWER SW: | 80009 | 200-2735-00 |
| -32 |  |  | 1 | . SWITCH, PUSH: (SEE Al0S901 REPL) |  |  |
| -33 | 343-0088-00 |  | 1 | . CLAMP, LOOP:0.062 INCH DIA | 80009 | 343-0088-00 |
| -34 | 131-1048-00 |  | 4 | . TERM.QIK DISC:CKT BD MT, $0.11 \times 0.02$ | 00779 | 61134-1 |
| -35 | ----- ----- |  | 1 | . SEMICOND DEVICE:(SEE A10u990 REPL) |  |  |
| -36 | ---------- |  | 1 | - THYRISTOR: (SEE AL0Q925 REPL) |  |  |
| -37 | 211-0304-00 |  | 1 | . SCREW, MACHINE:4-40 X 0.312, PNH | 01536 | OBD |
| -38 | 210-0406-00 |  | 1 | NUT, PLAIN, HEX. :4-40 X 0.188 INCH, BRS <br> - - * - - - | 73743 | 12161-50 |
| -39 | --..-- ----- |  | $9$ | . TERM, TEST POINT: (SEE AlOTP500,TP501,TP915, TP920, TP934, TP940, TP951, TP952 REPL) |  |  |
| -40 | 343-0969-00 |  | 1 | RETAINER, XSTR : POLYPHENYLENESULFIDE (attaching parts) | 80009 | 343-0969-00 |
| -41 | 211-0691-00 |  | 1 | . SCREW, MACHINE: 6-32 $\times 0.625$, PNH | 01536 | OBD |
| -42 | 210-0457-00 |  | 1 | NUT, PL, ASSEM WA: 6-32 $\times 0.312, S T L$ CD PL | 83385 | OBD |


| Fig. \& Index No. | Tektronix Part No. | Serial/Mo Eff | del No. Dscont | Qty | 12345 Name \& Description | Mfr Code | Mfr Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 2-43 | ----- --- |  |  | 2 | . TRANSISTOR: (SEE Al0Q940,Q942 REPL) <br> (attaching parts) |  |  |
| -44 | 342-0555-00 |  |  | 1 | . insulator, plate: heat sink,al | 80009 | 342-0555-00 |
| -45 | - |  |  | 1 | . Microcircuit, : (SEE AlOu985 REPL) (attaching parts) |  |  |
| -46 | 211-0304-00 |  |  | 1 | . SCREW, MACHINE:4-40 $\times 0.312$, PNH | 01536 | OBD |
| -47 | 210-0586-00 |  |  | 1 | . NUT,PL,ASSEM WA:4-40 X 0.25,STL CD PL | 83385 | OBD |
| -48 | 407-2729-00 |  |  | 1 | - BRACKET, heat SK:AL <br> (attaching parts) | 80009 | 407-2729-00 |
| -49 | 211-0303-00 | B010100 | B016349x | 2 | . SCREW, MACHINE:4-40 x 0.25, FLH 100 DEG | 01536 | OBD |
| -50 | 214-0973-00 | 8010100 | B016349x | 1 | . heat Sink, elec:0. $28 \times 0.18$ OVAL x 0.187 H | 80009 | 214-0973-00 |
|  | 195-4181-00 |  |  | 1 | . LEAD, ELECTRICAL: 26 AWC, $5.0 \mathrm{~L}, 8$-01 | 80009 | 195-4181-00 |
|  | 195-4182-00 |  |  | 1 | . Lead, electrical: 26 AWG, $5.5 \mathrm{~L}, 8-02$ | 80009 | 195-4182-00 |
|  | 195-4183-00 |  |  | 1 | . Lead, electrical: 26 AWG,5.5 L, 8-03 | 80009 | 195-4183-00 |
|  | 195-4184-00 |  |  | 1 | . LEAD, ELECTRICAL:26 AWG, $7.0 \mathrm{~L}, 8-04$ | 80009 | 195-4184-00 |
| -51 | 214-1061-05 |  |  | 1 | SPRING, GROUND: PLATED | 80009 | 214-1061-05 |
| -52 | 200-2519-00 |  |  | 1 | CAP., CRT SOCKET:NATURAL LEXAN | 80009 | 200-2519-00 |
| -53 | ----- ---- |  |  | 1 | DELAY LINE, ELEC: (SEE DL350 REPL) |  |  |
| -54 | 213-0882-00 |  |  | 2 | SCREW,TAPPING: 6-32 X 0.437 TYPE C | 01536 | OBD |
| -55 | 426-1766-00 |  |  | 1 | MOUNT, RESILIENT:CRT, REAR | 80009 | 426-1766-00 |
| -56 | 136-0202-04 |  |  | 1 | SKT, PL-IN ELEK: ELECTRON TUBE, 14 CONT | 80009 | 136-0202-04 |
| -57 | 334-1379-00 |  |  | 1 | LABEL: CRT, ADHESIVE BACK | 80009 | 334-1379-00 |
| -58 | 334-1951-00 |  |  | 1 | MARKER, IDENT: CRT WARNING | 80009 | 334-1951-00 |
| -59 | 337-2774-00 |  |  | 1 | Shield, elec: CRT, STEEL | 80009 | 337-2774-00 |
| -60 | 386-4443-00 |  |  | 1 | SUPPORT, SHIELD: CRT, FRONT, PLASTIC | 80009 | 386-4443-00 |
| -61 |  |  |  | 1 | electron tube: (SEe v870 Repl) |  |  |
| -62 | 134-0158-00 |  |  | 2 | BUTTON, PLUG:0.187 DIA, NYLON | 02768 | 207-080501-00 |
| -63 | 334-3379-02 |  |  | 1 | MARKER, IDENT:MARKED GROUNDSYMBOL | 80009 | 334-3379-02 |
| -64 |  |  |  | 1 | CONN, RCPT, ELEC: (SEE J8001 REPL) |  |  |
| -65 | 200-2264-00 |  |  | 1 | CAP., FUSEHOLDER:3AG FUSES | S3629 | FEK 0311666 |
| -66 | 200-1388-03 |  |  | 1 | COVER, FUSE, LEAD: POLYURETHANE | 80009 | 200-1388-03 |
| -67 | 204-0833-00 |  |  | 1 | BODY, FUSEHOLDER: 3AG \& 5 X 20MM FUSES | S3629 | 031.1653(MDLFEU) |
| -68 | 210-1039-00 |  |  | , | WASHER, LOCK: INT,0.521 ID X 0.625 INCH OD | 24931 |  |
| -69 | 210-0202-00 |  |  | 1 | TERMINAL, LUG: 0.146 ID, LOCKING, BRZ TINNED <br> (ATTACHING PARTS) | 78189 | 2104-06-00-25200 |
| -70 | 210-0457-00 |  |  | 1 | NUT, PL, ASSEM WA: $6-32 \times 0.312$, STL CD PL | 83385 | OBD |
| -71 | 348-0738-00 |  |  | 1 | Grommet, PLASTIC:0.312 Id, NYLON, BLACK | 28520 | SB-437-5 |
| -72 | 337-2947-00 |  |  | 1 | SHIELD, ELEC: INDUCTOR <br> (ATtACHING pARTS) | 80009 | 337-2947-00 |
| -73 | 211-0303-00 |  |  | 2 | SCREW, MACHINE:4-40 X 0.25 , FLH 100 DEG | 01536 | OBD |
| -74 | - |  |  | 1 | COIL, RF: (SEE L925 REPL) |  |  |
| -75 | 161-0033-26 | B010100 | B014649 | 1 | Cable ASSY, PWR:3,18 AWG,125V,101.3 L | 16428 | KH-9230 |
|  | 161-0104-00 | B014650 |  | 1 | CABLE ASSY, PWR, 3 WIRE, $98.0^{\prime \prime}$ LONG | 16428 | KH8352 |
|  | 131-1084-03 | Xb014650 |  | 1 | CONN, RCPT, ELEC: PWR, MALE , 250vac, 6a | 82389 | EAC-301 |
|  | 210-0586-00 | Xb014650 |  | 2 | NUT, PL, ASSEM WA: $4-40 \times 0.25$, STL CD PL | 83385 | OBD |
|  | 210-0803-00 | xb014650 |  | 1 | WASHER, FLAT: 0.15 ID X 0.032 THK, STL CD PL | 12327 | OBD |
|  | 211-0323-00 | Xb014650 |  | 2 | SCREW, MACHINE:4-40 X 0.312, FLH, 100 deg | 83385 | OBD |
|  | 213-0882-00 | Xb014650 |  | 1 | SCREW, TAPPING:6-32 X 0.437 TYPE C | 01536 | OBD |
|  | 343-0002-00 | xb014650 |  | 1 | CLAMP, LOOP:0.188 INCH DIA | 95987 | 3-16-6B |
|  | 195-0389-00 | Xb014650 |  | 1 | LEAD, ELECTRICAL:18 AWG,4.0 L, 5-4 | 80009 | 195-0389-00 |
|  | 195-5498-00 | Xb014650 |  | 1 | LEAD, ELECTRICAL: 18 AWG, $2.5 \mathrm{~L}, 8$-9 | 80009 | 195-5498-00 |
|  | 195-5499-00 | Xb014650 |  | 1 | LEAD, ELECTRICAL: 18 AWG, $3.5 \mathrm{~L}, 8-0$ | 80009 | 195-5499-00 |
| -76 | 358-0161-00 | B010100 | B011399 | 1 | bSHG, STRAIN RLF:FOR 0.50 INCH HOLE, PLASTIC | 28520 | 1147 SR-5P-4 |
|  | 358-0161-01 | B011400 |  | 1 | BSHG, STRAIN RLF:FOR 0.29 Inchdia cable | 28520 | 1154 SR-5L-1 |
|  | 348-0746-00 | Xb011400 |  | 1 | CUSHION, IDCTR: MOLDED POLYURETHANE | 80009 | 348-0746-00 |
|  | 211-0303-00 | xb011400 |  | 1 | SCREW, MACHINE:4-40 X 0.25, FLH 100 deg | 01536 | OBD |
|  | 210-0586-00 | xB011400 |  | 1 | NUT, PL, ASSEM WA:4-40 X 0.25 , STL CD PL | 83385 | OBD |
| -77 | 200-2531-00 |  |  | 1 | COVER, ${ }^{\text {POWER }}$ : PLASTIC | 80009 | 200-2531-00 |

Fig. \&


Fig. \&

| Index | Tektronix | Serial/Model No. |  |  |  |  | Mfr |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| No. | Part No. | Eff | Dscont | Qty 12345 | Name \& Description | Code | Mfr Part Number |

WIRE ASSEMBLIES

| 175-3092-00 | B010100 | B010417 | 1 | CA ASSY, SP, ELEC: 4, 26 AWG, 3.0 L, RIBBON | 80009 | 175-3092-00 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 175-4662-00 | B010418 |  | 1 | CA ASSY, SP, ELEC: 4, 22 AWG, 3.5 L, RIBBON (FROM Al0 TO Al2P1100) | 80009 | 175-4662-00 |
| 352-0162-02 |  |  | 2 | - CONN BODY, PL, EL: 4 WIRE RED | 80009 | 352-0162-02 |
| 175-3616-00 | B010100 | B010417 | 1 | CA ASSY, SP, ELEC: 4,26 AWG,5.0 L,RIBBON | 80009 | 175-3616-00 |
| 175-4663-00 | B010418 |  | 1 | CA ASSY, SP, ELEC: 4,22 AWG,5.5 L, RIBBON (FROM A10 TO A12P2100) | 80009 | 175-4663-00 |
| 352-0162-00 |  |  | 2 | . HLDR, TERM CONN: 4 WIRE BLACK | 80009 | 352-0162-00 |
| 175-3617-00 | B010100 | B010417 | 1 | CA ASSY, SP, ELEC: 7,26 AWG, $5.5 \mathrm{~L}, \mathrm{RIBBON}$ | 80009 | 175-3617-00 |
| 175-4664-00 | B010418 |  | 1 | CA ASSY, SP, ELEC:7,22 AWG,4.0 L,RIBBON (FROM A10P7001 TO Al2P7000) | 80009 | 175-4664-00 |
| 352-0165-00 |  |  | 1 | . CONN BODY, PL, EL: 7 WIRE BLACK | 80009 | 352-0165-00 |
| 175-3869-00 | B010100 | B010417 | 1 | CA ASSY, SP, ELEC: 10, 26 AWG, 8.0L, RIBBON | 80009 | 175-3869-00 |
| 175-4665-00 | B010418 |  | 1 | CA ASSY,SP,ELEC: 10,22 AWG, $7.0 \mathrm{~L}, \mathrm{BIBBON}$ (FROM A10P6001 TO Al2P6000) | 80009 | 175-4665-00 |
| 352-0168-00 |  |  | 2 | . CONN BODY, PL, EL: 10 WIRE BLACK | 80009 | 352-0168-00 |
| 175-4466-00 |  |  | 1 | CABLE ASSY,RF:50 0HM COAX,4.0 L,9-1 | 80009 | 175-4466-00 |
| 352-0169-00 |  |  | 1 | . HLDR, TERM CONN: 2 WIRE BLACK | 80009 | 352-0169-00 |
| 175-3615-00 |  |  | 1 | CA ASSY, SP, ELEC: 3, 26 AWG, 9.0 L, RIBBON (FROM A10P7005 TO DL350) | 80009 | 175-3615-00 |
| 352-0161-00 |  |  | 1 | . HLDR, TERM CONN: 3 WIRE, BLACK | 80009 | 352-0161-00 |
| 175-4232-00 |  |  | 1 | CA ASSY, SP, ELEC: 2,26 AWG,4.0 L,RIBBON | 80009 | 175-4232-00 |



Fig. \&

| $\begin{aligned} & \text { Index } \\ & \text { No. } \end{aligned}$ | Tektronix Part No. | Serial/Model No. Eff Dscont | Qty | 12345 | Name \& Description | Mir Code | Mir Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| accessories |  |  |  |  |  |  |  |
| -1 | 010-6120-01 |  | 2 | probe, voltage: P6120,1.5m l, 10 X W/access |  | 80009 | 010-6120-01 |
|  | -013-0191-00 |  | 2 | TIP PRobe: |  | 80009 | 013-0191-00 |
|  |  |  | 1 | manual, tec | ORS | 80009 | 070-3398-00 |
|  | 070-3398-00$070-3826-00$ |  | 1 | manual, tec |  | 80009 | 070-3826-00 |
|  | 159-0021-00 | в010100 в021999 | 1 | fuse, CARTR | , 2A, 250V, ,AST-BLOW | 71400 | AGC 2 |
|  | 159-0019 | в022000 | 1 | fuse, CARTR | , 1A, 250v, sLow blow | 71400 | MDLI |

OPTIONAL ACCESSORIES

|  | 020-0672-00 | 1 | ACCESSORY KIT: | 80009 | 020-0672-00 |
| :---: | :---: | :---: | :---: | :---: | :---: |
| -2 | 200-2520-00 | 1 | . COVER,SCOPE:FRONT, ABS | 80009 | 200-2520-00 |
| -3 | 016-0677-00 | 1 | - POUCH, ACCESSORY: | 80009 | 016-0677-00 |
|  | 386-4674-00 | 1 | - . PLATE, MOUNTING: ACCESSORY POUCH | 80009 | 386-4674-00 |
|  | 386-2370-00 | 2 | . . PLATE, REINF:ACCESSORY POUCH | 80009 | 386-2370-00 |
|  | 212-0068-00 | 4 | . . SCREW, MACHINE:8-32 X 0.312 INCH, TRH STL | 77250 | OBD |
|  | 220-0736-00 | 4 | . . NUT, PL, EXT WSHR: 8-32 X 0.344 HEX, NYLON | 23050 | OBD |

## MANUAL CHANGE INFORMATION

At Tektronix, we continually strive to keep up with latest electronic developments by adding circuit and component improvements to our instruments as soon as they are developed and tested.

Sometimes, due to printing and shipping requirements, we can't get these changes immediately into printed manuals. Hence, your manual may contain new change information on following pages.

A single change may affect several sections. Since the change information sheets are carried in the manual until all changes are permanently entered, some duplication may occur. If no such change pages appear following this page, your manual is correct as printed.

MANUAL CHANGE INFORMATION
$\qquad$
Date: 5-17-82
Change Reference: $\mathrm{Cll/182}$ (REV)
Product:
2215 OSCILLOSCOPE SERVICE Manual Part No.: 070-3826-00

## DESCRIPTION

## INTRODUCTION

Your instrument contains in the power supply either the Current Limit board (A19) or the Preregulator board (A18). Instruments with a SN BO22000 or above contain the Preregulator board. Some instruments below that serial number were built containing the Preregulator board. To determine if yours is one of these, look to see if there is an Option 48 sticker attached to the rear of the instrument. If there is not an Option 48 sticker attached and the serial number is below B022000, your instrument contains the Current Limit board.

All instruments manufactured in Europe contain the Preregulator board (A18). These instruments have serial numbers 200000 and up.

This material provides the additional information required to support those instruments which have been produced with the Preregulator circuit board. When servicing your instrument, use the appropriate text, schematic diagram, board charts, and dollies applicable to your particular instrument.

## TEXT CORRECTIONS

Page 3-25 First paragraph following the POWER SUPPLY heading REPLACE WITH:

The Power Supply circuitry converts the ac-source voltage into the various voltages needed for instrument operation. It consists of the Power Input, Preregulator, and Inverter circuits (which drive the primary of the power transformer) and other Secondary circuits (which produce the necessary supply voltages for the instrument).

This instrument has either the Current Limit board (A19) or the Preregulator board (A18) installed as part of the power supply. Refer to the appropriate circuit description for your particular instrument configuration.

Page 3-25 Power Input heading
REPLACE WITH:
Current Limit Board Configuration
DELETE: Preregulator heading

## DESCRIPTION

Page 3-26 Immediately preceding the Inverter heading
ADD:
Preregulator Board Configuration
The Power Input circuit converts the input ac-source voltage to filtered dc for use by the Preregulator.

The POWER switch (S90l) connects the ac-supply source through fuse $F 901$ to bridge rectifier CR904. The bridge full-wave rectifies the source voltage, and its output is filtered by c909. Input surge current at the time of instrument powerup is limited by thermistors RT901 and RT902. Initially their resistances are high, but as they warm up, their resistances decrease and they dissipate less power. The instrument is protected from large voltage transients by suppressor VR9øl. Conducted EMI is attenuated by line filter FL9ø0l, common-mode transformer T90l, differential-mode transformer T907, and capacitors C901, C903, C904, and C905. Capacitors C907, C908, and C9l0 form a high-frequency bypass network to prevent the diodes in CR904 from generating EMI.

The Preregulator provides a regulated dc-output voltage for use by the Inverter circuitry.

When the instrument is turned on, voltage developed across C909 will charge C913 through R9ll. When the voltage has risen to a level high enough that U920 can reliably drive Q933, U920 will receive its Vcc voltage through Q915. This level is set by zener diode VR917 in the emitter circuit of Q917 and by the voltage divider consisting of R 912 and R913. The zener diode will keep Q917 off until the voltage at its base reaches approximately 6.9 V . Then $Q 917$ will be biased into its active region and the resulting collector current will cause a voltage drop across R9l6. This voltage drop will bias on Q915, and the positive feedback through R9l4 will reinforce the turn on of Q917. Thus Q915 and Q917 will drive each other into saturation very quickly. Once Q915 is on, U920 will begin to function.

Pulse-width modulator IC U920 controls the output voltage of the Preregulator by regulating the duty cycle of the pulse applied to the gate of Q933. It utilizes an oscillator whose frequency is determined by R 920 and C 920 (approximately 40 kHz ) and whose output at pin 5 is a sawtooth voltage. An internal comparator compares this sawtooth voltage with the output voltage produced by the two error amplifiers. Whenever the sawtooth voltage is greater than the error-amplifier output voltage, Q933 is biased on to supply current to both C934 and the rest of the circuitry. The two error amplifiers are used to maintain a

## DESCRIPTION

constant output voltage and to monitor the output current of the Preregulator. One input of each amplifier is connected through a divider network to the internal $+5-\mathrm{V}$ reference. The output voltage of the Preregulator is monitored by the voltage divider at pin 2. The voltage drop across R933, produced by the Preregulator output current, is applied to the current-limit amplifier via R929.

When the instrument is first turned on, the current limit amplifier controls the conduction time of Q933. While Q933 is conducting, the output current increases until a sufficiently large voltage drop is developed across R933 to invoke the current limit mode. The current-limit amplifier holds the output current to the current-limit threshold at approximately 1 ampere. When the voltage across C934 reaches approximately 43 V , the voltage amplifier starts controlling the duty cycle of $Q 933$ and the Preregulator will not limit current unless there is excessive current demand.

With Q933 off, C933 charges to the output voltage of the Power Input circuit. When Q933 turns on, current through the FET will come from the winding connected to pins 1 and 2 of T933 and from C933. Current to C933 is supplied by the winding connected to pins 4 and 5 of T933. When U 920 shuts off Q933, the collapsing magnetic field will raise the voltage at the anode of CR933. This diode then becomes forward biased and passes current supplied by the winding connected to pins 4 and 5 of T933 and from C933. For this part of the cycle, current to C933 will be supplied by the winding connected to pins 1 and 2 of T933. This process will continue for each period of the oscillator, and the duty cycle will be altered as necessary to maintain 43 V across C934. To shut off Q933 during each oscillator period, Q931 is used to discharge the gate-drain capacitance. Pin lø of U920 goes LO, reverse biasing CR93l and turning on Q93l to effectively short together the gate and source, thus shutting off the FET.

Once the supply is running, power to $492 \emptyset$ will be supplied from the winding connected to pins 6 and 7 of T933. Diode CR9l3 half-wave rectifies the voltage across pins 6 and 7 to keep filter capacitor C913 charged and to maintain Vcc voltage to U920 through Q915.

Instrument protection from excessive output voltage is supplied by silicon-controlled rectifier Q935. Should the Preregulator output voltage exceed 51 V , zener diode VR935 will conduct, causing Q935 to also conduct. The Preregulator output current will then be shunted through 9935 , and the output voltage will very quickly go to zero. With the Vcc voltage of $U 920$ not longer being supplied by the winding connected to pins 6 and 7 of T933, the Preregulator will shut down and Q935 will be reset.

## DESCRIPTION

The supply will then attempt to power up, but will again shut down once the overvoltage condition is reached. This sequence continues until the overvoltage condition is corrected.

Page 3-26 First sentence of the forth paragraph in the Inverter part

## REPLACE WITH:

In instruments having the Current Limit board, diodes CR940 and CR942 serve as a negative-peak detector to generate a voltage controlling the outputs of both the Preregulator and the error amplifier. In instruments having the Preregulator board, diodes CR940 and CR942 serve as a negative-peak detector to generate a voltage for controlling the output of the error amplifier.

Page 3-26 Immediately preceding the last paragraph of the the inverter part

ADD:
NOTE
The following paragraph applies only to instruments having the Current Limit board.

Page 5-4 WARNING following part a
REPLACE WITH:

## WARNING

To avoid electric shock and instrument damage when checking either the Head Room Voltage or the Preregulator Output Voltage, use a digital voltmeter that is isolated from ground, since the Inverter power-supply circuitry common is at line potential.

## DESCRIPTION

Page 5-4 Parts $b, c$, and $d$
REPLACE WITH:
b. Connect the digital voltmeter low lead to common (TP934). If the instrument has the Current Limit board (A19), connect the volts lead to TP952. If the instrument has the Preregulator board (Al8), connect the volts lead to the + side of c937.
c. CHECK-Reading is +4.2 V to +4.4 V for instruments having the Current Limit board. Reading is 41 V to 43 V for instruments having the Preregulator board. If the reading is within these limits, skip to part e.

NOTE
The following adjustment is only applicable to instruments having the Current Limit board (A19).
+4.3 V . ADJUST-Head Room Voltage Adjust (R952) for
Page 6-13 Cathode-Ray Tube procedure, after step 3
ADD:
3a. For instruments with the Preregulator board (A18), remove two nuts securing the shield at the back of the crt and remove the shield.

Page 6-13 Cathode Ray Tube procedure, after step 6
ADD:
6a. If applicable, reinstall the shield at the back of the crt; then secure it with two nuts (removed in step 3a).

Page 6-14 High-Voltage Shield procedure, after step 5
ADD :
5a. For instruments equipped with the Preregulator board (A18), remove the screw from the front of the High-Voltage Shield at the upper-left hand corner.

Page 6-14 High-Voltage Shield procedure, after step 7
ADD :
7a. If applicable, reinstall the screw to the front of the High-Voltage Shield (removed in step 5a).

Page 6-18 Immediately preceding the Current Limit Circuit Board procedure

ADD :
NOTE
The instrument has either the Current Limit board (Al9) or the Preregulator board (A18) installed. Use the appropriate board-removal procedure for your particular instrument.

Page 6-18 After the Current Limit Circuit Board procedure ADD:

## Preregulator Circuit Board

To remove the Preregulator circuit board, perform the following steps:

1. Remove the High-Voltage shield (see the "High-Voltage Shield" removal procedure).
2. Remove two screws securing the Preregulator board mounting brackets (one at the rear-top of the frame and one on the right side near the back corner of the frame).
3. Remove the securing screw through the access hole of the clear plastic shield from the top of the Preregulator board at the front-right corner.
4. Disconnect four wire connectors from the Preregulator board and note their positions for reinstallation reference.

Product: 2215 OSCILLOSCOPE
DESCRIPTION

To reinstall the Preregulator board, perform the following steps:

## NOTE

Check for sufficient silicon grease and proper positioning of the insulator in the plastic holder housing Q933.
5. Reconnect two wire connectors (P803 and P804) to the front edge of the Preregulator board at the positions noted in step 4. Then position the board into the instrument frame.
6. Reconnect two wire connectors (P801 and P802) to the left edge of the Preregulator board at the positions noted in step 4.
7. Reinstall the securing screw at the top of the Preregulator board (removed in step 3).
8. Reinstall the two bracket screws (removed in step 2).
9. Reinstall the High-Voltage shield (see the "High-Voltage Shield" reinstallation procedure).
$\qquad$
$\qquad$

# ELECTRICAL PARTS LIST CHANGES <br> (When Option 48 is added) 

REMOVE:

A19
ADD:
A18

|  |  |
| :--- | :--- |
| A18C903 | $285-1192-00$ |
| A18C904 | $285-1192-00$ |
| A18C907 | $285-1192-00$ |
| A18C908 | $285-1192-00$ |
| A18C909 | $290-0988-00$ |
| A18C910 | $283-0335-00$ |
| A18C913 | $290-0770-00$ |
| A18C920 | $281-0852-00$ |
| A18C921 | $281-0775-00$ |
| A18C923 | $281-0772-00$ |
| A18C925 | $281-0820-00$ |
| A18C929 | $281-0809-00$ |
| A18C933 | $285-0932-00$ |
| A18C934 | $290-0831-00$ |
| A18C935 | $283-0208-00$ |
|  |  |
| A18CR904 | $152-0750-00$ |
| A18CR913 | $152-0061-00$ |
| A18CR931 | $152-0061-00$ |
| A18CR933 | $152-0661-00$ |

A18E933 276-0640-00
A18L937 108-0422-00
A18L938 108-0422-00

| A18P801 | $131-1048-00$ |
| :--- | :--- |
| A18P802 | $131-1048-00$ |
| A18P803 | $131-1048-00$ |
| A18P804 | $131-1048-00$ |
|  |  |
| A18Q915 | $151-0164-00$ |
| A18Q917 | $151-0432-00$ |
| A18Q931 | $151-0164-00$ |
| A18Q933 | $151-1152-00$ |
| A18Q935 | $151-0506-00$ |

CKT BOARD ASSY:CURRENT LIMIT

CKT BOARD ASSY:PREREGULATOR
CAP.,FXD,PPR DI:0.0022UF,20\%,250V
CAP.,FXD,PPR DI:0.0022UF,20\%,250V
CAP.,FXD,PPR DI:0.0022UF,20\%,250V
CAP.,FXD,PPR DI:0.0022UF, $20 \%, 250 \mathrm{~V}$
CAP.,FXD,ELCTLT:75UF, $+50-10 \%, 450 \mathrm{~V}$
CAP.,FXD,CER DI:0.1UF,20\%,600V
CAP.,FXD,ELCTLT:100UF,+50-10\%,25V
CAP.,FXD,CER DI: $1800 \mathrm{PF}, 10 \%, 100 \mathrm{~V}$
CAP.,FXD,CER DI:0.1UF,20\%,50V
CAP.,FXD,CER DI:4700PF, $10 \%, 100 \mathrm{~V}$
CAP.,FXD,CER DI:680PF,10\%,50V
CAP.,FXD,CER DI:200PF,5\%,100V
CAP.,FXD,PLASTIC:1UF, $10 \%, 400 \mathrm{~V}$
CAP.,FXD,ELCTLT:470UF, $+50-10 \%, 50 \mathrm{~V}$
CAP.,FXD,CER DI:0.22UF, $10 \%, 200 \mathrm{~V}$
SEMICOND DVC,DI:RECT BRIDGE,600V,3A,FAST REC SEMICOND DVC,DI:SW,SILICON,175V,0.1A
SEMICOND DVC,DI:SW,SILICON,175V,0.1A
SEMICOND DVC,DI:RECT,SILICON,600V,3A,FAST REC
CORE,EM:0.187 X 0.188
COIL,RF:FIXED,82 UH
COIL,RF:FIXED,82 UH
TERM,QIK DISC:0.11 $\times 0.02$ BL TERM,QIK DISC:0.11 $\times 0.02 \mathrm{BL}$ TERM,QIK DISC: $0.11 \times 0.02 \mathrm{BL}$ TERM, QIK DISC: $0.11 \times 0.02 \mathrm{BL}$

TRANSISTOR:SILICON,PNP
TRANSISTOR:SILICON,NPN
TRANSISTOR:SILICON,PNP
TRANSISTOR:SILICON,MOSFE,N-CHANNEL
SCR:SILICON

## DESCRIPTION

REMOVE:

| A18R903 | $315-0512-00$ |
| :--- | ---: |
| A18R904 | $315-0512-00$ |
| A18R907 | $315-0561-00$ |
| A18R908 | $315-0561-00$ |
| A18R911 | $303-0154-00$ |
| A18R912 | $315-0104-00$ |
| A18R913 | $315-0104-00$ |
| A18R914 | $315-0104-00$ |
| A18R916 | $315-0302-00$ |
| A18R917 | $315-0512-00$ |
| A18R920 | $315-0203-00$ |
| A18R921 | $321-0289-00$ |
| A18R922 | $321-0379-00$ |
| A18R923 | $315-0154-00$ |
| A18R925 | $315-0682-00$ |
| A18R927 | $315-0103-00$ |
| A18R928 | $315-0391-00$ |
| A18R929 | $315-0103-00$ |
| A18R931 | $315-0302-00$ |
| A18R933 | $308-0843-00$ |
| A18R934 | $308-0441-00$ |
| A18R935 | $315-0121-00$ |
| A18R936 | $315-0470-00$ |
| A18R937 | $301-0822-00$ |
| A18RT901 | $307-0350-00$ |
| A18RT902 | $307-0350-00$ |
|  |  |
| A18T901 | $120-1449-00$ |
| A18T907 | $120-1441-00$ |
| A18T933 | $120-1439-00$ |
| A18U920 | $156-1627-00$ |
| A18VR917 | $152-0166-00$ |
| A18VR935 | $152-0255-00$ |
|  |  |

RES.,FXD,CMPSN:5.1K OHM,5\%,0.25W
RES.,FXD,CMPSN:5.1K OHM,5\%,0.25W
RES.,FXD,CMPSN:560 OHM,5\%,0.25W
RES.,FXD,CMPSN:560 OHM,5\%,0.25W
RES.,FXD,CMPSN:150K OHM,5\%,1W
RES.,FXD,CMPSN: 100 K OHM, $5 \%, 0.25 \mathrm{~W}$
RES.,FXD,CMPSN: 100 K OHM, $5 \%, 0.25 \mathrm{~W}$
RES.,FXD,CMPSN: 100 K OHM $, 5 \%, 0.25 \mathrm{~W}$
RES.,FXD,CMPSN:3K OHM, $5 \%, 0.25 \mathrm{~W}$
RES.,FXD,CMPSN:5.1K OHM,5\%,0.25W
RES.,FXD,CMPSN:20K OHM,5\%,0.25W
RES.,FXD,FILM: 10.0 K OHM $, 1 \%, 0.125 \mathrm{~W}$
RES.,FXD,FILM: 86.6 K OHM $, 1 \%, 0.125 \mathrm{~W}$
RES.,FXD,CMPSN:150K OHM, $5 \%, 0.25 \mathrm{~W}$
RES.,FXD,CMPSN:6.8K OHM,5\%,0.25W
RES.,FXD,CMPSN:10K OHM,5\%,0.25W
RES.,FXD,CMPSN:390 OHM,5\%,0.25W
RES.,FXD,CMPSN: 10 K OHM, $5 \%, 0.25 \mathrm{~W}$
RES.,FXD,CMPSN:3K OHM,5\%,0.25W
RES.,FXD,WW:0.2 OHM,5\%,1W
RES.,FXD,WW:3 OHM,5\%,3W
RES.,FXD,CMPSN: 120 OHM, $5 \%, 0.25 \mathrm{~W}$
RES.,FXD,CMPSN: 47 OHM, $5 \%, 0.25 \mathrm{~W}$ RES.,FXD,CPSN:8.2K OHM,5\%,0.5W RES.,THERMAL:7.5 OHM, $10 \%, 3.9 \% / D E G$ C RES., THERMAL: $7.5 \mathrm{OHM}, 10 \%, 3.9 \% /$ DEG C

TRANSFORMER:
TRANSFORMER:RF:POT CORE TRANSFORMER:RF:

MICROCKT,LINEAR:MOD CONTR CKT
SEMICOND DVC DI:ZENER,0.4W,6.2V,5\% SEMICOND DVC DI:ZENER,0.4W,51V,5\%

Remove from the A10 circuit board assembly.
REMOVE:

| A10C912 | $281-0770-00$ |
| :--- | :--- |
| A10C915 | $290-0188-00$ |
| A10C917 | $290-0808-00$ |
|  |  |
| A10CR903 | $152-0040-00$ |
| A10CR904 | $152-0040-00$ |
| A10CR905 | $152-0040-00$ |
| A10CR906 | $152-0040-00$ |
| A10CR917 | $152-0141-00$ |
| A10CR931 | $152-0782-00$ |
| A10CR933 | $152-0782-00$ |

CAP.,FXD,CER DI:0.001UF,20\%,100V
CAP.,FXD,ELCTLT:0.1UF,10\%,35V
CAP.,FXD,ELCTLT:2.7UF,10\%,20V
SEMICOND DEVICE:SILICON,600V,1A
SEMICOND DEVICE:SILICON, $600 \mathrm{~V}, 1 \mathrm{~A}$
SEMICOND DEVICE:SILICON, $600 \mathrm{~V}, 1 \mathrm{~A}$
SEMICOND DEVICE:SILICON,600V,1A
SEMICOND DEVICE:SILICON,30V,150MA
SEMICOND DEVICE:RECTIFIER,SILICON,600V
SEMICOND DEVICE:RECTIFIER,SILICON,600V

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REMOVE:

| A10P9025 | $131-1048-00$ |
| :--- | ---: |
|  |  |
| A10Q918 | $151-0432-00$ |
| A10Q921 | $151-0508-00$ |
| A10Q925 | $151-0538-00$ |
|  |  |
| A10R911 | $301-0184-00$ |
| A10R912 | $315-0104-00$ |
| A10R914 | $301-0184-00$ |
| A10R915 | $321-0230-00$ |
| A10R916 | $315-0223-00$ |
| A10R917 | $315-0154-00$ |
| A10R918 | $315-0753-00$ |
| A10R920 | $301-0105-00$ |
| A10R925 | $315-0510-00$ |
| A10R926 | $301-0471-00$ |
| A10R952 | $311-1562-00$ |
| A10R953 | $315-0361-00$ |
|  |  |
| A10T925 | $120-1384-00$ |
| A10TP915 | $214-0579-02$ |
| A10TP920 | $214-0579-02$ |
| A10TP921 | $214-0579-02$ |
|  |  |
| A10U931 | $156-0885-00$ |
|  |  |
| A10VR913 | $152-0304-00$ |
| A10VR914 | $152-0149-00$ |
| A10VR915 | $152-0149-00$ |
| A10VR938 | $152-0788-00$ |

ADD:
A10R953 315-0203-00
A10W952 176-0122-01

REMOVE:

| F901 | $159-0021-00$ |
| :--- | :--- |
| L925 | $108-1096-00$ |
|  |  |
| ADD: |  |
| F901 | $159-0019-00$ |

TERM,QIK DISC:CKT BD MT,0.11 X 0.02

TRANSISTOR:SILICON,NPN
TRANSISTOR:UJT,SI,2N6027,T0-98 TRANSISTOR:TRIAC,SI, $600 \mathrm{~V}, 8.0 \mathrm{~A}, \mathrm{TO}-220$

RES.,FXD,CMPSN:180K OHM,5\%,0.50W
RES.,FXD,CMPSN:100K OHM,5\%,0.25W
RES.,FXD,CMPSN:180K OHM,5\%,0.50W
RES.,FXD,FILM:2.43K OHM,1\%,0.125W
RES.,FXD,CMPSN:22K OHM,5\%,0.25W
RES.,FXD,CMPSN:150K OHM,5\%,0.25W
RES.,FXD,CMPSN:75K OHM,5\%,0.25W
RES.,FXD,CMPSN: 1 M OHM, $5 \%, 0.50 \mathrm{~W}$
RES.,FXD,CMPSN:51 OHM,5\%,0.25W
RES.,FXD,CMPSN: 470 OHM, $5 \%, 0.50 \mathrm{~W}$
RES.,VAR,NONWIR:2K OHM,20\%,0.50W
RES.,FXD,CMPSN: 360 OHM, $5 \%, 0.25 \mathrm{~W}$
TRANSFORMER,RF:TOROID,2 WINDS TERM,TEST POINT:BRS CD PL TERM,TEST POINT:BRS CD PL TERM,TEST POINT:BRS CD PL

MICROCIRCUIT,LI:OPTOELECTRONIC ISOLATOR
SEMICOND DEVICE:ZENER, $0.4 \mathrm{~W}, 20 \mathrm{~V}, 5 \%$ SEMICOND DEVICE:ZENER, $0.4 \mathrm{~W}, 10 \mathrm{~V}, 5 \%$ SEMICOND DEVICE:ZENER,0.4W,10V,5\% SEMICOND DEVICE:TRANSIENT SUPPRESSOR

RES.,FXD,CMPSN:20K OHM,5\%,0.25W WIRE:22 AWG BARE, 12.0 V

## CHASSIS PARTS

FUSE,CARTRIDGE:3AG,2A,250V,FAST-BLOW COIL,RF:FIXED,16MH,25\%

FUSE,CARTRIDGE:3AG,1A,250V,FAST-BLOW

| cincticter | Sctum |  | sermen | cincur | Sctem | cincur | $\xrightarrow{\text { Scemem }}$ |  | Stern |  | Stem |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| ${ }^{\text {R2921 }}$ |  | Rsob |  | ${ }^{\text {R73s }}$ | 7 | Reso | $\bigcirc$ | wsob | 4 | weano. |  |
| ${ }_{\substack{\text { Reas } \\ \text { aras }}}$ | ${ }_{3}^{3}$ | $\underbrace{}_{\substack{\text { sen } \\ \text { Res }}}$ |  | ${ }_{\substack{\text { Ra3g } \\ \text { R28 }}}$ | \% | ${ }_{\text {a }}^{\text {ams }}$ |  | ${ }_{\text {wise }}^{\substack{\text { wis }}}$ | ${ }_{10}^{4}$ | Wex | $\stackrel{9}{10}$ |
| $\underset{\substack{\text { Rasat } \\ \text { Rag }}}{\text { and }}$ | ${ }_{3}^{3}$ | ${ }_{\substack{\text { sin } \\ \text { nsil }}}$ |  |  | 7 |  | \% | was | 10 |  |  |
|  | ${ }_{4}^{4}$ | $\underbrace{\substack{\text { a }}}_{\substack{\text { sin } \\ \text { sid }}}$ |  | ${ }_{\text {aras }}^{\text {R73 }}$ | 7 | ${ }_{\text {cosem }}^{\text {Reas }}$ |  | wis | s |  |  |
| ${ }_{\substack{\text { and } \\ \text { Rat12 }}}^{\text {and }}$ |  |  |  |  | 7 |  |  | $\underbrace{\text { weso }}_{\text {wese }}$ | s |  | ${ }^{10}$ |
| ${ }_{\text {and }}^{\text {Ratis }}$ | ${ }_{4}^{4}$ | sis |  | ${ }^{2939}$ | 3 |  | 3 |  | $\stackrel{3}{5}$ | Weam |  |
|  | 4 | ${ }^{1525}$ |  | neo | ${ }^{6}$ | ${ }_{\text {cose }}^{\text {atas }}$ |  | wess |  | weooliz | ${ }_{10}$ |
| ${ }_{\text {Rex }}$ | 4 |  |  | 203 | \% |  |  | weas | , | wamom | ${ }_{10}$ |
|  | $\stackrel{4}{4}$ | ${ }_{\text {cose }}^{\text {Reos }}$ | $\stackrel{5}{4}$ | ${ }_{\text {and }}^{\text {Raba }}$ |  | ${ }_{\text {prom }}^{\text {pes }}$ |  | wher | $\stackrel{7}{6}$ |  | 10 |
|  | ${ }_{4}^{4}$ | ${ }_{\text {Reos }}^{\text {Reos }}$ | ${ }_{5}^{10}$ | ${ }_{\text {a }}^{\text {Ren12 }}$ | ¢ | ${ }_{\text {Tresel }}^{\text {Trese }}$ | \% | $\underbrace{\text { wase }}_{\text {wase }}$ | $\stackrel{6}{7}$ |  | ${ }_{8}^{10}$ |
|  | 4 |  |  |  | ${ }_{6}^{6}$ |  | ${ }_{2}^{2}$ | wear | 7 | weane | ${ }^{10}$ |
| cias | $\stackrel{4}{4}$ |  | ${ }_{4}^{4}$ | ${ }_{\text {and }}^{\text {anal }}$ | 8 | U198 |  | vers | ${ }_{6}^{6}$ |  | 10 |
| ${ }^{233}$ | 4 | ${ }^{6015}$ |  | near |  | 4270 |  | weats | $\frac{8}{7}$ | \%tamen |  |
|  | 4 |  | ${ }_{4}^{4}$ |  | ${ }^{6}$ | \% | ${ }^{3}$ | weir | 7 | wheooz |  |
|  |  |  |  | ${ }^{\text {ar36 }}$ | $8$ | 4317 |  | we78 |  | 20,27 |  |
|  | 4 |  | 5 | ${ }_{\text {a }}^{\text {ama }}$ | \% | 580 |  | ${ }_{\text {wess }}^{\text {wise }}$ |  | ${ }^{\text {Weorn }}$ |  |
|  | 4 |  | s | ${ }_{\text {aras }}^{\text {Rasa }}$ | : | \%or |  | mises | \% |  |  |
|  | ${ }_{4}^{4}$ |  | $\stackrel{5}{5}$ | ${ }_{\substack{\text { a } \\ \text { Re34 } \\ \text { Re3 }}}$ | ${ }_{6}^{6}$ |  | s | \%eser | $\stackrel{1}{7}$ |  |  |
|  | $\stackrel{4}{4}$ |  | $\stackrel{5}{10}$ |  | ${ }_{6}^{6}$ |  | ${ }_{5}^{4}$ | wese | $\stackrel{3}{7}$ | ${ }_{\text {wamen }}^{\substack{\text { wame. }}}$ |  |
|  | \% | ${ }_{\substack{\text { Ress } \\ \text { Rese }}}$ | ¢ | ${ }^{\text {Ras3 }}$ | ¢ |  |  |  |  |  |  |
|  | $\stackrel{4}{4}$ | fati | 5 | ${ }_{\text {a }}^{\text {ReaO }}$ | ${ }^{6}$ | Ueas | ¢ |  | 9 |  |  |
|  |  |  | ${ }^{10}$ |  | \% |  | 4 | cosion | ${ }_{6}$ |  |  |
| , | 4 | ${ }^{\text {anaza }}$ |  | ) | ${ }_{6}$ | Mass | 10 | wimol |  |  |  |
| ciac | ${ }_{4}^{4}$ | $\substack{\text { anos } \\ \text { anos }}$ | 7 | ${ }_{\text {asas }}^{\text {asas }}$ | ¢ | vae9 | ${ }^{6}$ |  |  |  |  |
|  | ${ }_{4}^{4}$ | $\substack { \text { fanc } \\ \begin{subarray}{c}{\text { poos }{ \text { fanc } \\ \begin{subarray} { c } { \text { poos } } } \end{subarray}$ | 7 |  | ${ }^{6}$ | 5as |  |  | $\frac{3}{2}$ |  |  |
| asi |  |  |  | nasi |  | wiro | 2 | come |  |  |  |
|  | ${ }_{4}^{4}$ | $\substack { \text { ano } \\ \begin{subarray}{c}{\text { pon } \\ \text { R12 }{ \text { ano } \\ \begin{subarray} { c } { \text { pon } \\ \text { R12 } } } \end{subarray}$ | 7 |  | ! | cos | ${ }_{2}^{2}$ | cosion | 3 |  |  |
|  | ${ }_{4}^{4}$ |  | 7 |  | \% |  | ${ }_{2}^{2}$ | cos | 2 |  |  |
|  | $\stackrel{4}{4}$ | ${ }_{\substack{\text { arab } \\ \text { R2as }}}^{\text {a }}$ | 7 | ${ }_{\substack{\text { acas } \\ \text { acas }}}^{\text {aid }}$ | \% | whes | ${ }_{3}^{2}$ | Wex | ${ }_{3}^{3}$ |  |  |
|  | ${ }_{4}^{4}$ |  | 7 |  | $\stackrel{9}{9}$ |  | $3_{3}^{3}$ | Wex | 10 |  |  |
|  | ${ }_{4}^{4}$ |  | 7 |  | \% |  | ${ }^{\frac{3}{3}}$ | come | ${ }_{4}^{4}$ |  |  |
|  | ${ }_{4}^{4}$ | ${ }_{\substack { \text { aris } \\ \begin{subarray}{c}{\text { ars }{ \text { aris } \\ \begin{subarray} { c } { \text { ars } } }\end{subarray}}$ | 7 |  | \% |  | ${ }_{3}^{3}$ | cosmex | ${ }_{4}^{4}$ |  |  |
|  | $\stackrel{4}{4}$ |  | 7 | ${ }_{\substack{\text { arab } \\ 8887}}^{\text {and }}$ | \% |  | ${ }_{3}^{3}$ | cosm | ${ }_{10}^{4}$ |  |  |
| $\underbrace{\text { aid }}_{\substack{\text { Reas } \\ \text { Reas }}}$ | $\stackrel{4}{4}$ |  | 7 | ${ }_{\text {arg }}^{\text {arg }}$ | \% | ${ }_{\text {cose }}^{\text {mag }}$ | ${ }_{3}^{3}$ |  | 10 |  |  |
| ${ }_{\substack{\text { anes } \\ \text { Raso }}}^{\text {Raso }}$ |  |  | 7 |  | \% | $\underbrace{\text { wase }}_{\text {wasid }}$ | $\stackrel{3}{4}$ | coiver | , |  |  |
| $\underbrace{\text { as }}_{\substack{\text { afas } \\ \text { Ras2 }}}$ |  |  | 7 | ${ }_{\text {a }}^{\text {ara }}$ | \% | $\underbrace{}_{\substack{\text { watal } \\ \text { wat2 }}}$ | ${ }_{4}^{4}$ | wixiol | ${ }_{4}^{10}$ |  |  |
|  |  | $\xrightarrow[\substack { \text { an7 } \\ \begin{subarray}{c}{775{ \text { an7 } \\ \begin{subarray} { c } { 7 7 5 } }\end{subarray}]{ }$ |  |  |  |  |  | come |  |  |  |
| $\underbrace{\text { Ras }}_{\substack{\text { Rass } \\ \text { Rase }}}$ |  | ¢ | 7 |  | \% |  |  | cinco |  |  |  |
|  | ${ }_{4}^{4}$ | $\substack { \text { R77 } \\ \begin{subarray}{c}{729{ \text { R77 } \\ \begin{subarray} { c } { 7 2 9 } } \end{subarray}$ | 7 |  |  |  | 8 | coin | 4 |  |  |
|  | ${ }_{4}^{4}$ |  | 3 | $\underbrace{\text { and }}_{\substack{\text { Rast } \\ \text { Ras }}}$ |  |  |  |  | ${ }_{\text {do }}$ |  |  |




|  | Selen | cemur | schem | citaur |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\stackrel{3}{9}$ | ${ }_{\substack{1038}}^{1038}$ | : | ${ }_{\substack{\text { anat } \\ \text { and }}}$ |  |
| (ex | $\stackrel{3}{8}$ |  | \% |  |  |
| cosicem | \% |  | \% |  |  |
| cis | 9 |  | \% |  |  |
| cose | \% | ema | \% |  | \% |
|  | ? |  | \% |  | \% |
|  | $\stackrel{9}{9}$ |  | \% |  | \% |
|  | \% | ${ }^{\text {man }}$ | \% |  | \% |
|  | ? |  | $\stackrel{9}{9}$ | coiction | \% |
|  | \% |  | \% |  | \% |




USE the following waveforms in place of those in the Power Supply diagram in the "Section 9-DIAGRAMS" section of the manual if your instrument has a Preregulator board.


3 V

PROBE GROUND LEAD ON PIN 4 OF U920

0 V



29


| ASSEMBLY A10 |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| CIRCUIT NUM8ER | SCHEM LOCATION | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATION } \end{aligned}$ | CIRCUIT <br> NUM8ER | SCHEM LOCATION | $\begin{aligned} & \text { BOARD } \\ & \text { LOCATION } \end{aligned}$ | CIRCUIT <br> NUM8ER | SCHEM LOCATION | BOARD LOCATION | CIRCUIT NuM8ER | SCHEM LOCATION | BOARD LOCATION |
| C447 | 28 | 5 K | CR972 | 5 H | 9 H | R872 | 3L | 41 | 0931 | 5E | 9K |
| C 448 | 4A | 5F | CR973 | 6 H | 9 G | R873 | 3N | 11 | U985 | 6． | 91 |
| C86 1 | 4 J | 71 | CR974 | 6 H | 9G | R874 | 3N | 11 | U990 | 2 H | 6G |
| C863 | 4J | 71 | CR977 | 2K | 7H | R875 | 4L | 4 H |  |  |  |
| C864 | 3K | 61 | CR985 | 5. | 9G | R876 | 5N | 11 | VR901 | 7E | 5K |
| C865 | 3K | 61 |  |  |  | R877 | 3L | 6G | VR951 | 3F | 10K |
| C871 | 2N | 1 J | DS867 | $2 J$ | 51 | R878 | 3L | 5 H |  |  |  |
| $\mathrm{C873}$ | 3 N | 1 J | DS868 | 2K | 51 | R879 | 4 L | 5H | W447 | 3B | 5 J |
| C876 | 5N | $1 J$ | DS870 | 7K | 51 | R880 | 4L | 5 H | W448 | 38 | 6G |
| C 877 | 2 L | 1 J |  |  |  | R881 | 4L | 5 H | W877 | 3 L | 41 |
| C878 | 4L | 5 H | L971 | 51 | 8G | R882 | 5L | 5 H | W878 | 3 L | 41 |
| C879 | 4L | 5 H | L972 | 61 | 10 H | R883 | 5L | 5 H | W887 | 2 L | 4.5 |
| C886 | 4N | 1 J |  |  |  | $R 884$ | 5 K | 5 H | W952 | 8E | 9K |
| C 901 | 18 | 5K | P7001－1 | 8 L | 6 E | R886 | 4N | 11 | W964 | 3 J | 4 J |
| C 937 | 6 F | 7 J | P7001－2 | 8L | 6 E | R887 | 4N | 11 | W965 | 41 | 9 G |
| C 945 | 4E | 10.5 | P7001－3 | 8L | 6E | $R 940$ | 7 F | 9 J | W966 | 3 J | 7 G |
| C 947 | 4F | 10.5 | P7001－4 | 8L | 6 F | R941 | 7G | 9 J | W968 | 31 | 6G |
| C 951 | 7 F | 10J | P8710－1 | 4N | 1．J | R942 | 7 F | 101 | W975 | 5． | 9G |
| C956 | 7 F | 9 J | P8710－2 | 3 N | 1J | R945 | 6D | 101 | W976 | 6 J | 9G |
| C957 | 7 F | 10． | P8710－3 | 5N | 1」 | $\mathrm{R946}$ | 7 D | 10K | W982 | 6 J | 4.1 |
| C961 | 3 J | 6 G | P8710－4 | 2 N | 1J | $R 947$ | 7 D | 10 J | W985 | 6. | 9G |
| C965 | 3H | 7H | P8710－5 | 21 | 1」 | R948 | 7E | 9 J | W986 | 6.1 | 9G |
| C971 | 51 | 9G | P9000－1 | 1 A | 6.5 | R950 | 6 E | 10K | W1001－5 | 8 D | 5A |
| C 972 | 61 | 9G | P9000－2 | 2A | 6.5 | $R 951$ | 7E | $9 J$ | W1001－14 | 8 D | 6 A |
| C 975 | 51 | 8 G |  |  |  | R953 | 8 E | 9 J | W1001－32 | 8D | 9A |
| $\mathrm{C976}$ | 61 | 10G | 0877 | 3L | 51 | R954 | 7F | 9 K | W4000－3 | 6 L | 6 F |
| C977 | 2 L | 7H | 0940 | 7G | 91 | R956 | 6 F | 9 J | W4000－23 | 6L | 9 F |
| $\mathrm{C985}$ | 6 J | 101 | 0942 | 8G | 91 | R990 | 21 | 6 H | W4000－24 | 7 L | 9 F |
| C990 | 21 | 6H | 0948 | 7 E | 10K | R 992 | 21 | 6 H | W4000－25 | 71 | 9 F |
| C 992 | 2 J | 6 H | 0954 | 7 E | 10K | R 994 | 2 J | 6 H | W4000－26 | 6 L | 9 F |
| C 995 | 2 J | 6H | 0956 | 7F | 9K | R995 | 2J | 6 H | $\begin{aligned} & \text { W4000-27 } \\ & \text { W'8010 } \end{aligned}$ | $\begin{aligned} & 6 L \\ & 18 \end{aligned}$ | $\begin{aligned} & 9 \mathrm{~F} \\ & 6 \mathrm{~J} \end{aligned}$ |
| CR860 | 3 J | 71 | R447 | 28 | 6 K | S901 | 1 A | 5 K | W8020 | 2 B | 6.5 |
| CR863 | 4K | 61 | R448 | 38 | 5K |  |  |  | W8030 | 8 H | 81 |
| CR867 | 2 J | 61 | R450 | 2 A | 5K | T448 | 38 | 5 J | W8040 | 6G | 81 |
| CR868 | 2K | 61 | R860 | 3.1 | 4K | T925 | 3 C | 7K | W8700-1 | 5 L | 5 H |
| CR940 | 7G | 91 | R861 | 4．J | 4K | T940 | 2H | 7H | W8700－2 | 7 L | 5 H |
| CR942 | 8 G | 10 J | R863 | 4J | 71 | T942 | 7 F | 8 J | W8700－3 | 8 L | 5 H |
| CR956 | $7 F$ | 9 J | R864 | 3． | 61 |  |  |  | W8700－4 | 8 L | 5H |
| CR961 | 31 | 7 G | R865 | 3K | 61 | TP500 | 8 D | 10 G | W8700－5 | 7L | 51 |
| CR963 | 31 | 7 G | R867 | 2．」 | 6 H | TP501 | 8 D | 10 B | W8700－6 | 7 L | 51 |
| CR965 | 3 H | 7G | R868 | 2J | $61$ | TP934 | $6 \mathrm{G}$ | $8 \mathrm{~K}$ | W8700-7 | 7 L | 51 |
| CR967 | $4 \mathrm{H}$ | 7G | R870 | 2N | 11 | TP952 | 7 G | 8． | W8700－8 | 5 L | 51 |
| CR971 | 5 H | 8 G | R871 | 2N | 11 | ． |  |  |  |  |  |
| Partial 410 also shown on diagrams 2，3，4，5，6， 7 and 10. |  |  |  |  |  |  |  |  |  |  |  |

TABLE（CONT）

Power supply, probe adjust \& CRT $\stackrel{\text { § }}{ }$ (CONT)

| assemivant |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| cement | seatime | Reano |  | sexmm | como | ${ }_{\text {cosem }}^{\text {cimam }}$ | Scation | cesmo |  | sotam | cosemo |
|  | $\stackrel{0}{ }$ | ${ }^{10}$ |  | c | ${ }^{2 a}$ | $\underbrace{}_{\substack{\text { and } \\ \text { and }}}$ |  |  |  | ${ }^{\circ}$ | ${ }^{10}$ |
| ${ }_{\substack{\text { casse } \\ \text { cess }}}$ | ${ }_{x}^{6}$ | ${ }_{10}^{10}$ | P8006-2 |  | 10 |  |  | , | (tas |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |  |  |
| Assemili Als |  |  |  |  |  |  |  |  |  |  |  |
| cemerar |  | Leano |  | somm | Leamon |  | Scamo | samis | cemar | scation | beation |
|  |  |  | ceis | ${ }_{30}^{26}$ | 3 | $\underbrace{}_{\substack { \text { nema } \\ \begin{subarray}{c}{\text { Remam }{ \text { nema } \\ \begin{subarray} { c } { \text { Remam } } }\end{subarray}}$ |  | ${ }_{4}^{\text {3F }}$ | $\underbrace{\substack{\text { max } \\ \text { end }}}_{\text {max }}$ |  |  |
|  | $\begin{gathered} \text { cic } \\ 200 \\ 20 \end{gathered}$ | 26 | cess | ${ }^{6}$ | ${ }^{\text {ac }}$ |  |  | , |  |  | \% |
|  |  |  | $\left.\right\|_{\substack{989 \\ \hline 989}} ^{\text {an }}$ | ${ }_{8}^{\circ}$ | ${ }_{8}^{\circ c}$ |  |  | coid | $\underbrace{}_{\substack{\text { fresen } \\ \text { Resa }}}$ | ${ }_{\substack{18 \\ 18}}$ | ${ }_{\substack{8 \\ 88}}$ |
|  | 近 |  |  | , |  |  |  |  | (tay | ${ }_{\substack{20 \\ 20}}^{20}$ |  |
|  |  |  | ${ }^{\text {Reme }}$ | \% | ${ }_{0}^{\circ}$ |  |  |  |  | ${ }_{4}^{26}$ | ${ }_{\substack{28 \\ 50}}^{\substack{28}}$ |
|  |  |  |  |  |  | $\underbrace{\substack{\text { amas } \\ \text { and } \\ \text { and }}}_{\text {and }}$ | 品 | cos | yeny | ${ }_{\substack{3 \\ 50}}$ | ${ }_{86}$ |
|  | - |  |  |  |  |  |  |  |  |  |  |
| Chassis mounteo Pants |  |  |  |  |  |  |  |  |  |  |  |
| creur | Somen | deame |  | Scem |  |  | Sumb | amen | cemer | Sown | ,oatio |
|  |  |  | 1935 |  |  |  |  |  |  |  |  |
| (tam | ${ }_{\substack{2 \\ 20}}$ | coms |  | "m | cos cims |  | ${ }_{\text {and }}^{\substack{\text { 2n }}}$ |  | vax | " |  |



MANUAL CHANGE INFORMATION
Date: 7-22-82
$\qquad$ Change Reference: C14/782

Product: $\qquad$ Manual Part No.: 070-3826-00

## DESCRIPTION

EFF SN: SEE BELOW

## TEXT CHANGES

Page 5-4 PROCEDURE STEPS (EFF ALL SN)
CHANGE: Step 1 b to read....
Set the digital voltmeter to 200 VDC, connect the low lead to common (TP934) and connect the volts lead to TP952.

REPLACEABLE ELECTRICAL PARTS LIST CHANGES

## CHANGE:

PN
A10C628A/B 295-0138-01 A10C803 281-0791-00
A10C990 285-1184-00
A10C995 285-1184-00
A10C992 285-1184-00
A10U305
A 10 U 310
A 10U315
A10U317
A10U607
A 10U640
A13U665 A 13 U670 A13U690
A13U693
156-0728-02

156-0384-02 156-0388-03
156-0382-00
156-1195-01
156-0382-02
156-0382-02
156-0385-02
156-0480-02

SN
DESCRIPTION
B020950
CAP,SET,MATCHED: 1UF,0.01UF, $1 \%$ OA RANGE 3\%
B018550
CAP,FXD,CER DI: 270PF, 10\%, 100V
CAP,FXD,MTLZD: $0.01 \mathrm{UF}, 20 \%, 4000 \mathrm{~V}$
CAP,FXD,MTLZD: 0.01UF,20\%.4000V
CAP,FXD,MTLZD: $0.01 \mathrm{UF}, 20 \%, 4000 \mathrm{~V}$
MICROCIRCUIT,DI: QUAD 2-INP STATE W/OC
MICROCIRCUIT,DI: QUAD 2-INP NAND SCHMITT
MICROCIRCUIT,DI: QUAD 2-INP NAND GATE
MICROCIRCUIT,DI: DUAL D FLIP-FLOP
MICROCIRCUIT,DI: QUAD 2-INP NAND GATE
MICROCIRCUIT,DI: DUAL RETRIG/RESET
MICROCIRCUIT,DI: QUAD 2-INP NAND GATE
MICROCIRCUIT,DI: QUAD 2-INP NAND GATE
MICROCIRCUIT, DI: HEX INVERTER
MICROCIRCUIT, DI: QUAD 2-INP AND GATE

REMOVE:
A10C602
A 10 C 606
A10C607
281-0862-00
B018550
CAP, FXD, CER DI: $0.001 \mathrm{UF},+80-20 \%, 100 \mathrm{~V}$
281-0862-00
B018550
CAP, FXD, CER DI: $0.001 \mathrm{UF},+80-20 \%, 100 \mathrm{~V}$
281-0862-00
B018550
CAP, FXD, CER DI: $0.001 \mathrm{UF},+80-20 \%, 100 \mathrm{~V}$

ADD :
A 10 C 372
A 10 C 606
A 18 C 905

281-0862-00
281-0862-00
285-1250-00

B016700
CAP,FXD CER DI: $0.001 \mathrm{UF},+80-20 \%, 100 \mathrm{~V}$
CAP, FXD, CER DI: $0.001 \mathrm{UF},+80-20 \%, 100 \mathrm{~V}$
CAP,FXD, PPR DI: $0.1 \mathrm{UF}, 20 \%, 250 \mathrm{VAC}$

B022000

CAP,

Product: 2215 SERVICE Date: 7-22-82 Change Reference: C14/782

## DESCRIPTION

## SCHEMATIC CHANGES

DIAGRAM $2 \mathrm{CH} 1 \& \mathrm{CH} 2$ VERTICAL PRIAMPS ADD: C372 (1000PF) from pin 7 of U197D to ground.

DIAGRAM 4 SWEEP GENERATOR \& LOGIC REMOVE:

C607 (0.001UF) and C602 (0.001UF)
PC40
Remove the run connecting the A TRIGGER SIG from Q492 to pin 4 of U603A (location 4 C ). Then ground pin 4 of U603A.

DIAGRAM 6 AUTO INTENSITY \& Z-AXIS CHANGE:

C803 from 680PF to 270PF
DIAGRAM 9 POWER SUPPLY, PROBE ADJUST \& CRT CHANGE:

```
C990 & C995 from 0.02UF to 0.01UF
C992 from 0.02UF to 0.01UF
```


# Tektronix <br> COMMITTED TO EXCELLENCE 

Date: 10/21/82 Change Reference: $\qquad$
Product: 2215 Service

Manual Part No.: $\qquad$

## DESCRIPTION

EFF SN: See Below

## ELECTRICAL PARTS LIST CHANGES

Part No. Ser. No. Description $\quad \underline{P C}$

REMOVE:

| A10C264 | $283-0084-00$ | B021950 | CAP,FXD, CER DI: 270PF,5\%, 1000V | 53 |
| :--- | :--- | :--- | :--- | :--- |
| A10R496 | $315-0124-00$ | B025695 | RES,FXD,CMPSN: 120K OHM,5\%,0.25W | 66 |
| A12C140 | $281-0775-00$ | B025200 | CAP,FXD,CER DI: 0.1UF,20\%,50V | 77 |
| A12CR626 | $152-0245-00$ | B025695 | SEMICOND DVC,DI: SI,40V | 66 |
| A12CR676 | $152-0245-00$ | B025695 | SEMICOND DVC,DI: SI,40V | 66 |
| A12CR630 | $152-0141-02$ | B025695 | SEMICOND DVC,DI: SI, 30V | 66 |
| A12CR680 | $152-0141-02$ | B025695 | SEMICOND DVC,DI: SI, 30V | 66 |

ADD:

| A10C619 | $281-0808-00$ | B025695 | CAP,FXD, CER DI: 7PF, 20\%, 100V | 66 |
| :--- | :--- | :--- | :--- | :--- |
| A10C674 | $281-0808-00$ | B025695 | CAP,FXD,CER DI: 7PF,20\%,100V | 66 |
| A10Q502 | $151-0199-00$ | B025695 | TRANSISTOR: PNP,SI | 64 |
| A10Q505 | $151-0424-00$ | B025695 | TRANSISTOR: NPN,SI | 64 |
| A10R500 | $315-0203-00$ | B025695 | RES,FXD, CMPSN: 20K OHM,5\%,0.25W | 64 |
| A10R502 | $315-0203-00$ | B025695 | RES,FXD,CMPSN: 2OK OHM,5\%,0.25W | 64 |
| A12Q626 | $151-0424-00$ | B025695 | TRANSISTOR: NPN,SI | 66 |
| A12Q676 | $151-0424-00$ | B025695 | TRANSISTOR: NPN,SI | 66 |
| A12R113 | $307-0107-00$ | B025200 | REX,FXD,CMPSN: 5.6 OHM,5\%,0.25W | 77 |

CHANGE TO:

| A10C645 | $290-0136-00$ | B025695 | CAP,FXD, ELCTLT: 2.2UF,20\%,20V | 66 |
| :--- | :--- | :--- | :--- | :--- |
| A10C647 | $281-0852-00$ | B025695 | CAP,FXD,CER DI: $1800 \mathrm{PF}, 10 \%, 100 \mathrm{~V}$ | 66 |
| A10R619 | $315-0472-00$ | B025695 | RES,FXD,CMPSN: 4.7K OHM,5\%,0.25W | 66 |
| A10R623 | $315-0331-00$ | B025695 | RES,FXD,CMPSN: 330 OHM, $5 \%, 0.25 \mathrm{~W}$ | 66 |
| A10R673 | $315-0331-00$ | B025695 | RES,FXD,CMPSN: 330 OHM,5\%,0.25W | 66 |
| A10R674 | $315-0472-00$ | B025695 | RES,FXD,CMPSN: 4.7K OHM,5\%,0.25W | 66 |
| A12R627 | $315-0201-00$ | B021950 | RES,FXD,CMPSN: 200 OHM,5\%,0.25W | 56 |
| A13Q664 | $151-0424-00$ | B025695 | TRANSISTOR: NPN,SI | 70 |

DIAGRAM CHANGES

DIAGRAM 〈 1 CH $1 \&$ CH 2 ATTENUATORS
REPLACE--C140 (Location $2 N$ ) with R113, a $5.6 \Omega$ resistor.

Page 1 of 2

## DESCRIPTION

## DIAGRAM CHANGES (Cont'd)

DIAGRAM $\mathrm{CH} 1 \& \mathrm{CH} 2$ VERTICAL PREAMP
REMOVE--C264 (Location 7C).

DIAGRAM 4 TRIGGER
REMOVE--R496 (Location 5L).
ADD --Q502, R500, Q505, and R502 (see partial schematics).


DIAGRAM
SWEEP GENERATOR \& LOGIC
CHANGE--C645 (Location 4B) to a $2.2 \mu \mathrm{~F}$ capacitor.
--C647 (Location 4B) to an 1800 pF capacitor.
REMOVE--CR626, CR630; ADD--C619, Q626; CHANGE--R619, R623 (see partial. schematic).


DIAGRAM


TIMING SWITCH
CHANGE--R627 (Location 7C) to a $200 \Omega$ resistor.

DIAGRAM
ALTERNATE B SWEEP
REMOVE--CR676, CR680; ADD--C674, Q676; CHANGE--R673, R674 (see partial schematic).



[^0]:    ${ }^{\text {a Performance Requirement not checked in Service Manual. }}$

[^1]:    ${ }^{a}$ Performance Requirement not checked in Service Manual.

[^2]:    ${ }^{\text {a }}$ Performance Requirement not checked in Service Manual.

[^3]:    ${ }^{a}$ Performance Requirement not checked in Service Manual.

[^4]:    2. Apply a signal to either vertical-channel input connector and set the VERTICAL MODE switch to display the channel used. To display two time-related input signals use both vertical-channel input connectors and select BOTH VERTICAL MODE; then select either ALT or CHOP, depending on the frequency of input signals.
[^5]:    ${ }^{\mathrm{a}}$ Voltage equivalent for levels (voltage discharged from a $100-\mathrm{pF}$ capacitor through a resistance of $100 \Omega$ ):

    | $1=100$ to $500 \vee$ | $4=500 \vee$ | $7=400$ to $1000 \vee$ (est) |
    | :--- | :--- | :--- |
    | $2=200$ to $500 \vee$ | $5=400$ to $600 \vee$ | $8=900 \vee$ |
    | $3=250 \vee$ | $6=600$ to $800 \vee$ | $9=1200 \vee$ |

[^6]:    
    varies with
    AUTESTITH
    SETTNG

