Errata

Title & Document Type: 5326C/27C Frequency Counters Operating and Service

Manual

Manual Part Number: 05326-90038

Revision Date: June 1974

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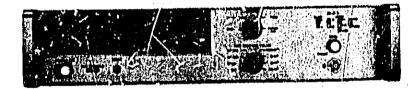
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FREQUENCY COUNTERS

5326C/5327C





HEWLETT PACKARD

CERTIFICATION

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OPERATING AND SERVICE MANUAL

FREQUENCY COUNTERS 5326C/5327C

SERIAL PREFIXES:

5326C — 1312A 5327C — 1312A

This manual applies to HP Model 5326C and HP Model 5327C having serial prefix number 1312A.

SERIAL PREFIXES NOT LISTED

For serial prefixes above 1312A (5326C and 5327C), a "Manual Changes" sheet is included with this manual. For serial prefixes below 1312A, refer to Section VII of this manual.

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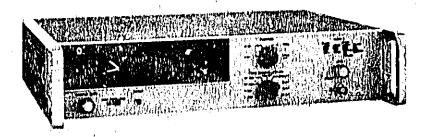
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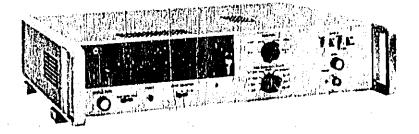
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Figure 1-1. 5326C/5327C Frequency Counters and Power Cord

MODEL 5326C



MODEL 5327C



RACK MOUNT KIT

POWER CORD





1

SECTION I

GENERAL INFORMATION

1-1. DESCRIPTION

1-2. The Hewlett-Packard Models 5326C/5327C Frequency Counters measure frequency, period, period average, ratio, and perform totalizing and scaling functions. The models feature a 7-digit display (8 digits optional), 1 M Ω input impedance, and display storage. Decimal point and unit readouts (annunciators) are displayed automatically with each operating selection. The input channel has an attenuator, trigger slope control, level control, and ne-de coupling. The basic difference between the two models is the addition of the prescaler assembly in the 5327C. This assembly increases the frequency range from 50 MHz to 650 MHz.

1-3. Electrical and mechanical spreifications are listed in Table 1-3.

1-4. IDENTIFICATION

1-5. Hewlett-Packard instruments have a 2-section, 9-digit serial number (0000A00000), located on the rear panel. The 4-digit serial prefix identifies instrument changes. The 5-digit number is the serial number of each instrument. If the serial prefix of your instrument differs from that listed on the title page of this manual, there are differences between this manual and your instrument. Lower serial prefixes are documented in Section VII, and higher serial prefixes are covered with manual change sheets included with the manual. If the change sheet is missing, contact the nearest Hewlett-Packard Sales and Service Office, listed on the inside rear cover of this manual.

1-6. APPLICATIONS

1-7. The 5326C model measures frequency from dc to 50 MHz (5327C to 550 MHz) and period average to 10 MHz. With the FUNCTION switch in the START position, the counter will totalize the number of input pulses. With an external signal applied to OSC jack and the switch set to EXT, the counter will display the ratio between EXT and INPUT A signals, scaled by the time base setting.

1-B. OPTIONS

1-9. Both models can be ordered with the following options: Option 001, 8-digit; Option 002, remote programming; and Option 003, digital recorder output.

1-10. EQUIPMENT SUPPLIED AND ACCESSORIES AVAILABLE

1-11. Table 1-1 lists equipment supplied, and Table 1-2 lists accessories available.

Table 1-1. Equipment Supplied

Description	HP Part No.
Detachable Power Cord, 7-1/2 feet (229 cm) long	8120-1378
Ruck Mounting Kit	05326-60046

Table 1-2. Accessories Available

Description	HP Part No.
Digital Recorder	5055A
Interconnect Cr ble, Digital Recorder, 6 feet (183 cm)	562 A- 160
50-ohm, BNC-to-BNC Coaxial Cable, 4 feet (122 cm)	10503-6001
Circuit Board Extender, 15-pin	5060-0049
Input Amplifier Circuit Board Exter der	10532-60001
Circuit Board Extender, 18-pin	5060-2041
Extender Board Kit; includes two 5060-0049, and one each 5060-2041, and 10532-60001	10532 A

Table 1-3. Specifications

INPUT CHANNEL A

Ranger

de coupled: 0 — 50 MHz ne coupled: 20 Hz — 50 MHz

Sensitivity:

0.1 V rms sine wave
0.2 V peak-to-peak pulse
8 ns minimum pulse width
Sensitivity can be decreased by 10 or 100
times, using the ATTEN switch.

Impedance

 $1 \, \mathrm{M}\Omega$ shunted by less than $25 \, \mathrm{pF}$

Dynamic Input Voltome Range:

0.1 to 3 V rms actions attenuator setting. ±5 V dc times attenuator setting.

Trigger Level:

PRESET to center triggering about 0 V or variable over the range of -3 V to +3 V times attenuator setting. Trigger threshold band <1.0 mV, referred to input at maximum frequency.

Overload Protection:

250 V rms on all attenuator settings, except 25 V rms on X1 above 50 kHz.

Slope:

Independent selection of positive or negative slope.

START (Totalizing and Eculing)

Range: 0 - 10 MHz

Factor: 1 - 10s in decade steps

Output: Rear Panel TIMF BASE BNC

Display: Channel divided by scaling factor

FREQUENCY

Range: 0-50 MHz

Input:

Channel A (provides triggered frequency measurement)

Gate Times:

0.1 µs to 10 s in decade steps

Accuracy:

Direct: ±1 count ± time base accuracy

Display:

kHz, MHz, or GHz with positioned decimal point

PERIOD AVERAGE

Range: 0 - 10 MHz

Periods Averaged: 1 - 108 in decade steps

Frequency Counted: 10 MHz

Accuracy: ±1 count ± time base accuracy ± trigger error*

Display:

ne, us with positioned decimal point

RATIO

Display:

Any input function F_{ext} times Multiplier (M) M=1 to 10⁸ selectable in decade steps

Range:

Any input function: See appropriate function section. F_{ext}: (External Oscillator Input) 100 Hz — 10 MHz

Mode:

Any input function

Accuracy:

Accuracy of selected input function ± trigger error of F_{ext}.

*Trigger error is less than +0,3% of one period ÷ periods averaged for signals with 40 dBm or better signal-to-noise ratio and 100 mV rms amplitude.

INPUT CHANNEL B (5327C ONLY)

Ranger

Direct: 0 to 50 MHz. Prescaled: 0 to 550 MHz.

Sensitivity:

Direct: 15 mV rms. Prescaled: 25 mV rms.

Impedance: 50Ωnominal.

Maximum Input: 3.5 V rms; 5 V peuk.

Trigger Level: JV

Accuracy:

Direct: ±1 count t time base accuracy
Prescaled: ±1 count displayed* ± time base
accuracy

Display:

MHz, kHz, or GHz with positioned decimal point

GENERAL SPECIFICATIONS

Display: 7 digits (8 optional)

Blanking:

Suppresses display of unwanted zeros left of the most significant digit.

Display Storage:

Holds reading between samples. Rear panel switch overrides storage.

Sample Late:

FAST position: Continuously variable from less than 100 µs to approximately 20 ms. NORM position: Continuously variable from less than 20 ms to approximately 5 seconds. HOLD position: Display can be held indefinitely.

Overflow:

Neon indicates when display range is exceeded.

Operating Temperature: 0° to 50° C

*±10 counts of input frequency (±1 count displayed)

TIME BASE

Crystal Frequency: 10 MHz

Stability:

Aging Rate: <3 parts in 10⁷/mo.
Temperature: ≤±2.5 parts in 10⁸, 0° to 50°C.
Line Voltage: ≤±1 part in 10⁷ for 10% line voltage.

Oscillator Output:

10 MHz, TTL type output levels, 50Ω series impedance at rear panel BNC.

External Input:

100 Hz - 10 MHz; 1 V rms into 1 k Ω .

Time Base Output:

Negative pulses, +3 V to 0 V (open circuit), typically 100 ns wide. In START output is INPUT A frequency divided by TIME BASE/MULTIPLIER switch setting. Available at rear panel BNC.

Gate Output:

TTL level pulses; low while gate open, high while gate closed. Available at rear panel Bl.C.

Weight:

5326C: Net, 15 lb, 3 oz. (6, 9 kg). Shipping 18 lb, 1 oz. (8, 5 kg).

5327C: Net, 15 lb, 7 oz. (7, 0 kg). Shipping, 18 lb, 5 oz. (8, 3 kg).

Dimensions:

3-25/32" high by 16-%" wide by 11-%" long (88 by 425 by 286 mm).

Equipment Supplied:

Power cord (7-1/2 ft.) and rack mounting kit

Accessories Available:

HP 10503A, 50ΩBNC Cable, 4 Ft. (122 cm). HP 10532A, Fxtender Board Kit containing 2 ea. 15-pin extender 5060-0049, 1 ea. 18-pin extender 5060-2041, and 1 ea. Amplifier Extender, 10532-60001.

HP Cable 562A-16C (6 ft, 183 cm) to connect 5326C with Option 003 to HP 5050B or

5055A Digital Recorder.
Option 001: 8-digit display

Option: 002: Remote programming

Controls: All front panel controls are single line programmable except:

NORM-CHK
FAST/NC RM Mode
Input Attenuators
AC/DC Input Signal Coupling

Control Signal: Single line control using either contact closure to ground, DTL or TTL drive on all lines except trigger level which is analog programmed (+3 to -3 Vdc).

Connector: Rear panel connector: HP 1251-0085; Amphenol 57-40360-375. (36-pin blue ribbon)

Mating Connector: HP 1251-0084; Amphenol 57-30360-375.

Options 003: Digital output (for numerals only)

Code: 4-line 1-2-4-8 BCD, "1" state high "0" state +0.25V at -1 mA; "1" state: +5V open circuit, 2.5 k Ω source impedance nominal.

Print Command: +5 V to 0 Vdc coupled; occurs at end of gate.

Storage: Buffer storage is provided so BCD output is constant while next measurement is being made.

Inhibit Input: Inhibits gate when instrument's cycle time is less than the time required for external equipment to interrogate BCD output. Positive inhibit +5 Vdc. Connector: Rear panel connector: HP 1251-0087; Amphenol 57-40500-375. (50-pin blue abbon).

Mating Connector: HP 1251-0086; Amplienol 57-30500-376.

Option 010: Oscillator Assembly.

Crystal Frequency: 10 MHz
Type: TCXO
Aging Rate: <1 × 10.7/ma.
Temp. Stability: <45 × 10.7, 0° to 50°C,
Short Term Fluctuation: <1 × 10.4 rms.
(1 sec. avg.).

Warm-up: (room temp crystal).

Line Voltage: < 15 x 10 * (10% change).

Option 011: Oscillator Assembly.

Crystal Frequency: 10 MHz.

Type: Oven-controlled oscillator
Aging Rate: <5 x 10⁻¹⁰/day.

Temp. Stability: <3 x 10⁻¹⁰, 0° to 50°C,

Short Term Fluctuation: <1 x 10⁻¹¹ rms
(1 sec avg.).

Warm-up: <±5 x 10 ^a in 15 min. Line Voltage: <5 x 10 ^a (10% change).

*Typical

SECTION II INSTALLATION

2-1. INTRODUCTION

2.2. This section contains information for unpacking, inspection, repacking, storage, and installation. The instructions for remote programming are also given in this section.

2-3. UNPACKING AND INSPECTION

2-4. If the shipping carton is damaged, ask that the carrier's agent be present when the instrument is unpacked. Inspect the instrument for damage (scratches, dents, broken knobs, etc.). If the instrument is damaged or fails to sulf-check (Self-Check Procedures, Figure 3-3), notify the carrier and the nearest Hewlett-Packard Sales and Service Office immediately (offices are listed at the back of this manual). Retain the shipping carton and padding material for the carrier's inspection. The sales and service office will arrange for the repair or replacement of your instrument without waiting for the claim against the carrier to be settled.

2-5. STORAGE AND SHIPMENT

2-6. Packaging

- 27. To protect valuable electronic equipment during storage or shipment always use the best packaging methods available. Your Hewlett-Packard Sales and Service Office can provide packing material such as that used for original factory packaging. Contract packaging companies in many cities can provide dependable custom packaging on short notice. Here are two recommended packaging methods:
- a. RUBBERIZED HAIR. Cover painted surfaces of instrument with protective wrapping paper. Pack instrument securely in strong corrugated container (250 lb/sq. in, bursting test) with 2-inch rubberized hair pads placed along all surfaces of the instrument. Insert fillers between pads and container to ensure a snug fit.
- b. EXCELSIOR. Cover painted surfaces of instrument with protective wrapping paper. Pack instrument in strong corrugated container (350 lb./sq. in. dursting test) with a layer of excelsior about 6 inches thick packed firmly against all surfaces of the instrument.

2-8. Environment

- 2.9. Conditions during storage and ship nent should normally be limited as follows:
 - a. Maximum altitude; 25,000 feet.
 - b. Minimum temperature: 40°F (40°C).
 - c. Maximum temperature: +167°F (+75°C).

2-10. RACK INSTALLATION

- 2-11. The counter is ready for bench operation as shipped from the factory. Additional parts necessary for rack mounting are packaged with the instrument. To convert to rack installation, proceed as follows:
 - n. Remove tilt stand.
- b. Remove feet (press the footrelease button, slide foot toward center of instrument, and lift off)
- c. Remove adhesive-backed trim strips at front end of sides.
- d. Attach filler strip along bottom edge of front panel, using two screws on outer edges of filler strip. Omit the center screw.
- e. Attach flanges to front end of sides (larger corner notch toward bottom of instrument). Instrument is now ready to mount in standard rack.

CAUTION

Ambient temperature in rack during operation should not exceed 122°F (50°C). Be sure instrument position in the rack permits adequate air circulation and that nearby equipment does not discharge hot air directly on the instrument.

2-12. POWER CONNECTION

2-13. Line Vollage

2-14. The counter may be operated from either 115 or 230 volt (±10%) power lines. A slide switch on the rear panel permits qu'ck conversion for operation from either voltage. Insert a narrow-blade screwdriver in the switch slot and slide the

switch to the right for 230 volt operation ("230" marking exposed) or to the left for 115 volt operation ("115" marking exposed). The counter is supplied with e. 115 volt fuse; be sure to replace this fuse for 230 volt operation (see Table 2-1).

CAUTION

Before plugging instrument into ac power line be sure LINE VOLTAGE switch is properly positioned.

Table 2-1. 115/230 Volt Conversion

Line Voltage Conversion	115 Volt	230 Volt
Slide Switch	Left (115)	Right (230)
AC Line Fuse	1.5 Ampere Slow-Blow (HP 2110-0304)	.8 Ampere Slow-Blow (HP 2110-0020)

2-15. Power Cable

- 2-16. The counter is equipped with a detachable 3-wire power cable. Proceed is follows for installation.
- a. Connect plug (3-socket connector) to ac line jack at rear of instrument.
- b. Connect plug (2-blade with round grounding pin) to 3-wire (grounded) power outlet. Exposed portions of instrument are grounded through the round pin on the plug for safety; when only 2-blade outlet is available, use connector adapter (HP part No. 1251-0048), then connect short wire from side of adapter to ground.

2-17, REMOTE PROGRAMMING

2-18. The following paragraphs describe remote programming requirements for the counter.

2-19. Front-Panel Controls

- 2-20. The following front-panel controls are programmable:
 - a. FUNCTION
 - b. TIME BASE/MULTIPLIER

- e. LEVEL control
- d. SAMPLE RATE and HOLD
- e. SLOPE
- f. RESET
- g. INPUT SELECTOR (5327C only)
- 2-21. The following front-panel controls are NOT programmable:
 - a. AC/DC
 - b. FAST/NORM
 - c. ATTEN
 - d. CHK
- 2-22. The trigger LEVEL control may be remotely programmed, or the front-panel LEVEL control may be used manually. When remote programming is used, the LEVEL control must be set to PRESET. Display time may be remotely programmed and/or the front-panel control may be used.

2-23. Remote Programming Requirements

- 2-24. All lines may be controlled by TTL or DTL signals or contact closure to ground J10(36), with the exception of the trig level (J10 pin 30), which is programmed by an analog level, and the sample rate hold (J10 pin 35). The sample rate hold line should NOT be palled up to +5 V by less then 20012 while programming. In order to remote program the counter, the following must be done:
- a. Set FUNCTION switch to any function except START or STOP.
- b. Ground the EXT line at J10 pin 17. Ground is available at J10(36),
- c. Select the desired FUNCTION (see paragraph 2-29).
- d. Select the desired TIME BASE (see peragraph 2-31).
- e. In addition to these, the following may be

Select the slope (+ or -) for the input channel by grounding the slope line for negative (-) and leaving it open for positive (+). This is done on J10 pin 28.

2-25. Trigger Level Programming

2-26. Before programming the trigger LEVEL control, place the knob in the PRESET position. Select the trigger level by putting a dc voltage that is between -3.0 and +3.0 volts onto the trigger level line at J10 pin 30. This voltage, times the attenuator, is the trigger level. Preset is programmed by leaving the pin open or grounded (grounding is preferable if noise exists on the remote program lines). The AC/DC and ATTEN switches on the front panel must be set manually, as they are not programmable.

2-27. Adjusting the Display Time (Sample Rate Knob, Plus FAST/NORM Switch).

2-28. Adjusting the display time, when remote programming, is accomplished by using one of the following methods:

a. Manually adjust the display time by using the front-panel controls, (SAMPLE RATE/FAST-NORM).

b. Set the SAMPLE RATE control full cw and the FAST/NORM/HOLD switch in NORM and connect a 1 M Ω variable resistor in series with 1 k Ω from +5 V to pin 35. This will give a display-time range of about 10 ms to 5 seconds. If a shorter time is desired, move the FAST-NORM-HOLD switch to FAST, which then gives a range of about 50 μ s to 10 ms.

c. Set the SAMPLE RATE control full ccw, place FAST/NORM/HOLD switch in the FAST position, and hold the SAMPLE RATE HOLD line (J10 pine 35) to ground for the desired display time. The display continues for about 100 µs after the ground is released.

2-29. The sample rate disable line is used only with the start command to initiate a totalizing measurement. The sample rate disable command disables auto reset and enables continuous plus and minus transfer commands.

2-30. The computer inhib't command (when Low) inhibits the main gate from opening. This command may be sent from a computer to prevent the counter from making any further measurements. It may also be used as an external sample rate signal, since the command would determine the time between measurements. Auto reset and print command signals are not disabled by computer inhibit.

NOTE

DO NOT ground or otherwise program any of the remote programming lines if the unit is not being operated remotely (EXT line HIGH = not programmed remotely). The line should be left open or, at worst, be pulled up to +5 V by a source impedance of not less than 5 k Ω .

2-31. Function Selection Programming

2-32. To program the desired function, ground (<7 volt) the proper line at J10 as follows:

STOP	Pin 32
START'	Pin 1, 32
PERIOD AVERAGE	Pin 2
FREQ	Pin 6
ENABLE A*	Pin 8
ENABLE B*	Pin 9
ENABLE B+10*	Pin 18

NOTE

When switching between START and STOP, doe not remove the ground from Pin 32.

2-33. Time Base Seizclion Programming

2-34. To program the Time Base, ground (<.7 volt) the proper line at J10:

.1 μs/1	Pin I	Ð
1 με/10	Pin 2	90
10 дв/102	Pin 2	21
.1 ms/10 ⁷	Pin 2	22
1 ms/10 ⁴	Pin 2	2:3
10 ms/10 ⁸	Pin 2	24
.1 s/105	Pin 2	25
1 s/10 ⁷	Pin 2	26
10 s/10 ⁸	Pin 2	27

2-35. BLANKING DEFEAT

2-36. This counter is designed to blank insignificant zeros (zeros to left of data). When blanking occurs, the digital recorder output for the blanked columns is BCD 15 (HHHH). To use this instrument with a digital-analog converter, it is necessary to defeat the blanking feature by repositioning the two jumpers on the A9 Display board. Move the jumpers to position 2, as shown in A9 Component Locator (Section VIII). This connects pin 10 of A9U7 and A9U8 to +5 V. Also, lift the pin 1 lead of A8U2 and connect pin 1 to ground (available at U2 pin 7).

*5327C Model only. Must also select function.

SECTION III

OPERATION

3-1. INTRODUCTION

3.2. Section III contains the operating information that is needed to obtain the most affective performance from the instruments. This includes a general description of the operating modes, the function of all controls and indicators, a self-check procedure, and setup procedures for making basic measurements.

8-8. OPELATING MODES

3.4. The following paragraphs describe the operating modes of totalize, frequency, period average, and ratio.

3-5. Totalize Mode

3-6. START and STOP positions on the FUNCTION selector allow manual opening and closing of the counter's main gate. When the switch is in the STARY position, the counter does not measure frequency, but instead, counts the number of times the signal passes through the trigger point. The input signal, connected to the front-panel INPUT A or B jack, is divided by the MULTIPLIER switch setting prior to counting. For example, when the MULmi. PLIER switch is set to the 1 position, every purse is counted. When the switch is set to 103, the counter registers every one-thousandth pulse. When the FUNCTION switch is set to STOP, the counter stops totalizing and holds the displayed count until the RESET switch is pressed or the MULTIPLIER sylitch setting is changed. If the FUNCTION switch is again set to START before a reset is generated, the count continues to totalize from the previous displayed value. With the FUNCTION switch set to START, the scaled input signal is available at the rear-panel TIME BASE OUTPUT lack. The unit indicators and decimal points are blanked during the totalize mode. The C light is on (in START), indicating counting is taking place.

3-7. Frequency Mode

3-8. In the frequency mode, the input signal connects to the front-panel jack and can be conditioned with the LEVEL, SLOPE, and ATTEN controls. Each cycle of the input signal produces an internal pulse; these pulses are counted while the main gate is open. The gate time is determined by the setting of the TIME BASE switch. The longer the gate is open, the better the resolution and accuracy.

Example: Measure 1 MHz with gate times of 0.1 ms and 10 ms.

1 MHz = 1 x 10s pulses/second

SIGNAL	TIME BASE SWITCH	DISPLAY
1 x 10 ⁶ p/s	× 0,1 x 10 ⁻³ aec =	$1,00~\mathrm{MHz}$
1 v 105 n/a	v 10 × 10 ⁴ sec ≈	1000,0 kHz

3-9. Period Average Mode

3-10. The period average made allows multiple period averages to be made with input frequencies of up to 10 MHz. This mode is useful for making low frequency measurements where maximum resolution is desired. For example, if 10² period averaging is selected, the counter will display the average of 100 periods of the input frequency with the proper decimal point. The accuracy for period average is:

For single period measurements, set MULTIPLIER switch to the 1 position.

3-11, Ratio

3-12. The counter may be used to measure the ratio of two signals in either the frequency or period-average mode. By setting the rear-panel OSC INT-EXT switch to EXT, the counter will accept an external signal (Fext) for use as the internal oscillator. This frequency should be 100 Hz to 10 MHz at 1 V rms minimum to 5 V peak maximum. A second signal (FA), applied to either INPUT A or B jack, is used as the comparator signal. The MULTIPLIER switch controls the resolution of the display. For a ratio of frequencies, the Ratio = FA = DISPLAYED NUMBER:

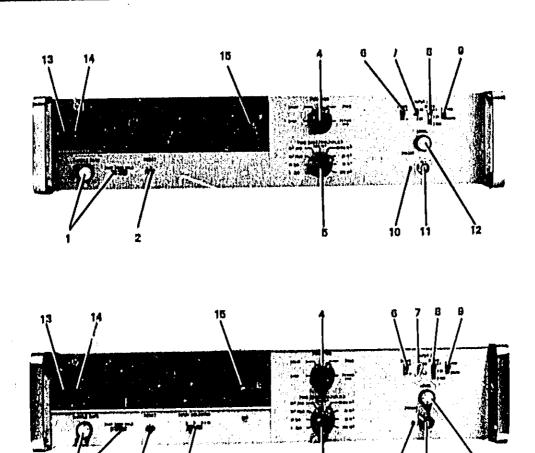
Fext MULTIPLIER SETTING

ratio of periods (P), the Ratio =

$$\frac{P_A}{P_{oxt}} = \frac{F_{ext}}{F_A} = \frac{DISPLAYED NUMBER}{MULTIPLIER SETTING.}$$

3-13. Disregard the units and decimal point; also, ignore any zeros to the left of the most significant digit. It makes no difference which signal is higher in frequency, as long as the two frequencies are within the specifications of their respective channels.

Figure 3-1. Front Panel Controls and Indicators



- 1. SAMPLE RATE control. Applies primary power. Works in conjunction with FAST-NORM-HOLD slide switch to control interval between measurements.
 - a. FAST Varies display time from <100 μ s to >20 ms. STORAGE switch (rear panel) must be ON to use this mode.
 - b. NORM Varies display time from <20 ms to >5 seconds.
 - c. HOLD Holds display indefinitely.
- 2. RESET pushbutton. Resets display and internal count to zero and starts new measurement.

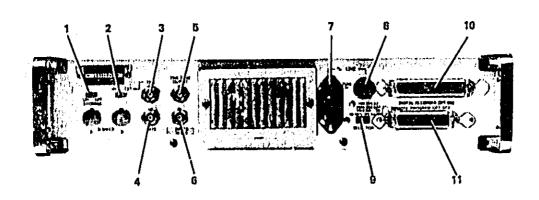
- 3. INPUT SELECTOR switch (5327C). Selects the desired input channel and counting method, direct or prescaled.
 - n. A Counter measures signal applied to INPUT A.
 - b. B -- Counter directly measures signal applied to INPUT B (rear panel). Channel is de coupled to accept de to 50 MHz at 15 mV rms (min).
 - c. B+10 Counter prescales signal applied to INPUT B (rear panel). Channel is dc coupled to accept 0-550 MHz at 25 mV rms (min).

Figure 3-1. Front Panel Controls and Indicators (Continued)

- FUNCTION selector. Selects made of operation. Blue lettering matches corresponding blue lettering on TIME BASE/ MULTIPLIER switch. Select channel with INPUT SELECTOR switch (5327C).
 - a. STOP, START Used during totalize mode to manually open and close counter's main gate and to turn scaled output on and off. Usable with any input channel,
 - b. PERIOD AVG Sets counter to measure period of signal applied to any input channel. Use MULTIPLIER switch to select number of periods to be averaged.
 - c. FREQ Sets counter to measure frequency applied to any input channel. Selecting gate time with TIME BASE switch determines resolution.
- 5. TIME BASE/MULTIPLIER switch.
 Works with the FUNCTION switch; and
 for each mode, it performs as follows:
 - a. Totalize Determines scaling factor for input signal prior to counting.
 - b. Frequency Opens the main gate for the selected amount of time.
 - c. Period Average Used to select the number of periods to be average l.
- SLOPE slide switch. Permits triggering on positive or negative slope of input signal.
- 7. AC-DC slide switch. Used to select direct or capacitor coupling for input signal. Minimum input frequency on AC setting is 20 Hz.
- 8. ATTEN (attenuator) slide switch. Used to select attenuation for input signal. Used in conjunction with LEVEL control

- to set input triggering point. Maximum input; 250 V rms on all ranges except 25 V rms on XI range above 50 kHz. Recommended input is 0.1 V rms to 2 V rms times attenuator setting.
- 9. CHK-NORM (check-normal) switch
 - a. CHK substitutes internal 10 MHz oscillator signal for INPUT A signal.
 - b. NORM counter performs measurements in normal manner.
- 10. Trigger Lamp. Lamp lights when input signal passes through anaplifier trigger level.
- 11. INPUT A jack. Accepts signal for all measurements when INPUT SELECTOR switch is set to A position. Input impedance is $1 M\Omega$ shunted by less than 25 pF, $10 M\Omega$ when using ten-to-one divide probe,
- 12. LEVEL conicol. Used with ATTEN switch to determine trigger level. With X1 attenuator setting, level is variable ±3 volts, on X10, ±30 volts; and on X100, ±300 volts.
- 13. C (count) Annunciator light. Lights when counter's main gate is open. For short-duration gate times, the annunciator circuits include a 50 ms one-shot MV to allow a visible flash to the C light.
- 14. OF (overflow) Annunciator light. Lights when accumulated count exceeds counter capacity.
- 15. * (asterisk) light. Indicates that proper units are not displayed with combination of function/time base selection. To interpret display, add a ziro to right of least significant digit displayed on counter.

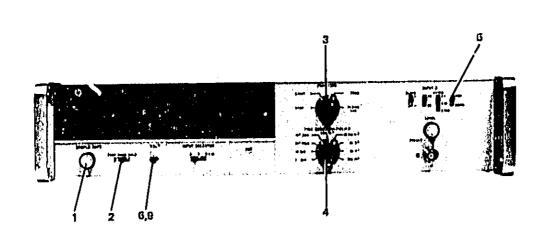
Figure 3-2. Rear Panel Controls and Connectors



- 1. CrORAGE switch. When set to ON, counter displays previous measurement while new measurement is being made. In OFF position, allows continuous display of data being fed into decade dividers.
- 2. OSC switch. In INT position, counter operates in normal manner, using internal 10 MHz oscillator for the time base. In EXT position, permits use of external time base for increased stability, normalizing, or ratio measurements.
- 3. OSC jack. With INT-EXT switch set to INT, provides 10 MHz, >3 volt peak-to-peak output (no load), 50-ohm series impedance. With INT-EXT switch set to EXT, allows external time base input of 100 Hz to 10 MHz at 1 volt rms (5 V peak maximum).
- 4. GATE OUTPUT jack. Provides >2.4 volts output (no load) for external use (50Ω series resistance). Output is low (0 to 0.4 V) when counter's main gate is open.
- 5. TIME BASE OUTPUT jack. Provides negative going >±3 V to 0 V pulses (open

- circuit), >50 nanoseconds wide. In totalize, output is the input frequency divided by MULTIPLIER setting.
- 6. INPUT B jack (5327C). Works with INPUT SELECTOR switch to measure signal directly or by prescaling. Trigger level is zero volts, and the signal is not conditioned by the LEVEL, SLOPE, or ATTEN controls.
- 7. AC LINE jack. IEC type with offset pin connected to chassis.
- 8. AC LINE fuse, 1.5 Ampere slow-blow fuse for 115 V, 0.8 Ampere slow-blow fuse for 230 V operation.
- 9. 115/230 volt switch. Insert narrow screwdriver and slide the switch to show desired voltage,
- 10. DIGITAL RECORDER connector (Option 003 only). 50-pin connector for digital recorder interconnection.
- 11. REMOTE PROGRAM connector (Option 002 only), 36-pin connector to allow remote control of counter modes and functions.

Figure 3-3, Self-Check (for 5326C and 5327C)



- 1. Set SAMPLE RATE control slightly clockwise out of OFF.
- 2. Set FAST-NORM-HOLD switch to NORM.
- 3. Set FUNCTION switch to STOP.
- 4. Set MULTIPLIER selector to 1.
- 5. Set CHK-NORM switch to CHK.
- 6. Press RESET button and check that counter's right-hand column displays a 0 and all other digits are blanked.
- 7. Set FUNCTION switch to START and check that counter totalizes and the C light is on. Check that OF light goes on as display overflows.
- 8. Set FUNCTION switch to STOP. Check that the C light goes out and display is held.
- Push RESET button. Display should be zero.
- 10. Set FUNCTION switch to PERIOD AVG. Set MULTIPLIER switch (4) as shown in table below, and check for proper display.

Part of Figure 3-3. Self-Check (For 5326C/5327C) (Continued)

	DISPLAY (± 1 Count)			ANNUNCIATOR		
MULTIPLIER			327C 5326/27C		5327C	
	Λ	В	B+10	Λ	В	B÷10
1	.1	,0	,u0	μв	ha	hu
10	.10	,00,	.000	μв	hв	İΤΒ
102	.100	,000	,0000	hв	μв	ha
103	100.0	٥,	.00	ns	ne	ns
104	100.00	,00	,000	ns	ลต	ns
105	100,000	,000	,0000	ns	ทธ	ns
104	100,0000	,0000	,00000	ns	ns	ns
107 Standard	00,00000	,00000	,000000,	ns OF	ns	ns
107 Cption 001	100.00000		}	ns		
10 ⁸ Standard	0,000000	,000000	.0000	ns OF1	ns	•
10 ⁸ Option 001	00.000000			ns OF1		

Wait 10 seconds

11. Set FUNCTION switch to FREQ, set TIME BASE as shown in Table below and check for proper display.

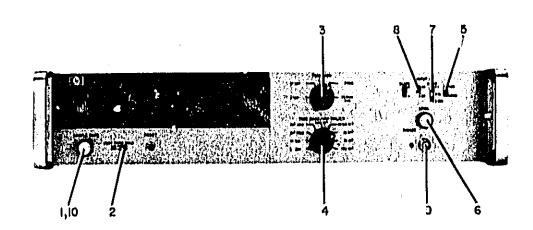
	DISPLAY (± 1 Count)			ANNUNCIATOR		
TIME BASE	5326/27C			327C 5326/27C		5327C
	A	В	B÷10	Λ	В	B÷10
,1 в	,01	,00	,0	GHz	GHz	GHz
1 8	10	0	,00	MHz	MHz	GHz
10 в	10.0	.0	0	MHz	MHz	MHz
.1 ms	10,00	,00,	.0	MHz	MHz	MHz
l ms	10.000	,000	,00	MHz	MHz	MHz
10 ms	10000,0	,0	0	kHz	kHz	kHz
18	10000.00	,00	.0	kHz	kHz	kHz
1 s Standard	0000,000	.000	.00	kHz OF	kHz	Khz
1 s Option 001	10000,000	,000	,00	kHz	kHz	kHz
10 s Standard	000,0000	.0000	.000	kHz OF	kHz	kHz
10 s Option 001	0000,0000	,0000	,000	kHz OF	kHz	kHz

Wait 10 seconds.

NOTE

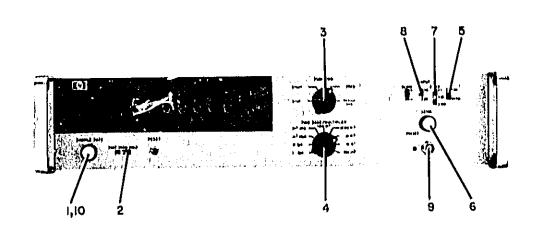
Push RESET after changing positions of INPUT SELECTOR switch.

Figure 3-4. Frequency Measurements



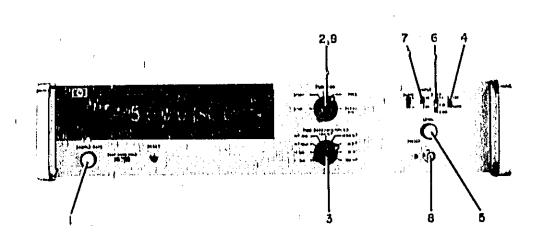
- I. Set SAMPLE RATE control slightly clockwise out of OFF.
- 2. Set FAST-NORM-HOLD switch to NORM.
- 3. Set FUNCTION switch to FREQ.
- 4. Set TIME BASE switch for desired gate time.
- 5. Set CHK-NORM switch to NORM.
- 6. Set LEVEL control to PRESET for triggering at zero volts or to desired trigger level.
- 7. Set ATTEN switch to match input signal amplitude.
- 8. Set AC-DC switch to AC or DC.
- 9. Connect input signal (0 to 50 MHz) to iNPUT A jack.
- 10. Adjust SAMPLE RATE control for convenient measurement interval. SAMPLE RATE and FAST/NORM control the delay between measurements.

Figure 3-5. Period Average Measurements



- 1. Set SAMPLE RATE control slightly clockwise out of OFF.
- 2. Set FAST-NORM-HOLD switch to NORM.
- 3. Set FUNCTION switch to PERIOD AVG.
- 4. Set MULTIPLIER to number of periods to be averaged.
- 5. Set CHK-NORM switch to NORM.
- 6. Set LEVEL control to PRESET for triggering at zero volts or to desired trigger level.
- 7. Set ATTEN switch to match input signal amplitude.
- 8. Set AC-DC switch to AC or DC.
- 9, Connect input signal (0 to 10 MHz) to INPUT A jack.
- 10. Adjust SAMPLE RATE control for convenient measurement interval.

Figure 3-6, Totalizing Measurements



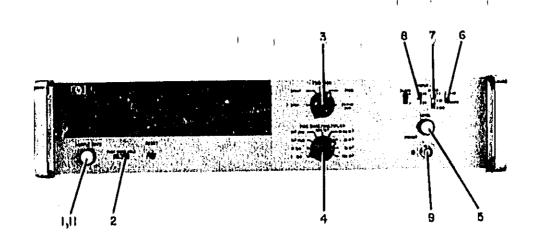
- 1. Set SAMPLE RATE control slightly clockwise out of OFF, SAMPLE RATE does not work in totalize, since the input signal controls the display time.
- 2. Set FUNCTION switch to STOP.
- 3. Set MULTIPLIER switch to input signal scaling factor. Input signal is divided by setting of switch.
- 4. Set CHK-NORM switch to NORM.
- Set LEVEL control to desired trigger level or to PRESET for triggering at zero volts.
- Set ATTEN switch to match input signal amplitude.
- 7. Set AC-DC switch as needed.
- 8. Connect input signal (0 to 10 MHz) to INPUT A jack.
- 9. Set FUNCTION switch to START. The displayed value will accumulate at a rate determined by the input signal divided by the MULTIPLIER switch setting.

NOTE

Input signal divided by MULTIPLIER switch setting is available at the rearpanel TIME BASE OUTPUT jack.

4

Figure 3-7. Ratio Measurements



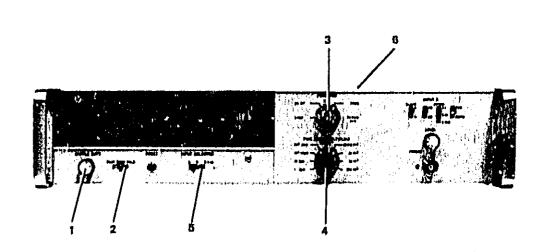
- Set SAMPLE RATE control slightly clockwise out of OFF.
- 2. Set FAST-NORM-HOLD switch to NORM.
- 3, Set FUNCTION switch to FREQ or PERIOD AVG.
- 4. Set MULTIPLIER switch to desired dividing factor.
- 5. Set INPUT A LEVEL control to desired trigger level or to PRESET for triggering at zero volts.
- 6. Set CHK-NORM switch to NORM.
- 7. Set ATTEN switch to match input amplitude.

- 8. Set AC-DC switch as needed.
- 9. Connect F_A (0 to 50 MHz) to INPUT A jack.
- 10. Set OSC INT-EXT switch (rear-panel) to EXT. Connect F_{ext} to OSC jack F_{ext} can be 100 Hz to 10 MHz at 1 V rms (min) to 5 V peak maximum.
- 11. Adjust SAMPLE RATE control for convenient measurement interval.
- 12. Ratio = $\frac{F_A}{F_{ext}}$ = $\frac{Display}{MULTIPLIER}$ for frequency measurements or

Ratio = $\frac{P_A}{P_{ext}} = \frac{F_{ext}}{F_A} = \frac{Display}{MULTIPLIER}$ for the period average mode.

Disregard units and decimal point,

Figure 3-8. Input B Measurements



(5327C only)

- Set SAMPLE RATE control slightly clockwise out of OFF.
- 2. Set FAST-NORM-HOLD switch to NORM.
- 3. Set FUNCTION switch to FREQ or PERIOD AVG.
- 4. Set TIME BASE/MULAYPLIER switch for desired resolution.
- 5. Set INPUT SELECTOR switch to B for direct counting or B+10 for prescaling by 10 of the input signal.
- 6. Connect input signal to INPUT B jack (rear panel). Input signal is dc to 50 MHz at 15 mV to 3.5 V rms for B position and 0 to 550 MHz at 25 mV to 3.5 V rms for B+10 position. Maximum input frequency for PERIOD AVG B+10 measurement is 100 MHz. Maximum input frequency for PERIOD AVG B measurement is 10 MHz.

SECTION IV

THEORY OF OPERATION

4-1, INTRODUCTION

4.2. This section discusses the general operating principles of the instrument. Assembly description is covered in more detail in Section VIII, opposite each schematic diagram. Logic fundamentals are explained in Paragraph 4.3 through 4.16.

4.3. LOGIC SYMBOLS

4-4. Two states exist in the binary system, 1 and 0. In positive logic, the 1 state is more positive than the 0 state. High (H) and Low (L) are used to represent the 1 and 0 levels. HIGH ALWAYS REPRESENTS THE MORE POSITIVE LEVEL, WHETHER IT BE POSITIVE OR NEGATIVE LOGIC.

4-5. A circle at the input line of a logic symbol indicates that a low activates the function. Figure 4-1B shows that a low at both inputs produces a high output. A circle at the output line of a logic symbol indicates a low when activated, as shown in Figure 4-1C.

4-6. Gating and Logic

L

L

L

L

4-7. Figure 4-1A represents a basic AND gate. The output is high if all inputs are high. An AND

gate may have two or more inputs. Figure 4-1D represents a basic OR gate. The OR gate output is high if one or more of its inputs is high. An OR gate may have two or more inputs. An OR gate with a circle on the output is called a NOR gate. An AND gate with a circle on the output is called A NAND gate. An EXCLUSIVE NOR (Figure 4-1E) has two inputs; and the output will be low if one, but not both, of the inputs is high. The output will be high if the inputs are both low or both high.

4-8. INTEGRATED CIRCUIT OPERATION

4-0. JK Master-Slave Flip-Flop

4-10. The JK master-slave flip-flop is basically a bistable multivibrator. With simultaneous high inputs to J and K, before the clock pulse, Q and \vec{Q} will change states after the clock pulse. Refer to Figure 4-2 and Table 4-1. This circuit triggers on the trailing edge (negative transition) of the clock pulse. The set (S) and reset (R) inputs operate as follows: when a low is applied to set input, \vec{Q} goes low and \vec{Q} goes high; when a low is applied to reset input \vec{Q} goes low and \vec{Q} goes high. Set or reset can override all other inputs at any time.

H

L

L

E C В EXCLUSIVE NOR AND INVERTED INPUT INVERT_D OUTPUT ٥h G Н X = Ā, Ē X . A.B X + A·B X = A · B X = Ā+B X · Ā+B $X = \overline{A + B}$ X * A+B χ Α В X A В X X A 8 Α B L Н н Н Н Н H H H н н L н H L H Н L L H L L H H L Ħ L L L Н H L H L

Figure 4-1. Gate Symbols and Ligic Comparisons

н

Figure 4-2, JK Flip-Flop

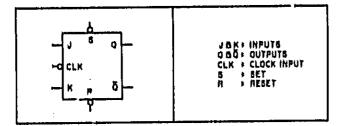


Table 4-1. Truth Table

t	t _n		+ 1	t _n = Before clock pulse	
.J	K	'ର ଦି		TN + 1 = After clock pulse	
L	L	G ^E	Q _n	If J = L and K = L, then Q and Q will not change from what they were before the clock pulse.	
Н	Ļ	H	H	If J = H and K = L, then Q will be H and Q will be L after the clock pulse.	
L	H	L	H	If J = L and K = H, then Q will be L and Q will be H after a clock pulse.	
H	H	Q _n	Q _n	If $J = H$ and $K = H$ before the clock pulse, then after the clock pulse Q and \overline{Q} will change states.	

4-11. Time-Base Decade

4-12. In the reset state, Carry Output (CO) (see Figure 4-3) is high and, if the Gate input (G) is low, Gated Output (GO) is low. Ten pulses on the Gate input produce a negative transition at the Gated Output. If the G input is high, GO is open-circuited, regardless of the count. The Carry Output gives a positive transition after 10 pulses.

4-13. Open-Collector Gate

4-14. The output of an open-collector gate can be paralleled with gates of the same type to perform a wire-OR function, as shown in Figure 4-4. When the outputs are tied to the same line, any one of the gates can pull the line low without damaging itself.

Figure 4-3. Time-Base Decade 1820-0412

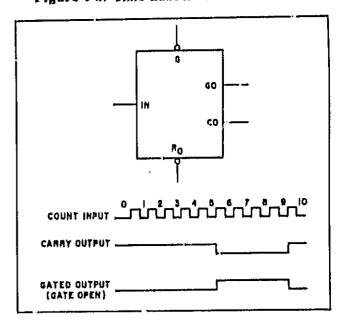
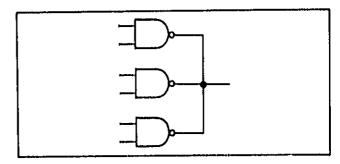


Figure 4-4. Open-Collector Gate 1820-9327



4-15, Logic Levels

4-16. This counter uses three types of logic: TTL (transistor-transistor logic), ECL (emitter-coupled logic), and DTL (diode-transistor logic). See Table 4-2 for specific logic levels.

Table 4-2. Logic Levels

Туре	H (Min)	L (Max)	Trigger	Supply
ECL	7 V	-1.4 V	-1.2 V	-5,0 V
TTL	2.4 V	0.4 V	1.5 V	5,0 V
DTL	2.6 V	0.4 V	1.5 V	5,0 V

4-17. OVERALL COUNTER OPERATION

4-18. The signal connected to INPUT A is conditioned by the front-panel switches of the Attenuator Assembly. These switches set the operating conditions for trigger level, coupling, and the required slope. The Input Amplifier converts the signal

into narrow pulses for more efficient usage throughout the counter (Figure 4-5). In the 5327G, INPUT B provides an alternate path through the Prescaler Assembly, which divides the signal by 10 or passes it directly to the Function Control. The path taken is determined by the setting of the front panel INPUT SELECTOR switch.

4-19. The Function Control accepts both the input signal and the 10 MHz internal oscillator pulses and routes them in accordance with the mode of operation being used. One of these signals is sent to the Time Base Assembly, which divides the signal as determined by the front panel TIME BASE/MULTIPLIER switch. The first and last pulse of the divided signal controls the length of time the main gate is open. During this time, the other signal is sent directly to the main gate for totalizing in the decade counters and is subsequently displayed. The synchronizer prevents the main gate from opening unless an input signal is present.

4-20. The sample rate circuits control the interval between measurements. When the main gate closes, these circuits provide a delay, as controlled by the front panel SAMPLE RATE controls. When the sample rate period has clapsed, a reset pulse is generated to reset the counter and start a new measurement.

4-21. The signal to be counted, either the internal oscillator or input signal, passes through the main gate to the decade counters. The buffer storage registers store the BCD count before it is translated into a decimal equivalent and displayed on the front panel. Also displayed on the front panel are the units of measurement and the decimal point. The left and right readout assemblies contain the unit indicators and the logic necessary to position the decimal point.

PRESCALER B> TRIGGER LAMP FROM FUNCTION 5W RIGHT INPUT SYNCHRONIZER **ATTENUATOR** FROM AMP TIME BASE/ MULT SW BUFFER DECADE MAIN FUNCTION 05C GATE COUNTERS STORAGE CONTROL SAMPLE RATE SAMPLE DECODER TIME RATE CIRCUITS BASE DIVIDERS FROM FUNCTION 5W LEFT READOUT DISPLAY FROM TIME BASE MULT SW

Figure 4-5, Functional Block Diagram

4-22. Frequency Mode

4-23. Frequency is defined as the number of periodic events per unit of time. The counter, therefore, measures an unknown signal (COUNT) for a known length of time (Figure 4-6). The 10 MHz internal oscillator provides the known time and controls the opening of the main gate. The Time Base Assembly divides the oscillator frequency by powers of 10 to open the main gate from 10⁻⁷ seconds to 10 seconds. The longer the gate is open, the more pulses of the unknown frequency are counted and, therefore, the better the resolution and accuracy.

4-24, Period Average Mode

4.25. The MULTIPLIER switch selects the number of periods to be averaged (Figure 4-7). The Time Base Assembly counts the number of periods selected with the switch and holds the main gate

open until this count is complete. The Decade Counter totalizes the oscillator pulses while the main gate is open.

4-26, Totalize Mode

4.27. The main gate can be manually held open with the front-panel START/STOP positions of the FUNCTION switch. With the switch in the START position (Figure 4.8), the counter totalizes the number of times the signal passes through the trigger point. In the case of a typical frequency measurement, this would be a totalizing of the periods. Before the signal is counted, it is divided in the Time Base Assembly by the setting of the MULTIPLIER switch.

4-28. Instrument Timing

4-29. In addition to the timing diagrams relating directly to the individual measurements, Figure 4-9 shows other counter pulses necessary for operation.

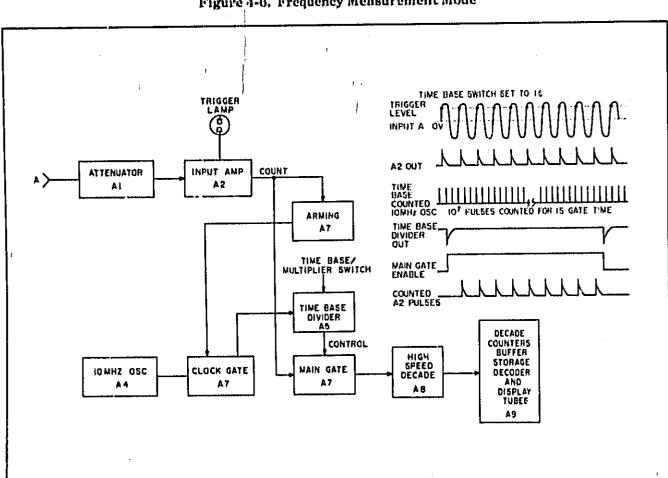


Figure 4-6. Frequency Mensurement Mode

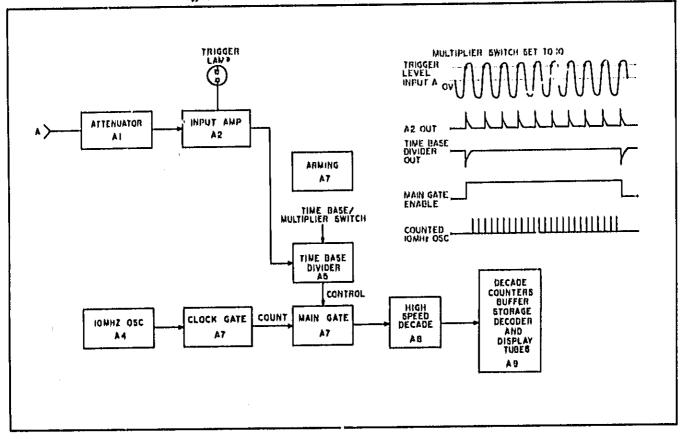
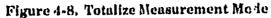


Figure 4-7. Period Average Measurement Mode



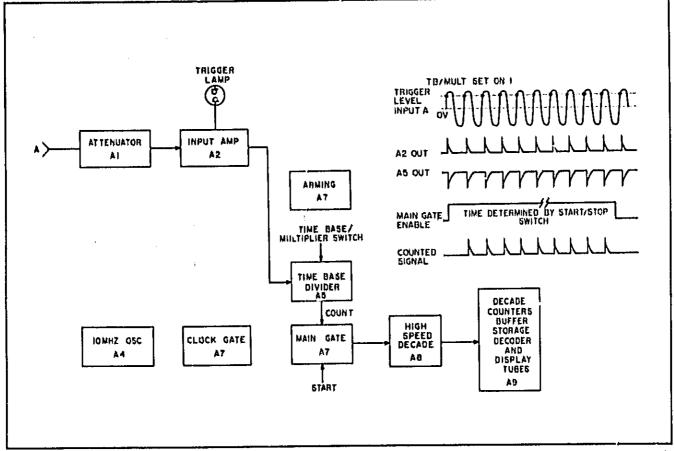
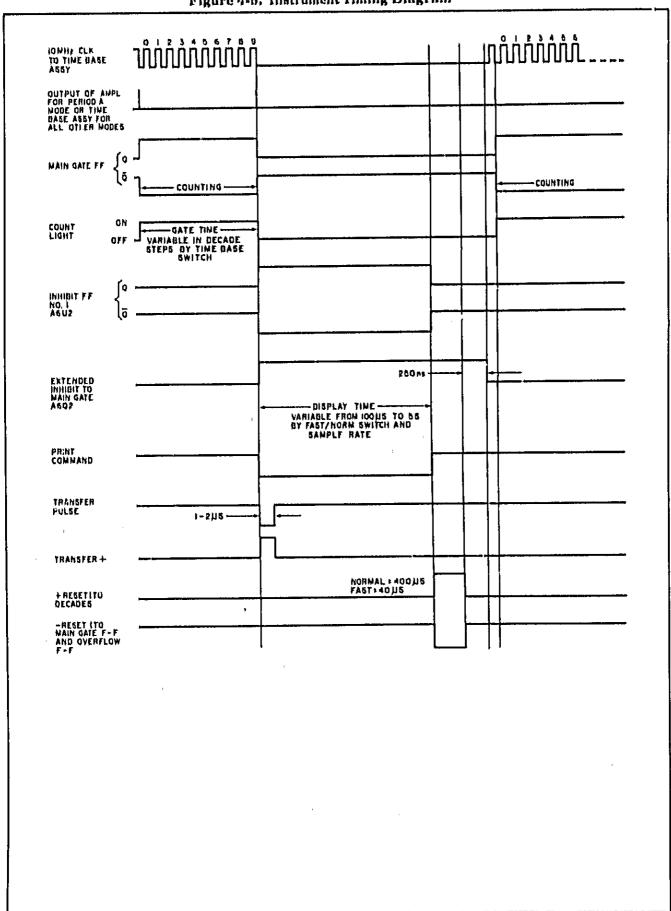


Figure 4-9. Instrument Timing Diagram



SECTION V

MAINTENANCE

5-1. INTRODUCTION

5-2. This section gives maintenance and service information. Included is a table of typical problems for trouble isolation, recommended test equipment, in-cabinet performance checks, which may be used to verify proper counter operation, and adjustment procedures.

5-3. ASSEMBLY DESIGNATIONS

6-4. Table 5-1 lists the designation, name, and Hewlett-Packard part number of assemblies used in this instrument.

Table 5-1. Assembly Identification

ABSY	NAME	HP PART NO.
Al	Attenuator	05326-60048
A2	Input Amplifier	05326-60004
- A3	Not Assigned	
A4	Oscillator	05326-60002
A 5	Time onse Control	05326-60005
A6	Sample Rate	05326-60013
A7	Function Control	05326-60045
A8 A9 A9	Display Support Display Display (Option 001)	05326-60009 05326-60008 05326-60025
A9 A10	Right Readout	05326-60036
All	Left Rendout	05326-60035
A12	Not Assigned	
A13	Not Assigned	
A14	Not Assigned	1
A15	Regulator	05327-60020
A16	Interconnect	05327-60028
A17	Not Assigned	
A18	Prescaler (5327C)	05327-60053

5-5. TEST EQUIPMENT

5-6. Test equipment recommended for maintaining and checking performance is listed in Table 5-2. Test equipment having equivalent characteristics may be substituted for the equipment listed.

Table 5-2. Recommended Test Equipment

Thing o'z, re	acommenden verk	
Instrument Type	Required Characteristics	Recommended Type
Frequency Standard	1 MHz output	HP 107A
Oncilloncope	50 MHz Bandwith	HP 180A
Vertical Plug-in	50 mV/cm Sensitivity	HP 1801A
Time Base Plug-in	50 MHz Bandwidth	HP 1820A
Trut Oscillator (2 required)	10 Hz to 10 MHz at 5 volta p·p	H2 651B
Audio Oscillator	2 Hz to 100 kHz 100 mV rms	HP 202C
HF Signal Generator	50 kHz to 50 MHz at 3 volts rms	HP 606B
VHF Signal Generator	10 to 480 MHz	HP 608F
Frequency Doubler	240 - 550 MHz	HP 10515/
Pulse Generator	10 MHz rep-rate 8 nsec pulse width 0,3 volts p-p outpu	HP 216A
Electronic Counter	.1 Hz to 10 MHz Frequency Measurements	HP 5245L
Variable Line Transformer	103 to 127 volts rms and 206 to 254 volts rms	Superior Electronic Power Stat 3PF116 (115 V) 3PF 216 (230 V
Digital Recorder	Print Rate: 10 lines/sec, Data Input: +8421 BCI parallel entry, accepts 1 = +5 V, 0 = +0.25 V. Accepts negative going +5 to 0V print command	HP 5055A
DC Voltmeter	0 to 200 Vdc, 1% Accuracy	111 4121
AC VTVM	0 to 250 Vac	HP 400F
RF Voltmeter	1 mV to 3 V	HP 3406A

5-7. IN-CABINET PERFORMANCE CHECK

- 5-8. GENERAL. The performance check, Table 5-3, and test card can be used to verify proper operation of all circuits in the counter and may also be used as follows:
- a. As part of an incoming inspection check of instrument specification.
- b. Pandically, for instruments used in systems where maximum reliability is important.
- c. As part of a precedure to locate defective circuits,
- d. After any repair or adjustment and before returning instrument to regular service.
- e. As a permanent record of instrument maintenance performed, because the test record page is perforated and may be removed.
- 5.9. VARIABLE LINE VOLTAGE. During the test (Table 5.3), the counter should be connected to a variable voltage source, so the line voltage may be varied ±10% from nominal (115 or 230 Vac.

5-10, TROUBLE ISOLATION

- 5-11. The trouble isolation table (Table 5-5) lists the common problems a counter might develop. Before beginning the overall troubleshooting, a quick check of the power supply voltages and the oscillator signal may save time. These test points are available on the main interconnect board—remove bottom cover.
- 5-12. The table is divided into three sections, under the headings of count problems, display problems, and input problems. The following is an explanation of table usage with problem 2 of "Input Problems" as the example. The Symptom column identifies the specific counter problem, while the initial conditions column calls out the counter's switch settings or specifies a mode or modes of operation. The test point column asks that A2P1(C) be checked (Test 1) for an ECL High. Do not proceed to Test 2 until the check is evaluated. If A2P1(C) is High (Test is Positive), perform Test Point 2. If A2P1(C) is Low (Test is Negative), the problem can be either a or b; check in order listed.

5-18. REPAIR

5-14, Printed Circuit Component Replacement

- 5-15. Component lead holes in the circuit boards have plated walls to ensure good electrical contact between conductors on opposite sides of the board. To prevent damage to this plating and the replacement component, apply heat sparingly, and work carefully. The following replacement procedure is recommended:
 - a. Remove defective component.
- b. Melt solder in component-lead holes. Use clean dry soldering iron to remove excess solder. Clean holes with a wooden toothpick or splinter. Do not use metal tool for cleaning as this may damage through-hole plating.
- e. Bend leads of replacement component to the correct shape and insert into component-lead holes. Using heat and solder sparingly, solder leads in place, Heat may be applied to either side of the board, but do not apply excess pressure with soldering iron.
- d. Through-hole plating breaks are indicated by separation of the round conductor pad from either side of the hoard. To repair breaks, press conductor pad against board and solder replacement component lead to conductor pad on both sides of board.

5-16. Replacing Integrated Circuits

- 5-17. Following are two recommended methods of replacing integrated circuits:
- a. SOLDER GOBBLER. This is the best method. Solder is removed from the board by a hollow tip soldering iron connected to a vacuum source. The IC is removed intact, so it may be re-installed if found to be operative.
- b, CLIP OUT. This method should be used as a last resort only. Clip the leads as close to the case as possible. With a soldering iron and longnose pliers, carefully remove the wires from each hole. Clean holes as described in Paragraph 5-15b.

1. TIME BASE STABILITY AND OUTPUT

n. Bet Counter controls as follows:

SAMPLE RATE	. Mid-position
FAST/NORM/HOLD	
FUNCTION	FREQ
TIME BASE/MULTIPLIER	
SLOPE	**********
AC/DC	
ATTEN	.,,,,,X1
CHK-NORM	, NORM
LEVEL	PRESET
STORAGE	ON
OSC ,,,,,,,,	INT

NOTE

Allow 1-hour warm-up before proceeding to Step b.

- b. Connect 1 MHz frequency standard to INPUT A.
- c, A counter display of 000,0000 (1000,0000 Option 001) indicates that counter time-base frequency is exactly 10 MHz. The affect between counter time base and 1 MHz frequency standard can be determined by subtracting 10 MHz from the indicated oscillator frequency.

COUNTER DISPLAY	A4 OSCILLATOR FREQUEN	
999,9950 kHz	10 000 050 Hz	
999,9960 kHz	10 000 040 Hz	
999,9970 kHz	10 000 030 Hz	
699.9980 kHz	10 000 020 Hz	
999,9690 kHz	10 000 010 Hz	
1 000,0000 kHz	10 000 000 Hz	
1 000,0010 kHz	9 999 860 Hz	
1 000,0020 kHz	9 999 980 112	
1 000,0030 kHz	9 999 970 Hz	
1 000,0040 kHz	9 999 960 Hz	
1 000,0050 kHz	9 999 950 Hz	

- d. Record frequency offset on test card. For long-term stability, operate the counter continuously for at least 1-month. Measure frequency offset at 1-month intervals.
- e. To calibrate the counter's time base to the frequency standard, perform time-base adjustment in Table 5-4.

NOTE

Temperature must be held constant or compensation for temperature difference must be made whenever a frequency difference is recorded. Unless a record of the temperature and date of last calibration is available, the frequency offset should not be considered drift or aging rate of the 10 MHz crystal.

Table 5-3. In-Cabinet Performance Check (Continued)

- f. To check time-base stability vs. line voltage variations, connect variable transformer to counter power cord. Vary line voltage ±10% and record frequency difference on test card. Should be ≤1 part in 107.
- g. To check time-base stability vs. temperature, vary counter operating temperature between 0°C and 50°C. Record frequency on test card. Should be ≤2.5 parts in 10°.

- .h. Connect oscilloscope vertical input to OSC jack on counter's rear panel. Use 10:1 probe at OSC jack to reduce load.
- i. Oscilloscope should display 10 MHz waveform nominal at >2.4 volts peak-to-peak amplitude. Record on test card.

2. DISPLAY, DECIMAL POINTS, AND DIVIDERS

Proper operation is verified in the Self-Check procedures in Figure 3-3. Record on test card,

3. FREQUENCY RESPONSE AND SENSITIVITY

- a. Set counter controls as in 1a, except TIME BASE to 1s and AC/DC switch to AC,
- b, Connect a BNC T connector to INPUT A jack. Connect sine wave test oscillator output to T connector. Connect oscilloscope's vertical input to T connector to monitor input signal amplitude; use a 50-ohm feedthrough at oscilloscope BNC.
- c. Adjust test oscillator from 20 Hz to 50 MHz, maintaining 100 mV rms input amplitude. Counter should properly display all frequencies in this range. Record on test card.
- d. Set audio oscillator frequency to 2 Hz. Counter should not count. Switch AC/DC switch to DC. Counter should count input signal.
- e. Connect a cable (using a clip-lead) to A16P1A(8) and connect other end to Z axis of oscilloscope.
- f. Adjust test oscillator output for 1,000 Hz at 8 volts peak-to-peak.
- E. Set LEVEL to PRESET and check that oscilloscope marker is at 0 volts.
- h. Set SLOPE ic ... Vary LEVEL control and check that marker is variable over at least 3.0 to +3.0 volts on the positive slope of waveform.
- i. Set SLOPE to . Vary LEVEL control and ca. 7k that marker is variable over at least 3,0 to +3,0 volts on the negative slope of waveform. Record on test card.

4. PULSE OPERATION

a. Set counter controls as follows:

FUNCTION	FREQ
TIME BASE	,,,,,, 0.1 в
SLOPE	,,,,,,,,,,
AC/DC	, AG
ATTEN	. , , , , , , , , X1
LEVEL	, PRESET
CHK-NORM	, NORM
STORAGE	ON
OSC	INT

Tuble 5-3, In-Cabinet Performance Check (Continued)

- b. Connect BNC T connector to INPUT A. Connect pulse generator to T. Connect oscilloscope to T connector, using 50-ohm feedthrough at the oscilloscope BNC.
- c. Adjust pulse generator output for 10 MHz repetition rate, 15 ns pulse width at 0.3 volts peak-to-peak indication on oscilloscope.
- d. Check that counter displays the repetition rate, count light flashes, and trigger lamp is on. Record on test card.
- e. Repent above check for 10 kHz. Record on test card.

5. PERIOD AVERAGE

- n. Set counter controls as in step 1a with cUNCTION to PERIOD AVG and MULTIPLIER to 1.
- b. Connect test oscillator to INPUT A, using BNC T. Connect oscilloscope to T, using 50-ohm feedthrough at oscilloscope BNC.
- c. Set test oscillator to 2 Hz at 100 mV rms. Counter should display the period of the input signal (approximately 0.5 sec.). Record on test card.
- d. Vary test oscillator frequency from 2 Hz to 10 MHz, maintaining 100 mV rms input amplitude. Change MULTIPLIER switch as needed to maintain meaningful display with change of frequency. Counter should properly display the period of the frequencies in this range within accuracy specifications of the instrument. Record on test card.

6. TOTALIZE

n. Set counter controls as follows:

FUNCTION			,,	F)		+ 1	. ,	 	,			• •	, ,	,	S	ľ۸	R	r
MULTIPLIE	R	٠,		,	 		, ,			٠,	٠	• •	•	, ,		••		1
CHK-NORM.								 								C	HI	<

- b. Check that display totalizes, count light (C) is on, and trigger lamp lights. Record on test card.
- c. Using 10:1 divider probe, connect oscilloscope vertical input to TIME BASE OUTPUT jack on counter rear panel.
- d. Check that oscilloscope indicates 10 MHz negative going pulses at least 3 volts peak-to-peak, typically >30 nsec wide at 50% points. Set MULTIPLIER to 10 and observe output pulses typically 100 ns wide. The frequency output is 1 MHz.
- e. Disconnect oscilloscope from TIME BASE OUTPUT jack and connect TIME BASE OUTPUT to 5245L Electronic Counter input. Set 5245L for frequency measurements.

Table 5-3. In-Cabinet Performance Check (Continued)

f. Set TIME BASE/MULTIPLIER as follows and check for proper counter display. Record on test card.

TIME BASE/MULTIPLIER	5245 DISPLAY
1	10 MHz
10	1 MHz
102	100 kHz
103	10 kHz
104	1 kHz
105	100 Hz
104	10 Hz
107	1 Hz
104	,1 Hz

7. RATIO

n. Set counter controls as follows:

FUNCTION FREQ
MOLTIPLIER 10'
SLOPE +
AG/DC AC
APTENXI
CHK-NORM NORM
LEVEL PRESET
OSC (rear panel) EXT

- b. Connect test oscillator to OSC jack, using BNC T. Connect oscilloscope to T connector, using 50-ohm feedthrough at oscilloscope BNC connector. Set oscillator output for 10 MHz at 2.8 volts peak-to-peak, as observed on oscilloscope.
- c. Connect a second BNC T connector to counter INPUT A jack. Connect second test oscillator to T connect ir. Connect B channel of oscilloscope to front-panel T connector, using 50-ohm feed through at oscilloscope BNC connector. Set variable oscillator for 100 kHz at 100 mV rms display on oscilloscope.
- d. Check that counter displays 100. Disregard units and decimal point readouts. Record on test card.
- e. Repeat test using 100 Hz into OSC jack and 100 kHz into INPUT A. Set TIME BASE/MULTIPLIER to 103. Display should be ratio of two input frequencies x 103, approximately 106. Disregard units and decimal point. Record on test card.

8. GATE OUTPUT AND SAMPLE RATE

- a. Disconnect setup.
- b. Set counter controls as follows:

FUNCTION	FREQ
TIME BASE ,	
CHK-NORM	CHK
FAST/NORM/HOLD	FAST
SAMPLE RATE	

Table 5-3, In-Cabinet Performance Check (Continued)

- e. Using 10:1 divider probe, connect oscilloscope vertical input to GATE output and observe positive pulses≥2.4 V with a pulse width of <100 µs. Record on test card.
- d. Slowly rotate SAMPLE RATE clockwise and observe that the pulse width increases.
- e. Set the TIME BASE switch to 10 ms and rotate the SAMPLE RATE fully clockwise. Observe that the pulse width is >20 ms. Record on test card.
- f. Set FAST/NORM/HOLD to NORM and turn SAMPLE RATE fully counterclockwise, just out of OFF. Observe the positive pulse width is <20 ms. Record on test card.
- g. Slowly rotate SAMPLE RATE clockwise, observing an increase in the pulse width.
- h. Set TIME BASE to 1 sec and rotate SAMPLE RATE fully clockwise. Verify that the time between flushes of the gate (%) later is greater than 5 seconds. Record on test card.
- i. Set FUNCTION to START and check that gate output is TTL Low (<0.4 V).
- Set FUNCTION to STOF and verify that gate output is TTL High (>2.4 V).
- 9. DIGITAL RECORDER (Option 003)
 - n. Set counter controls as follows:

RUNCTION		**********	.,,, FREQ
MINAR DARK	, , , , , , , , , , , ,		1 SEC
TIME DAME	* * * * * * * * * *		CHK
CHR-NORM	111111111		1110 (1111) 1110/11
FAST/NOR	M/HOLD		TATALON IN
SAMPLE RA	at	****	Mid-Position

- b. Connect oscilloscope to J9(48). Observe oscilloscope display a print command (drop from >2.4 to <0.4 V) immediately after the C lamp goes out.
- c. Connect jumper from J9(25) to J9(22).
- d. Check that counter main gate is inhibited. C light does not flash, and no print command pulses are generated.
- 'e. Verify proper output by connecting a 5055A printer or interrogating lines with logic probe or voltmeter and performing display tube self-check in Section III.

10. PRESCALER (5327C ONLY)

Frequency Response and Sensitivity

a. Set counter controls as follows:

FUNCTION	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	FREQ
TIME BASE	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	,,,18
INPUT SELECTOR	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	B+10

Table 5-3, .n-Cubinet Performance Check (Continued)

- Use the set of frequency generators (Table 5-2) necessary to cover the input frequency from 2 Hz to 550 MHz, while maintaining 25 mV rms input level. Adjust TIME BASE switch as necessary for best display.
- e. Check for stable count within stability of oscillator.
- d. Bet INPUT BELECTOR switch to B.
- e. Use the set of frequency generators necessary to cover the input frequency from 2 Hz to 50 MHz, while maintaining 15 mV rms input level. Adjust TIME BASE switch as necessary for best display.
- f. Check for stable count within stability of oscillator.

Pulse Operation

- n. Set pulse generator output to 10 MHz repetition rate, 10 ns wide pulses at 300 mV peak-to-peak. Connect pulse generator to INPUT B jack.
- b. Check that counter displays repetition rate and that count light flushes.
- c. Repent above check at 10 kHz.

PERFORMANCE CHECK TEST CARD

Hewlett-Packard Model 53260/53270 Frequency Counter	Test Performed by
Serial No.	Date
DESCRIPTION	онеск
1, TIME BASE STABILITY AND OUTPUT	
n. Oscillator	Counter displays 000,0000
b. Line voltage variations of ±10%	Frequency change is ±1 part in 107
c. Temperature variations between 0°C and 50°C	Frequency change ≤ 2,5 parts in 10 ⁸
d. Output at OSC jack	10 MHz at>2.4 V
2. DISPLAY, DECIMAL POINTS, AND DIVIDERS	Operates in accordance with Table 3-3 procedures
B. FREQUENCY RESPONSE AND SENSITIVITY	
n. Test oscillator varies from 20 Hz	Counter displays all frequencies in this range
b. Trigger level	Trigger level is variable from +3 V to -3 V on + and - slopes
4, PULSE OPERATION	
a. Input at 10 MHz, 15 ns pulse width and 5,3 V peak-to-peak	Counter displays repetition rate, gate light mashes, and trigger lamp is on
b. Input at 10 kHz, 15 ns pulse width and 0.3 V peak-to-peak	Counter displays repetition rate, gate light flashes, and trigger lamp is on
5. PERIOD AVERAGE	
n. Test oscillator at 2 Hz, 100 mv	Counter displays period of input signal (0,5 s)
b. Vary test oscillator from 2 Hz to 10 MHz at 100 mV	Counter displays periods of frequencies in this range
6. TOTALIZE	
a. Totalizing internal clock pulses	Display totalizes, gate light flashes, and trigger lamp lights
b. TIME BASE OUTPUT Jack	———— Scales output signal with setting of MULTIPLIER switch
7, RATIO	
a. 100 kHz on INPUT A, 10 MHz on OSC jack	Counter displays 100
b. 100 kHz on INPUT A, 100 Hz on OSC juck	Counter displays 10s

PERFORMANCE CHECK TEST CARD

Hewlett-Packard Model 5326C/5327C Frequency Counter	Test Performed by
Berial No.	Date
DESCRIPTION	CHECK
8, GATE OUTPUT AND SAMPLE RATE	
n. SAMPLE RATE switch to FAST, control to max, cew	Pulse width is <100 μs at≥2.4 V
b. SAMPLE RATE control max cw	Pulse width is >20 ms
c. SAMPLE RATE switch to NORM, control to mux cew	Pulya width is < 20 ms
d. SAMPLE RATE control max, cw	Display time is>5 s
9. DIGITAL RECORDER	Printed output agrees with counter display
10. PRESCALER (5327C ONLY)	
Frequency Response and Sensitivity	And the second
Pulse Operation	p er - 20, 10, 10, 10,

Table 5-4. Adjustments

1. POWER SUPPLY A15

- Connect counter line cord to variable power transformer. Monitor output voltage with AC VTVM. Adjust transformer for 115 volt indication on VTVM.
- b. Turn counter SAMPLE RATE control clockwise out of OFF.
- e. Connect VTVM to A15 Pin 7 and adjust A15R10 for +18,5 volts.
- d. Connect VTVM to A15 Pin 6 and adjust A15R13 for -16.5 volts.

2. BENSITIVITY AND OFFSET A2

- a. Connect a BNCT connector to INPUT A jack.
- b. Connect test oscillator output to T connector.
- c. Connect oscilloscope vertical input to T connector, using 50-ohm feedthrough at oscilloscope input BNC.
- d. Using a clip lead, connect A16P1A(8) output to oscilloscope Z-axis input,
- e. Adjust test oscillator for 1 kHz output at 100 mV rms.
- f. Bet counter controls as follows:

FUNCTION		,,,,,,,,,,,,,,,,	FREQ
CHKNORM		,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	NORM
AUVITEN		,,,,,,,,,,,,,,,,,	XI
AC.DO	**********	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	pc
ANDO 1111	.,,,,,,,,,,,,,	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	ppregr
LEVEL			t tetréries r

- g. Set SLOPE switch to . and + positions and observe marker position on oscilloscops waveform.
- h. On Input Amplifier board A2, adjust A2R2 until + and marker positions have a symmetrical offset about the zero volt axis for + and slope switch positions.

B. OSCILLATOR A4

- a. Connect I MIIz frequency standard to INPUT A jack.
- b. Bet counter controls as follows:

CHK-NORM		10RM
FUNCTION	********	FREQ
TIME BASE		., 10 в
SAMPLE RA	TE alightly clockwise out o	COFF

- c. Remove top cover.
- d. Using insulated tuning tool, adjust A4C3 until display indicates all zeros with cover on. (Wait 10 seconds between adjustments for counter to make measurement.)

NOTE

On standard instruments without Option 001, the counter display will overflow; however, all digits are valid.

Table 5-4. Adjustments (Continued)

4. PRESCALER ADJUSTMENTS (5327C ONLY)

n. Set counter controls as follows:

FUNCTION						, ,	• 1	 ,			,	, ,	 ٠	,		,]	FREQ
TIME BASE					,		·	 ,		. ,			 ٠		٠	,	,		а1,0 ,
INPUT SELI	ĽĊ	T'()1	1					٠			ı.	 ,	,	ŀ	٠			B+10

- b. With no input signal applied, adjust R3 offset pot for 0 V on U2 pin 4.
- c. Adjust R10 bins pot for 0.65 ±.05 V on U2 pin 3.
- d. Adjust R27 bins pot for 0.0 ±.05 V on U3 pin 3.
- e. Check that the previously adjusted voltage on U2 pin 3 's correct. If voltage has shifted, adjust R10 for proper reading and recheck U3 pin 3.
- f. Set HP VHF Signal Generator and doubler for 550 MHz at 1 V rms. Measure the output with an HP 3406A RF Voltmeter using a 50t1 termination 2t the probe. Connect signal source to INPUT B of counter.
- .t. Reduce output level until counter's display becomes unstable. Adjust R3 for a stable display. Repeat this procedure until unable to obtain a stable reading. Increase signal level until display just becomes stable.
- h. Diconnect input and connect to voltmeter; reading should be 25 mV or less. Check other frequencies with the band.
- i. Set INPUT SELL CTOR switch to B.
- j. Set input signal to 50 MHz at 15 mV and connect to INPUT B. Counter should display 50 MHz.

Table 5-5. Trouble Isolation

symptom .		missons brossins	EVA	LUATION
COUNT PROBLEMS	INITIAL CONDITIONS	TEST POINT	TEST IS POSITIVE	TEST IS NEGATIVE
I, COUNTER DOES NOT COUNT — HAS TRIGGER LIGHT BUT NO	For problem in <u>one</u> function only.	Check for a TTL low A7: Pin 1 for FREQ Pin 2 for PERIOD AVG Pin 4 for START	a. No input signal to A7. b, Problem is on A7.	Switch circuitry or wiring for specific mode is faulty
COUNT LIGHT (C)	For problem in <u>all</u> function modes. (Use Fast Gate Time when	1. Check for oscil- intor at A7(T).	Perform Step 2 of Test Point.	Problem is in oscil- Intor circuit of A4 or A5
	checking OTB signal). Start position may have count light.	2, Check for ITB signal at A7(U).	Perform Step 3 of Test Point.	a. MAIN GATE IN- HIBIT line is staying High, (Check A6 Inhibit circuits & A7U7A. b. No input signal to A7. c. A11(1) is High (also check A16CR1). d. Problem is on A7.
		3, Check for OTB signal at A7(V).	Problem is on A7,	n. A5 GATE ENABLE line is High. b. Problem is on A5.
2. COUNTER DOES NOT COUNT —	Use an input signal with CHK/NORM	1, Check A2P2(5 & 6) for input signal.	Perform Step 2 of Test Point.	Problem is on AL
NO TRIGGER LIGHT NO COUNT LIGHT (C)	switch set to NORM	2. Check for ECL Low nt A2P1(4,D).	Problem is on A2.	A7 OSC CH circuit is bad.
3, COUNTER DOES NOT COUNT — HAS TRIGGER LIGHT HAS COUNT LIGHT (C)		1, Check for counted signal at ABJ1(5).	a. No -Transfer sig- nal at A8P1(A), b. Problem is on A9,	a. A8P1(B) is always High. b. No input signal at A8P1(2). c. Problem is on A8.
4. INACCURACY IN COUNT	Place FUNCTION to START, connect known 1 MHz frequency	1. Check for exact oscillator frequency at A4(1,A)	Perform Step 2 of Check Point,	Frequency must be adjusted for exactly 10 MHz.
	standard,	2, Check TIME BASE output jack (using another counter) for correct A5 scaling of oscillator frequency,	Problem is on A8 or A9.	Problem is on A5.
5. COUNTER TOTALIZES ALL MEASUREMENTS			No reset signsl from A6(16).	
6, ONE DIGIT DOES NOT COUNT	Set counter display so that tube would normally count (use START/STOP).	Check A9 Circuitry for that position.		

Tuble 5-5, Trouble Isolation (Continued)

вумічом		missem izezzkim	EVA	NOITAUL
Diblina Avaluation	Initial conditions	TEST POINT	TEST IS POSITIVE	TEST IS NEGATIVE
, nixies do pot Light		Check A15 for +5 V, -5 V, and +175 V.	a, Problem on AB or A0, b, A8 or A0 has bad connection.	n, Power not con- nected to counter, b, Blown line fuse, e, A15 or A16 power supp'y circuits are bad,
, one nixie does Not light	Set counter display so that tube would hormally be lit.		Problem is on Att.	
B. UNLIT PORTION OF DISPLAYED PIGIT (HOLES)	,		Repiace display tube.	
4, NIXIE DISPLAYS WRONG DIGIT	Set display on incor- rect digit, using START/STOP switch,	Check A9 codes for that position,		
5, ONE DECIMAL POINT DOES NOT LIGHT	Set up combination of FUNCTION/TIME BASE switches,	Check for Low on DP line of A11,	Problem is on AB or A0.	n, FUNCTION and/or TIME BASE lines are not pulled Low for A11. b, A11 circuitry is bad.
6, DECIMAL POINTS DO NOT LIGHT	For problem in Frequency Mode only, FUNCTION switch to FREQ	Check for Low at A11: Pin 3 for FREQ A or B Pin C for FREQ B÷:0	Severnl IC's on All are bad	a. A10 input lines for FREQ are High. (One or more; A10(B) should be Low, A10(1) may be low, .or A10(2) may be Low.) b. A10 circuitry is bad
	For problem in Period Average Mode only, FUNCTION switch to PERIOD AVG.	Check for Low at All; Pin 4 for Per Avg A ,or B Pin D for Per Avg B+10	Several IC's on A11 are bad,	n. A10 input lines for Per Avg are High. (One or more; A10(A) should be Low, A10(1) may be Low, or A10(2) may be Low.) b. A10 circuitry is bad
, , , , , , , , , , , , , , , , , , ,	For both Freq and Period Avg Modes (INPUT SELECTOR tyed in different positions)	Check for Low on DP line at ABP1	A8 "Decimal Point Drivers" circuit is bad in common point.	n. A11(2) is High. b. A11(B) is High. c A16CR2, 3 are bad.
7. ANNUNCIATOR DOES NOT LIGHT	Set front punel controls for problem conditions.	For right readout, Check A10 for Low on selected function and time base lines.	Problem is on A10	Switch circuitry for line with High level is bad.
· · · · · · · · · · · · · · · · · · ·		For left readout, check for Low on A11 input line, (Overflow, C light, or EXT). Also, see "Count Problems."	Problem is on A11.	COUNT LIGHT-proble is on A5. OVERFLOW-problen is on A9. EXT-problem is in Option 002 wiring or pressure connector

Table 5-5, Trouble isolation (Continued)

BYMPTOM DISPLAY PROBLEMS	initial conditions	test point	IAVE TEST IS POSITIVE	NOTTAU TEST IS NEGATIVE
B, DISPLAY DOES NOT STORE COUNT		Check STORAGE switch (rear panel) — must be on.	A6(K) is always Low.	Place switch to ON position.
0, COUNT LIGHT (C) DOES NOT LIGHT AT FAST GATE TIMES			Gate lamp one-shot on A5 is had,	
INPUT PROBLEMS				
1. COUNTER MAKES MEASUREMENT HAS NO TRIGGER LIGHT		Check for trigger light pulses at A2P2(1)	Problem is on Al	Problem is in Trigger Light Driver circuit of A2.
2. CHECK MODE IS INOPERATIVE	CHK/NORM switch to CHK	1, Check for an ECL High at A2P1(C).	Perform step 2.	n. Wire to A1P1 is broken. b. Problem is on A1.
		2, Check for OSC CH signal at A2P1(4,D)	Problem is on A2,	Problem is on A7.
3. TRIGGER LEVEL DOES NOT COVER NORMAL RANGE		1, Check for +16.5 V and +16.5 V at A2 P1(6, F) and (7, H), respectively.	Perform step 2.	Problem is on A15,
		2. Check that A2P2(D) is adjustable from at let +3 V to -3 V with front-panel LEVEL control	Problem is on A2	Problem is on A1,
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SECTION VI

REPLACEABLE PARTS

6-1. INTRODUCTION

- 6-2. This section contains information for ordering replacement parts. Table 6-1 lists parts in alphanumerical order of their reference designators and indicates the description and HP Part Number of each part, together with any applicable notes. The table includes the following information.
 - n. Description of part (see abbreviations below).
- b. Typical manufacturer of the part in a fivedigit code; see list of manufacturers in Table 6-2.
 - c. Manufacturer's part number.
- d. Total quantity used in the instrument (TQ column).

6-3. Miscellangous parts are listed at the end of Table 6-1.

6-4. ORDERING INFORMATION

- 6-5. To obtain replacement parts, address order of inquiry to your local Hewlett-Pockard Sales and Service Office (see lists at rear of this manual for addresses). Identify parts by facir Hewlett-Packard part numbers.
- 6-6. To obtain a part that is not listed, include:
 - a. Instrument model number.
 - b. Instrument serial number.
 - c. Description of the part.
 - d. Function and location of the part.

A ampere AFC automat AMPL amplific BFO best fre BE CU beryllin BH binder bandpas BICS brass BIYO backwai CCW counter CER ceramic CMO counter COMO common COMO common COMPL complet COMPL complet COMPL complet COMPL complet COMPL connect COMPL	or ry citor ler y time ce signating (lamp) r electronic part eres malic frequency control	P11 IC J K L L IS M MK		fuse Hiter Integrated circuit jack relsy Inductor b ud speaker nicter nictorbone	MP P Q R RT 6		mechanical part plug transistor resistor	V V VR		integrated circuit vacuum, tube, neor bulb, photocall, etc
B - motor BT - battery C - capacitr CP - coupter CI: a diode DL - delay lis DS - device a E - miac el A - ampère AFC - automal AMPL - amplific BFO - best fre BE CU - beryllis BH - binder l BH - binder l BH - binder l BH - binder l BH - counter CER - ceramic COM - counter CER - ceramic COMO - connect COEF - coeffici COMO - connect COMPL - complet COMPL - complet COMPL - complet COMPL - complet COMPL - connect COMP - connec	or ry citor ler y time ce signating (lamp) r electronic part eres malic frequency control	PI IC J K L IS M MK		illier integrated circuit jack relay inductor bud speaker pieter	P Q R RT 6 T		plug transistor resistor	VR	.	bulb, photocell, etc
BT battery C c capacific CP cuppler CD delay in DE delay in DE delay in DE mine el A ampere AFC antomas AMPL amplific BFO best fre BE CU berylliu BH binder l BP bandpas BICO backwal CCW counter CER ceramic COMO counter COMO confect COMF connect COMP compos CCMPL compic COMP compos CCMPL compic COMP connect COP cadmius CP cadmius CP cadmou CCP cadmou	ery citor ler v y line ce algorito, (lamp) c electronic part erea maile trequency control	ic J K L L IS M MK		integrated circuit jack relay inductor bud speaker pieter	Q R RT 6 T		transistor resistor			
CP - capacitr CP - coupter CP - dode CP - delay it CP - submit CP - submit CP - submit CP - coupter CPR - ceramic CPR - conflict COM - counter CPR - conflict COM - counter CPR - ceramic CPR - ceramic CPR - conflict COM - counter CPR - conflict COM - counter CPR - coupter CPR - ceramic CPR - coupter CPR - coupter CPR - ceramic	citor ter t y line ce algoritos (lamp) : electronic part erea malic trequency control	K L L IS M MK		jack relay Inductor b ud speaker pieter	R RT 6 T		resistor			
CP - coupter CD - dodgy DL - delay lib DE - misc el A - ampère AFC - sutomal AMPL - smplific BFO - best fre BE CU - beryllis BH - binder l BC - counter CER - ceramic CCW - counter CER - ceramic COMO - connect COMO - connect COMO - complet COMPL - complet COMN - connect CP - cathode COMPL - cathode CP - cathod	ler y y y y tine ce algneting (tamp) r electronic part erea malic frequency control	K L IS M MK		relay Inductor b ud speaker paster	RT 6 T					voltage regulator
Cit. diode DL delay li DL delice delay li DE CU hepilli DE delay li DE CU hepilli DE delay li DE CU hepilli DE delay li DE d	y y line ce signuith, (lamp) : electronic part erea malic trequency control	L IS M MK		Inductor bud speaker pieter	6 T			W		cable
DL delay linds a device a E mine el device a E mine el device a E mine el device a submine de device a submine de device a submine de device a submine de device de de	y line ce algneling (lamp) c electronic part erea malic trequency control	NK	p.	bud speaker pieter	T		lhermistor	x	-	Bocket
A ampère attende de la control	e algnating (lamp) : electronic part èrea malic frequency control	MK MK		pieter		•	awitch		•	
A ampere AFC automal AMPL amplife BFO beat fre BE CU berylling BH binder l BP bandpan BIGS brass BTVO backwai CCW counter CER ceramic CMO cabinet COMP compos CCMPL comple CCMPL comple CCMP compos CCMPL comple CCMP compos CCMPL comple CCMP compos CCMPL comple CCMP compos CCM	relectronic part eren malle trequency control	MK				•	transformer	Y		crystal
A ampere AFC automal AMPL amplife BFO beat fre BE CU berylling BH binder l BP bandpan BIGS brass BTVO backwai CCW counter CER ceramic CMO cabinet COMP compos CCMPL comple CCMPL comple CCMP compos CCMPL comple CCMP compos CCMPL comple CCMP compos CCMPL comple CCMP compos CCM	relectronic part eren malle trequency control		•	microphone	TB		terminal board	2	•	tuned cavity,
AFC automal AMPL amplific amplific amplific amplific applications of the control	matte frequency control				TP	•	test point			network
AFC automal AMPL amplific amplific amplific amplific applications of the control	matte frequency control			ABBREVIAT	IONS					
AMPL - smplifle BFO - best fre BE CU - beryllin BH - binder l BH - binder l BP - bandpas BITS - brass BITYO - backwai CCW - counter CER - ceramic CMO - connect COEF - coeffici COMP - compos CCMPL - complet CONP - connect CP - cadminu CP -		H		henries	N/O		normally open	111111	•	rack mount only
AMPL - smplifle BFO - best fre BE CU - beryllin BH - binder l BH - binder l BP - bandpas BITS - brass BITYO - backwai CCW - counter CER - ceramic CMO - connect COEF - coeffici COMP - compos CCMPL - complet CONP - connect CP - cadminu CP -		HDW		hardware	NOM	•	nominal	2 1 1 P 1 1 P 1 P 1 P 1 P 1 P 1 P 1 P 1	•	root-mean aquare
BFO beat Ire BE CU beryllin BH binder l BP bandpan BRS brass BYO backwai CCW counter CER ceramic CMO cablnet COMP compos CCMPL compos CCMPL compos CCMPL campos CCMP cadmiu CCP cadmo CCP	,	HEX		hexagonal	NPO		negative positive zero	RWY	٠	reverse working
BE CU berylling BH binder l BH binder l BP bandpas BINS backwai BYO counter CER ceramic CCW counter CER ceramic CMO eshinet COFF coeffici COM commos CCMPL complei CONP connect COPPL cadmiu CCP cathode CW clockwi DEPC deposit DEPC deposit DEPC encaps ENCAP encaps EXT cateria		HG		mercury			(zero temperature			voltage
BE CU berylling BH binder l BH binder l BP bandpas BINS backwai BYO counter CER ceramic CCW counter CER ceramic CMO eshinet COFF coeffici COM commos CCMPL complei CONP connect COPPL cadmiu CCP cathode CW clockwi DEPC deposit DEPC deposit DEPC encaps ENCAP encaps EXT cateria	frequency oscillator	HR .		hour(a)			coefficient)	• •		alow-blow
Bit binder l BP bandpan Bits brass Brvo backwai CCW counter CER ceramic CMO cabinet COMP compos CCMPL compos CCMPL compos CCMPL compos CCMPL compos CCMPL cadmino CP				hertz	NPN		negative-positive-	5-B	•	F-1-1
BP - bandpan BIGS - brans BITVO - backwal CCW - counter CER - ceramic CMO - cabinet COMP - complee COMP - complee CONP - connect CON - candmiu CRT - cathode CW - clockwi DEPC - deponit DR - drive ELECT - electro ENCAP - encaps EXT - externa		ne .	•	117776		-	negative	5CR	•	screw
BIS brass BYO backwai CCW counter CER ceramic CMO eshinet COEF coeffici COM commo COMP compos CCMPL complet CONN connect CP cadmiu CP cadmiu CRT cathode CW clockwi DEPC deposit DR drive ELECT encaps ENCAP encaps EXT cateria				1-1	NRFR		negative not recommended for	SE	•	ne lenium
IFVO - backwai CCW - counter CER - ceramic CMO - calinct COMP - compos CCMPL - comple CCMPL - comple CCMPL - canded COMP - complex COMP - canded COMP - complex		IF.	•	intermediate freq	114314	•		SECT	•	section(s)
CCW - counter CER - ceramic CMO - cabinet COEF - cerifici COMP - compos CCMPL - complet CONN - connect CP - cadmiu CRT - cathode CW - clockwi DEPC - deposit DR - drive ELECT - encaps ENCAP - encaps ENT - cateria		IMPG	۰	Impregnated			field replacement	BEMICON	٠	aemiconductor
CER ceramic CMO cablinet COGEF coeffici COM common CCMPL complet CONN connect CONN cadmiu CRT cathode CW clockwi DEPC deposit DR drive ELECT electro ENCAP encaps EXT externa	ward wave oscillator	INCD		Incandescent	NSR	٠	not acparately	БÌ		allionn
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CER ceramic CMO cablinet CMO conflict CMO conflict CMM common CCMPL complet CONN connect CCMPL callude CRT callude CW clockwi DEPC deposit DR drive ELECT circup ENCAP cacaga ELECT cacaga ELECT cacaga ELECT cacaga EXT cacaga Externa	iter-glockwise	INS	ь	inaulation(ed)	onn	_	order by description	SL.		alide
CMO eablact COEF coeffici COMP compos COMP compos CCMPL comple CONN connect CP cadmiu CRT cathode CW clockwi DEPC deposit DR drive ELECT encaps ENCAP encaps EXT catros		INT		internal	OBO	•		SPG	-	apring
COEF coefficient COM common COMPL complet CONN connect CP cadmiu CP cathode CW clockwi DEPC deposit DR drive ELECT electro ENCAP encaps EXT externa	nct mount unly	••••		• • • • • • • • • • • • • • • • • • • •	OH:	•	oval head		-	
COM - common COMP - compon CCMPL - compet CONN - connect CP - cadmiu CRT - cathode CW - clockwi DEPC - deposit DR - drive ELECT - electro ENCAP - encaps EXT - common		ĸ	•	kijo = 1000	OΧ	•	oxide	SPL	•	special
COMP compos CCMPL complet CONN connect CP cadmin CRT cathode CW clockwi DEPC deposit DR drive ELECT electro ENCAP cacter EXT catrons COMPOSITE ELECT catrons COMPOSITE COMPOS		LR		left hand	Þ		peak	BST	•	cialniess steel
CCMPL - complet CONN - connect CP - cadmigu CRT - cathode CW - clockwl DEPC - deposit DR - drive ELECT - electro ENCAP - encaps EXT - externa				, , ,	PC PC	-	printed circuit	sn	•	aplit ring
CONN - connect CP - cadmiu CRT - cathode CW - clockwi DEPC - deposit DR - drive ELECT - electro ENCAP - encaps. EXT - externa		LIN		linear laper		•		STL	•	mizel
CP cadmiu CRT cathode CW clockwi DEPC deposit DR drive ELECT electro ENCAP encaps EXT caterna		CIK WABII		*****	PF	•	htentarana -	71	_	tartalum
CRT cathode CW clockwi DEPC deposit DR drive ELECT cicetro ENCAP cncaps. EXT caterna		LOG		logarithmic taper			tarede	TA	•	
CRT cathode CW clockwi DEPC deposit DR drive ELECT cicetro ENCAP cncaps. EXT caterna	ntum plate	LPF		low pass lifter	PH DRZ		phosphor by , ,re	TD	•	time delay
CW clockwi DEPC deposit DR drive ELECT electro ENCAP encaps. EXT sexterna	ode-ray tube				PHL	٠	Phillips	TGI	*	toggle
DEPC deposite DR delve ELECT electro ENCAP encaps. EXT externa		M		mi' + 10-3	PIV		peak inverse vultage	THD		thread
DR = drive ELECT = electro ENCAP = encaps EXT = externa		MEG			PNP		positive-negative-	Tl		titanium -
DR = drive ELECT = electro ENCAP = encaps EXT = externa	mited carbon	MET FLM		metal film	,		positive .	TOL		tolerance
ELECT + electro ENCAP + encaps- EXT + externa		METOX	-	metallic oxide	p/n		part of	TRIM		trimmer
ENCAP = encaps. EXT = externa	T .	MFR		a.esulaciurer	POLY	_	polystyrene	TWT		traveling wave tube
ENCAP = encaps. EXT = externa	trolytic		•		PORC	•	porceiain		•	
EXT caterna		MHZ	•	mega hezik				u	•	micro + 10+6
		MINAT	٠	miniature	POS	*	position(s)		_	
F s farada		MOM		momentary	POT	•	potentiometer	VAIL	•	variable
		MOS		metal oxide autotrate	րթ	•	peak-to-prak	VDCW	•	de working volta
Fii • flat hea		MTG		mounting	PT	•	point			
FILH . Alliates	ster head	MY		"mylat"	PWV		peak working voltage	W		with
FXD + flace	d	m			-		- "	М.	=	watta
G + glga (I)	- <u>.</u>	N		nano (10-9)	RECT		rectifies	ŴIV		working inverse
	TIV-1	N/C	-	normally closed	P.F	-	radio frequency			voltage
			•		RH RH	•	round head or	ww		wirewound
GL - glass	nantum	NE		neon	un	•		W/O		without
GPD - ground!	nantum #	H PL	٠	nickel plate			right hand	W/U	•	≥ FUON!

SIDE FRAME
WINDOW
RIGHT FRONT
PANEL TRIM

SIDE COVER

SIDE TRIM

LEFT FRONT
PANEL TRIM
(BEHIND TRIM
AND WINDOW)

Figure 6-1. Panel Designations

Table 6-1. Replaceable Parts

Reference Designation	HF Part Number	Qtv	Description	Mfr Code	Mfr Part Number
AL	C5326~6CC48	k	DUARD ASSYBATTEMUATUR (SEPLES 1224A) (LIGADED DE C5326-6004B BLANK BUARD) NOTE 18 AIRIZIS NOT INCLUDED WHEN ALATTEN, ASSY IS ORDERED; ORDER AIRIZ SEPARATELY,	.*848C	U5376-63U48
A1C1 A1C2 A1C3 A1C4	016G-2244 016C-0939 C1EC-0378 C16C-0161	1 1 1	CIFRO CER 3.0+5-0.25 PF 500VOCH CI FRO NICA 430 PF 5% 300 VOCH CIFRO NY 0.01 UF 10% 2COVOCH	28480 28480 72136 56285	0160-2244 0160-0934 ADM152270355 192P10392-PT5
A1C5 A1C6 A1C7 A1C8 A1C9	016C-2]4C 016C-2930 C16C-2197 C16H-2146 016C-293C	10	CIFKO CEK 470 PF +80-2C% 1COCVDCM CIFKO CER G.C1 UF +80-2C% 1CGVDCM CIFKO MICA 1C PF 5% CIFKO CER G.OZ UF +8C-2OF IGOVDCM CIFKO CUR G.OL UF +8C-2OF IGOVDCM	91418 91418 72136 91418 91416	TYPE B FA Homisciquisc FA FA
A1CF1 A1CF2 A1CF3 A1CF4 A1CF5	1901-3040 1910-9916 1901-0376 1901-9376 1992-0041	19 11 2	DIGUEISTLICIN 50 MA JO MY DIGUEISTLICON J5V DIGUEISTLICON J5V DIGUEISTLICON J5V DIGUEIRRAKUUNN 5.11V 5R	07263 24482 28480 28480 U4713	FDG1088 1910-0016 15C1-C176 1901-0376 5210919-98
AICFO AIDSI AIJI AIJZ AIPI	1502-0641 2146-0047 1251-0472 1256-1163 0360-0451	l 1 1	DIOULINGEAROCHN 5-11V 5T LAMPINEON GLOW G-8 MILLIAMPS CUNNECTURING 12 CONTACTS CUNNECTURING BNC INPUT TERMINALISOLDER STUD O-GAC" DIA SHANK	0471 J 08806 71785 28480 00000	5/16939-98 AIC 292-06-39-300 1250-1163 08D
A1Q1 A1R1 A1R2 A1R3 A1R4	1855-0334 0183-2235 0183-9145 0183-1015 0183-9125	1 2 1 14	TSTRESE FFT DUAL N-CHANNEL REFED COMP 22K OHM 5% 1/4M REFED COMP 910K OHM 5% 1/4M REFED COMP 10C OHM 5% 1/4M REFED COMP 10C OHM 5% 1/4M	1785e 01121 01121 01121 01121	0N377 CR 2235 CR 9145 CR 1015 CR 9125
A1P5 A1P6 A1R7 A1R8 A1R9	C698-3576 C683-1055 C683-2576 C683-2215 C683-4715	2 2	HIFRO COMP 110K DHM 5% 1/4W REFXD COMP 1 NECOHM 5% 1/4W REFXD COMP 110K DHM 5% 1/4W REFXD COMP 200 CHM 5% 1/4W REFXD COMP 470 CHM 5% 1/4W	28480 01121 28480 01121 01121	0048-3576 CB 1055 G658-3576 CB 2215 CB 4715
A1P1C A1P11 A1P12 A1P13	7683-3325 2100-3728	12	REFXD COMP L NEGOMN 58 1/4W REFXD COMP 3300 DHN 5X 1/4W REFXR COMP 10K DH4 20T LIN 1/2W (SEE NOTE 1 ABOVE) REFXO COMP 2-2K DHN 58 1/4W	01121 01121 26480 01121	CB 1055 CB 3325 2100-3223 CB 2225
AlRIA ALSI ALSI ALSI ALSZ ALSZ	cen3-2225 3101-1278 3101-1279	2	REFRUICCHP 2-2K OHN 5K 1/4W SWITCHISLIOE DPDT ECHK-NORM) SWITCHISLITE OP 3 POSITIONS EATTEN:	01121 79727 79727	CH 2225 G-126-0007 G-128-5-0016
A153 A153 A164 A154 A155	3101-1311 3101-1276	3	SWITCHTSLIDE OPDT 0.5A L25V AC/OC IAC/OCI SWITCHTSLIDE OPDT (SLOPE) PART OF AIRIZ.	19121 19121	G126-0020 G-126-C007
Alk Aly Aly	0389-0046 01821-67401	1 2	SPACER:CAPTIVE KNCHETRIGGER L'YEL	00000 28486	neu C1821-5/401
A2	C5326-6C0C4	ı	INPUT AMPLIFIER ASSY ISERIES 472) ILUADED UN US326-ZUUG4 MLANK BUARD)	28480	G53Z6~60034
A2C1 A2C2 A2C3 A2C3	0160-2936 0160-2936 0160-2936 0180-0197		CIPAD CEN G.O1 UP +80-2GR 100VDCH CIPAD CER C.O1 UP +80-2GR 100VDCH CIPAD CER C.O1 UP +80-2GR 100VDCH CIPAD ELECT 2.2 UF 1CR 2CVDCH	9141 E 9141 E 9141 E 50289	TA TA TA 1500225X9020A2-DYS
A2C5 A2C6 A2C7 A2C8 A2C8	0180-c197 0160-c153 0170-055 0170-c55 C160-2930	3, 7	CIFKD ELECT 2-2 UF 10T 20VDCW CIFKD MY C-001 UF 10T 20VDCW CIFKD MY C-1UF 20T 20CVDCW CIFKD MY C-1UF 20T 20CVDCW CIFKD CER G-C1 UF +8G-20T 1GCVDCW	56289 56289 56289 56289 91418	1500225x9G20A2-DY: 192013292-PT5 192010402 192010402 TA
A2C BQ A2CHL A2CH2 A2CH3 A2CH3	0160-2930 1902-0049 1910-0016 1931-0540 1931-3016	 	CIFRO CER G.OL OF +83-208 LOCYUCW DIODERBALARDOWN 6.19V DT DIODERS HILV DIODERS HILV DIODERS HILV DIODERS BO HILV	91418 04713 28487 07263 28480	TA 5210939-122 1910-0016 FNG1088 1910-0016

Table 6-1. Replaceable Parts (Continued)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
A2CR5 A2L2 A2L3 A2L4 A2L5	1901-0040 9100-2255 9140-0144 9140-0144 9100-2255	2	PRODESSELICUM DO MA DO MY COLLYCHORE 0.47 UH ICR COLLIFRU RF 4.7 UH COLLIFRU RF 4.7 UH COLLIFRU RF 4.7 UH	C7263 28480 26480 28480 28470	FDG1046 9100-2255 9140-0145 9140-0145 9100-2255
A 2 L 6 A 2 L 7 A 2 L 5 A 2 L 9 A 2 L 10	9140-0144 9140-0144 9140-0142 9140-0144 9140-0144	1	COLLEFED RF 4.7 UH COLLEFED RF 4.7 UH CULLEFED RF 2.20 UH LOT COLLEFED RF A.7 UH CULLEFED RF 4.7 UH	28480 28480 82142 28480 28480	9149-0144 9140-0144 09-4436-48 9140-0144 9140-0144
A2Q1 A2Q2 A2Q3 A2Q4 A2Q5	1654-0092 1853-0015 1853-0015 1854-0345 1854-0345	14 9 3	TSTRESE NPN TSTRESE PNP TSTRESE PNP TSTRESE NPN TSTRESE NPN TSTRESE NPN	80131 80131 80131 80131	2h35h3 in364c 2h36sc 2h317s 2h317s
A296 A297 A298 A299 A2910	1853-0015 1853-0015 1854-0092 1853-0015 1752-0015		TSTRISI PHP TSTRISI PHP TSTRISI PHP TSTRISI PHP TSTRISI PHP	89131 89131 80131 .01.1 80131	203640 203640 203563 203640 203640
A2011 A2012 A2013 A2014 A2015	1853-0015 1853-0015 1853-9015 1854-0092 1854-0071	19	ISTRISI PMP ISTRISI PMP LITURESI PMP LITURESI PMP ISTRISI MPM ISTR	80131 80131 80131 80131 20480	2h3b40 2h3b40 2h3b40 2h3bo3 16b4-0071
A2016 A2017 A2018 A2019 A2019 A2020	1854-0092 1854-0092 1854-0365 1854-0092 1854-0071	7	TSTRISE NON TSTRISE NON TSTRISE NON TSTRISE NON TSTRISE NON TSTRISE NONESELECTED FRCM 2N3704)	87131 80131 80131 80131 28480	203563 203563 204410 203563 1854-6071
A2RL A3R2 A2R3 A2R3 A2R5	Q483-4835 2100-2520 C483-2215 G483-24C5 G483-3425	2 	RIFXO COMP 66K OHM 5K 1/4W RIVAR CERMET 50 OHM 2CK TYPE V 1/2W RIFXD COMP 220 OHM 5K 1/4W RIFXD COMP 24 OHM 5K 1/4W RIFXD COMP 34CO OHM 5K 1/4W	01121 2848G C1121 G1121 C1121	CB 6535 2100-2520 CB 2215 CB 2405 CB 3625
A2R6 A2R7 A2R8 A2R9 A2R10	CAB3-1015 OJB3-3025 OAB3-1025 OA98-3113 OA98-3381	6 27 3 2	RIFXO COMP 1GO OHM 5% 1/AW RIFXO COMP 3GOO OHM 5% 1/AW RIFXO COMP 1GOC OHM 5% 1/AW RIFXO CARBON 1GO OHM 5% 1/AW RIFXO COMP 150 OHM 5% 1/AW	01121 01121 01121 28480 28480	CR 1015 CB 1025 CB 1025 Ge58-3113 0658-3381
A2h11 A2h12 A2h13 A2h13 A2h14 A2h15	0698-5175 0698-3379 0698-3375 0698-1525 0698-5180	2 1 1 10 3	RIFKO COMP 360 OHM 5% 1/8W RIFKO COMP 68 OHM 5% 1/8W RIFKO COMP 33 OHM 5% 1/8W RIFKO COMP 1500 OHM 5% 1/4W RIFKO COMP 2% OHM 5% 1/8W	2848C 2848G 2848G G1121 2848C	0098-5175 0658-3375 0698-3375 CB 1525 0698-5180
AZRIO AZRIO AZRIO AZRIO AZRIO AZRIO	0698-5175 C498-5381 C683-1025 C698-3113 0683-1015		RIFXD COMP 360 DHM 5% 1/8M RIFXD COMP 150 OHN 5% 1/8M RIFXD COMP 1000 OHN 5% 1/4M RIFXD CARRON 10C OHN 5% 1/8M RIFXD COMP 10C OHN 5% 1/4M	28480 28480 01321 28480 01121	C698-3113 C8 1025 C8 1025 C8 1015
AZR21 AZR22 AZR23 AZR23 AZR24 AZR25	Cer3-3025 0#33-1625 Ce83-2225 210G-2521 Ce83-2225	ı	RIFRO COMP 3000 OHM 5% 1/4W RIFRO COMP 3800 OHM 5% 1/4W RIFRO COMP 2-2K OHM 5% 1/4W RIFRO COMP 2-2K OHM 5% 1/4W RIFRO COMP 2-2K OHM 5% 1/4W	G1121 G1121 G1121 2848C G1121	CB 3025 CB 3625 CB 2225 21CC-2521 CB 2225
A2R26 A2R27 A2R28 A2R29 A2R30	C083-1015 0683-1015 0683-6815 0683-6815 0683-4725	6 3	RIFRO COMP 100 ONN 5% 1/4W RIFRO COMP 100 ONN 5% 1/4W RIFRO COMP 680 ONN 5% 1/4W RIFRO COMP 680 ONN 5% 1/4W RIFRO COMP 680 ONN 5% 1/4W	01121 01121 01121 01121	CB 1015 CB 1015 CB 6015 CB 6015 CB 6015 CB 6725
A2R31 A2R32 A2R33 A2R34 A2R35	0683-1035 0683-1315 0683-1035 0683-3315 0683-1035	17 2	REFRO COMP LOR CHM 5% L/AW REFRO COMP 330 CHM 5% L/AW REFRO COMP 1CK CHM 5% L/AW REFRO COMP 330 CHM 5% L/AW REFRO COMP 10K CHM 5% L/AW	G1121 G1121 G1121 G1121	CB 1035 CB 3315 CB 1035 CB 3315 CB 1035
A2R36 A2R37 A2R38 A2R39 A2R40	0683-1015 C663-2235 C683-1025 C663-2215 C683-2225		RIFKO COMP 1CO OHN 5% 1/4M RIFKO COMP 22K UMM 5% 1/4M RIFKO COMP 1000 OHM 5% 1/4M RIFKO CUMP 220 OHM 5% 1/4W RIFKO COMP 2-2K OHM 5% 1/4W	01121 01121 01121 01121 01121	CB 1015 CB 2235 CB 1025 CB 2215 CB 2225
A2R41 A2R42 A2R43 A2R44 A2R45	0683-1525 0683-1025 0683-3035 0683-3615 6683-3315	l 6	PIFAD COMP 1596 UMM SK 174W BEFAD CUAP 1600 UMM SK 174W REFAD CUPP 30K UMM SK 174W REFAD CUPP 300 UMM SK 174W REFAD CUPP 330 UMM SK 174W	01121 01121 01121 01121 01121	CB 1525 CB 1025 CB 3035 CB 3015 CB 3515

Table 6-1. Replaceable Parts (Continued)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
A2h4b A2h47 A2h4b A2h4b A2h50	0573-1025 E563-1055 C693-2059 C693-2715 C693-2715	1 1	FIFRU CUMP LOGO ONM SK 1/AM RIFKO COMP ICH UHM SK 1/AM RIFKO COMP 2 MEGOMM ST 1/AM RIFKO COMP 270 OHM SK 1/AM RIFKO COMP 270 OHM SK 1/AM	G1121 G1121 G1121 G1121 G1121	CB 1025 GB 1065 GB 2455 GB 2715 GB 2715
A2U1 A2U2 A3	1820-0238 1820-0142	1 2	INTEGRATED CIRCUITEDIC 2 INPUT NOR GATE INTEGRATED CIRCUITEA INDUT 22-UR/NOR NOT ASSIGNED	04713 04713	MC 1810P MC1004P
A4	C5326-69902	1	OSCIPLATOR ASSY ISERIES 10371 ILMAUED ON O5326-20002 BLANK BUARD)	24480	G5326-88602
A4C1 A4C2 A4C3 A4C4 A4C4	0.80-0161 0180-0197 0121-0059 0180-2784 0180-2930	ì	CEPRO MY 0.01 UF LOT ZOGVOCH CEPRO ELECT 2.2 UF 10T ZGVOCH C.VAR CEP 2-8 PF JOGVOCH CEFRO CER 20 PF 5T 50GVOCH CEFRO CER 3.01 UF +85-21T 100VOCH	56285 56285 56285 2848C 72982 91418	192P10392-PT5 15C0225X9323A2-0Y5 0X21-0059 101-000-CUG9-200J TA
A4L1 A4U1 A4P1 A4R2 A4R3	9100-2216 1850-7158 0678-6037 0683-1025 0683-3015	1	COIL/CHOKE 100 UH 13% TSTRIGE PNP PIFRO MET FLM 46.4 OHH 1% 1/8W RIFXO CUMP 1000 OHM 5% 1/4W RIFXO CUMP 370 UHM 5% 1/4W	28489 80131 28489 01121 01121	9100-2276 202635 0690-4037 CB 1025 CB 3015
A401 A401	9643-3015 1820-0142 0410-0405	1	REFED COMP 300 DHM 5% L/AM INTEGRAFFO CIRCUITE4 INPUT, 2-08/NOR CRYSTALEQUARTE 10 MHZ	01121 04713 2848C	CB 3315 MC100AP 0410—6475
A5	05326~60005	1	TIME PASE CONTROL ASSY ISERIES Y72) ILOADED ON 05326-20005 BLANK BOARD)	28450	05128-60005
A5C1 A5C2 A5C3 A5C4	0180-0157 0186-0127 0186-0127 0186-02150	1 2 1	CIFNO ELECT 7.2 UF 10% 20VOCW CIFNO CER 1.0 UF 20% 25VOCW CIFNO ELECT 1.7 UF 10% 35VOCW CIFNO MICA 33 PF 5%	56289 56289 56289 28480	1500225x1020A2-0YS 5C13C5-CML 1500225x1020A2-0YS
A5C5 A5CR1 A5G1 // A5U2 A5U3	C16C-22C4 19G1-004C 1854-0092 1854-0092 1854-0071	2	CIFXO MICA LOGPF SX UECOEISILICON 50 MA 30 MV TSTRESI NPN TSTRESI NPN TSTRESI NPN TSTRESI NPN(SELECTED FRUM 2N3704)	72136 07263 60131 60131 28480	PDM15F101J3C FDG1088 2N3563 2N3563 1854-0071
A505 A506 A586 A581 A582	1854-0071 1854-0071 1854-0071 0683-1635 0683-1635		TSTRISI NPNESELECTED FROM 2N3704) TSFRO COMP LOK OHM 5E 1/4W	28480 28480 28480 01121 01121	1854-0071 1854-0071 1854-0071 CB 1035 CB 1035
A5R3 A5R4 A5R5 A5R6 A5R7	C683-5105 C683-3325 C683-4715 C683-3325 C683-1225	3	RIFKO COMP 51 OHM 58 1/4W RIFKO COMP 330C OHM 58 1/4W RIFKO COMP 470 OHM 58 1/4W RIFKO COMP 3/0C OHM 58 1/4W RIFKO COMP 1/0C OHM 58 1/4W	01121 01121 01121 01121 01121	CB 51C5 CB 3325 CB 4715 CB 3325 CB 1225
A5R8 A5R9 A5R10 A5R11 A5R12	C683-1725 C683-1025 C683-2215 C683-6835 C683-3325		REFXD COMP 1000 DHM 5% 1/4W REFXD COMP 1: JO DHM 5% 1/4W REFXD COMP 220 DHM 5% 1/4W REFXD COMP 68% UHM 5% 1/4W REFXD COMP 5500 CHM 5% 1/4W	01121 01121 01121 01121	CB 1025 CB 1025 CB 2215 CB 4835 CB 3325
A5R13 A5R14 A5R15 A5R16 A5R17	C683-3325 C683-3325 C683-3325 C683-1025 C683-1025		RIFXD COMP 3300 OHN 5% 1/4W RIFXD COMP 330C OHN 5% 1/4W RIFXD COMP 330C OHN 5% 1/4W RIFXD COMP 1003 OHN 5% 1/4W RIFXD COMP 100C OHN 5% 1/4W	01121 02121 01121 01121 01121	CB 3325 CB 3325 CB 3325 CB 1025 CB 1025
A5R18 A5R19 A5R20 A5R21 A3U1	C683-2225 C683-3225 C683-5105 G683-5105 1820-0413	7	PIFKO CUMP 2-2K CHM ST 1/4W RIFKU/COMP 2-2K OMM SK 1/4W PIFUD COMP 51 OMM SK 1/4W PIFVD COMP 51 OMM SK 1/4W INTEGRATED CIRCUITIDECADE DIVEDER	01121 01121 01121 01121 01.21 28-80	CB 2225 CB 2225 CB 51C5 CB 5105 1820-0413
A5U2 A5U3 A5U4 A5U5 A5U6	1820-0413 1820-0413 1820-0413 1820-0064 1820-0413	•	INTEGRATED CIRCUITIDECADE DIVIDER INTEGRATED CIRCUITIDECADE DIVIDER INTEGRATED CIRCUITIDECADE DIVIDER ICITIL QUAD 2-INPT HAND GATE INTEGRATED CIRCUITIDECADE DIVIDER	28480 28480 28480 01295 28480	1820-0413 1820-0413 1820-0413 5N7400N 1820-0413

Table 6-1. Replaceable Parts (Continued)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
			1		i
A5U? A5U8 A5U8 A5U81	1820-0413 1820-0413 1820-0413 1827-0174	1	INTEGRATED CIACULTAREADE HIVIDER INTEGRATED CIACULTERFEARE DIVIDER ILITTL BECAUE DIVIDER 12.5 MHZ MIN. ILITTL HEK THVERTER	2048C 2048C 2046C 01295	1870-0513 1870-0413 1820-6413 557404 (
A5	05326~60013	ı	SAMPLE HATE ASSY ESERTES 1224A) ELMANEN UN 05326-PC013 MLANK BJARD)	2948G	05 \26-44013
#6C1 #6C7 #6C3 #6C4 #6C5	0166-2261 0166-134 0166-0226 0166-0166 0146-0193	1 1 1 1	CIFYO MICA 31 PF 5% CIFYO MICA 220PF 57 ICGYOCH CIFYO LLFCF 22 UF 13% 15YOCH CIFYO MICA 87 PF 5%	72136 14655 56285 56285 28485	#C#15E51CJ1C FU#15E22UJC 1500220#5015H7=HY5 157P68392=PT5 014U=L153
ANCA ANC 7 ANCH ANCH ANCH	C160-0153 C16C-2177 C16C-0153 C180-0271 O160-0161	2	CTEND MA 27G1 TR FOR SCOADER CTEND FFECT 17C TR FOR SCOADER CTEND HA C'OCT TR FOR SCOADER CTEND HACW 7C BE 28 3GOADER CTEND HA 97COT TR FOR SCOADER	50285 20486 50285 50285 50289	152P1C272-PF5 O165-2149 142P1C242-PF5 15CN1C5K5C35A2-UY5 152P1C342-PF5
A6C11 A6C12 A6C91 A6C62 A6C63	0180-0114 0160-0114 1901-0040 1901-0040 1916-0016	6	CIFFD ELECT 4.0 UF +102-1CE 254DCW CIFRD ELECT 4.0 UF +1CG-1CF 254DCW DINDERSTELECUN 50 MA 30 WV DINDERSTELECUN 50 MA 3C MV DINDERGE 60 MIY	28480 28480 27263 07263 28480	1410-7019 Englass Fréiass Clus-cils
#6CR4 #6CR5 #6CR7 #9CRB #6CR9	1910-0016 1901-0040 1901-0040 1901-0040 1910-0016		DIDDEIGE 60 MIV DIDDEISELICON 50 MA 30 MV DIDDEESELICON 50 MA 30 MV DIDDEESELICON 50 MA 30 MV DIDDEEGE 60 MIV	2848C 07263 C1263 C7263 24484	1910-cc16 fugiora fugiora fugiora 1910-cc16
AACFIG AACFII AAGI AAGI AAGI	1901-0040 1901-0040 1854-0071 1854-0071 1854-0071		Oftopersilicon so ma 30 my Oftopersilicon so ma 30 my TSTREST NPM(SELECTED FROM 201704) TSTREST NPM(SELECTED FROM 201704) TSTREST NPM(SELECTED FROM 201704) TSTREST NPM(SELECTED FROM 201704)	C7263 07263 2848C 2848C 2048C	F1-61086 F861668 1854-0071 1854-0071
A604 A605 A606 A607 A608	1854-0009 1854-0071 1854-0071 1854-0215 1854-0071	9	TSTRESS NPN TSTRESS NPNESELECTED FRUN 2NSTUAS TSTRESS NPNESELECTED FRUN 2NSTUAS TSTRESS NPN TSTRESS NPNESELECTED FRUN 2NSTUAS	20171 50171 50171 50171	7h7L9 1654-CG71 1854-CG71 7h34C4 1854-G371
A609 A6010 A6011 A6012 A6013	1854-0071 1854-0071 1854-0071 1854-0071 1854-0071		TSIRES: NPNISELECTED FRUM 2037-04) ISIRES: NPNISELECTED FROM 2037-04) ISIRES: NPNISELECTED FROM 2037-04) ISIRES: NPNISELECTED FROM 2037-04) ISIRES: NPNISELECTED FROM 2037-04)	28480 26480 80131 28480 26480	1854-cc71 1854-cc71 18769 1854-cc71 1878-c071
#4# #6P 2 #6P 3 #6P 4 #6R 5	C683-1C15 (683-1525 C683-5125 C683-1U35 C683-1C35	a	REFRO COMP ICO OMM 5% I/AW REFRO COMP 1510 DHM 5% I/AW PEFRO COMP 5100 OMM 5% I/AW REFRO COMP ICK OMM 5% I/AW NEFRO COMP ICK OMM 5% I/AW	01151 01151 01151 01151 01151	CH 1015 CH 1525 CH 5125 CH 1035 CH 1035
AGPG AGRE AGRE AGRE AGRE AGRE	C683-5125 C683-1035 C683-3325 C683-3C15 C683-2C25	10	REFRO COMP 5100 OMM 5% 1/4W REFRO COMP 10% OMM 5% 1/4W REFRO COMP 33CC OMM 5% 1/4W REFRO COMP 3CC OMM 5% 1/4W REFRO COMP 2CCC OMM 5% 1/4W	C1151 C1151 O1151 O1151 C1151	CR 5125 CR 1035 CR 3325 CB 3015 CB 2025
AGRII AGRIZ AGRIZ AGRIA AGRIA	0683-2735 0683-5125 C683-3325 C683-1033 C683-3325	4	RIFKO COMP 27K OHM 5% 1/4W RIFKO COMP 51CO OHM 5% 1/4W RIFKO COMP 1300 OHM 5% 1/4W RIFKO COMP 1CK OHM 5% 1/4W RIFKO COMP 33CO OHM 5% 1/4W	61151 61151 61151 61151 61151	CH 2735 CP 5125 CB 3325 CP 1035 CB 3325
AGF L6 AGR 17 AGR 18 AGR 19 AGF 20	Cu83-5125 Cu83-3325 Cu23-3325 Cu23-3115 Cu83-2735	•	REFRO COMP 51CO DHM SR 1/4W REFRO COMP 35CO CHM 5F 1/4W REFRO COMP 3570 OHM 5F 1/4W REFRO COMP 510 OHM 5K 1/4W REFRO COMP 27K UHM 5K 1/4W	C1121 C1121 C1121 C1121	C9 5125 CF 3125 CB 3125 CB 5115 CB 2735
AGP 21 AGP 22 AGP 23 AGP 24 AGP 25	C683-1035 C683-3915 C683-2025 C .3-6215 Ouds-1525	t t	RIFKD COMP LOK DHM 5% L/AW RIFKD COMP 35G OHM 5% L/AW RIFKD COMP 20CO DHM 5% L/AW RIFKD COMP 62G DHM 5% L/AW RIFKD COMP 1500 OHM 5% L/AW	01121 01121 01121	CR 1035 CB 3915 CR 2025 CB 6215 CB 1525
A6R26 A6R27 A6R28 A6R29 A6R3¢	Co83-2025 Co83-3025 Co83-9115 Co83-1525 Ob83-2415	2 1	RIFXO COFP 2000 OHM 5% 1/4W RIFXO COMP 3000 OHM 5% 1/4W RIFXO COMP 910 OHM 5% 1/4W RIFXO COMP 1500 OHM 5% 1/4W RIFXO COMP 240 OHM 5% 1/4W	01121 01121 01121 01121 01121	C5 2025 C8 3025 C8 9115 C8 1525 CR 2415

Table 6-1. Replaceable Parts (Continued)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Numbe
A6R 31 A6P 32 A6P 34 A6P 34 A6P 35	1 063-1035 1083-1035 1083-2735 1083-2735 1083-2735		PEPRO COMP TOK WIM 5% 1/4W REPRO COMP FOR WIM 5% 1/4W REPRO COMP 27K WIM 5% 1/4W REPRO COMP 10K WIM 5% 1/4W REPRO COMP 10K WIM 5% 1/4W	01151 01151 01151 01151 01151	C 1035 CR 1035 CR 2735 CR 2735 CR 1035
ADF 36 ADR 37 ADR 38 ADR 39 ADR 40	2643-1015 3643-2645 6683-1015 6681-1035 6681-4735	1	REFXD COMP LUK BIN 5% LYAW REFXD CIMP ZCCK DIN 5% LYAW REFXD CIMP LUK UIM 5% LYAW REFXD CIMP LUK UIM 5% LYAW REFXD CUMP 47K UIM 5% LYAW REFXD CUMP 47K UIM 5% LYAW	01121 01121 01121 01121 01121	CH 1035 CH 2045 CH 1035 CH 1035 CH 1735
A6041 A6042 A6043 A6044 A6011	0683-1525 9683-1015 9683-4735 9683-1015 1820-05 i		RIFRO COMP 15GG ONN ST 1/AW RIFRO COMP 1CO ONN ST 1/AW RIFRO COMP 1CO ONN ST 1/AW RIFRO COMP 1CO ONN ST 1/AW ICITI UNAD 2-INPT MAND GATE	01121 01121 01121 01121 01295	CB 1525 CB 1015 CB 4735 CB 1015 5874008
A602 2603 A604 A605 A606	1820-02.2 1820-0068 1820-0354 1820-0328 1870-0147	1 1 1	IC:ECL TYPE D F/F ICITIC TRIFLE J-INPUT PUS NAMO GATE ICITIC QUAD Z-INPT YAND GATE ICITIC QUAD Z-INPT HOR GATE ICITIC QUAD Z-INPT HOR GATE	04713 12040 01295 G4713 04713	MCL022P 5N7410N 5N740CN 5N740CN MCL007P
A7	05326-6CC45	t	BOARD ASSYLFUNCTION CUNTROL (SERIES 1:36A) (LOADED ON US326-20045 BLANK BOARD)	2848C	05326-60045
ATC1 ATC2	016C 2199 014C-0201	l l	CIFAD MICA DO PF 5% CIFAD MICA 12 PF 5%	28480 28480	01502199 0140-0261
A7C3 A7C4 A7C5 A7C6 A7C7	G160-2327 G16C-2327 G16C-2327 G16C-2327 C16C-2327	7	CIFXD CER 1000 PF 20% 100V0CW	96733 96733 96733 96733 96733	B1C4BX1J2M B1C4BX1C2M B1C4BX1O2M B1C4BX1O2M B1C4BX1O2M
A7CH A7CG A7QL A7Q2 A7Q3	C140-2327 C16C-2327 1854-0215 1854-0215 1854-0609		CEFAD CER IGCO PF 20% IUCADEM CEFXD CER IGCO PF 20% IOCADEM ISTRISE APA ISTRISE APA ISTRISE APA	96733 96733 80131 80131 80131	81048×102M 81C48×102M 2N39C4 2N39C4 2N7C9
AT04 A705 A7R1 A7R2 A7R3	1654-CCU9 1654-CCO9 C663-2425 C663-2425 C663-4315	3	TSTRIS: NPN TSTRIS: NPN RIFID CUMP 2400 CHM 5% 1/4H RIFID CUMP 2400 CHM 5% 1/4H RIFIO CUMP 430 CHM 5% 1/4H	50131 80131 01121 01121 41121	2N709 2N709 CR 2425 CB 2425 CB 4315
A/R4 A7R5 A7R6 A7R7 A7R8	Cob3-1525 Cob3-2025 Ob3-1525 CCC3-2025 Ob83-5125		RIFRO COMP 1500 OHM ST 1/AM PIFRO COMP 2000 OHM ST 1/AM RIFRO COMP 1500 OHM ST 1/AM RIFRO COMP 2000 OHM ST 1/AM RIFRO COMP 5100 OHM ST 1/AM	01121 61121 01121 01121 61121	CB 1525 CB 2025 CB 1525 CB 2025 CB 5125
ATRO ATRIO ATRII ATRIZ ATRII	0683-1011 0483-27,75 0483-471.7 C483-7515 0483-4825) 3	RIFKD COMP TOO UHM ST L/AW RIFKD COMP 2700 DHM ST L/AW AIFKD COMP 470 UHM ST L/AW RIFKD COMP 750 OHM ST L/AW RIFKD COMP 430 OHM ST L/AW	01121 01121 01121 01121 01121	CB 1015 CB 2725 CB 4715 CB 7515 CB 4325
ATRL4 ATRL5 ATRL5 ATRL7 ATRL7	0683-1025 0683-7515 0683-1025 0683-4715 0663-3015		REFXD CUMP 1000 OHM 5% 1/AW REFXD CUMP 750 OHM 5% 1/AW REFXD CUMP 1000 OHM 5% 1/AW REFXD CUMP 470 OHM 5% 1/AW REFXD CUMP 300 OHM 5% 1/AW	G1121 01121 01121 01121 G1121	CB 1025 CB 7515 CB 1025 CB 4715 CB 3015
A7P19 A7P20 A7H21 A7H22 A7H23	1683-7915 C643-3015 C643-3025 G661-9115 G663-5615	ı	RIFXD COMP 750 OHM 5% 1/4m RIFXD COMP 300 OHM 5% 1/4m RIFXD COMP 3000 OHM 5% 1/4m RIFXD COMP 310 OHM 5% 1/4m RIFXD COMP 560 OHM 5% 1/4m	01121 01121 01121 01121	CB 7515 CB 3015 CB 3025 CB 9115 CB 5615
A7R24 'A7UL A7UZ L7U3 A7U4	CAB3-1325 1620-0145 1620-0340 1620-0322 1620-0212	1 1 4	REFXD COMP 1500 OHM 5E 1/4W ICIDIGITAL QUAD 2-INPT MOR GATE ICIECL DUAL RS F/F INTEGRATED CIRCUITIJ-K FLIP FLOP ICIECL QUAD LINE RECEIVER	01121 26460 04713 04713 04713	CB 1525 1820-0145 MC1016P MC1013P MC1020P
ATUS ATUS ATUT ATUB	1820-9489 1820-9489 1820-9252 1820-9253	3 1 1	ICIECL ICIECL ICIECL ICIECL DUAL 3-4 INPT OR/NOR GATE INTEGRATEO CIRCUITIDIGITAL ECL DUAL	28480 28480 04713 04713	1820-0489 1820-0489 MC1026P MC1035P

Table 6-1. Replaceable Parts (Continued)

	4		3-1. Replaceable Parts (Continued)	 ,	
Reference Designation	HP Part Number	Ωtγ	Description	Mfr Code	Mfr Part Number
Att	C5326~60009	1	DISPLAY SUPPORT ASSY ESERTES 994) (LOADED ON USSES-2009 HEANK ROARD)	284BG	0532n-+3339
ABC3 ARC2 ARCFL	016C-2930 0160-2199 1931-0340		CIFNO CER O.OL UF *80-2CT LCGVDCW CEFND MICA JC PF ST JOOVDCW DIGOEESILICUM SC MA 30 WV	91418 28480 07263	TA OBCJ-2859 FN:s1008
AUCP2 ABCR3 AUCR4 AUJL AUGI	1910-0016 1910-0016 1901-0040 1251-2035 1854-0092	1	DIGGREGE OD MEV DIGGERGE OD MEV DIGGERGE OD MEV CONNECTUREDE EDGE 12 k 15) 30 CUNTACT TSTRIS] NPN	2648C 2648G C7263 71745 8G131	1 #10-0016 \910-0016 /001088 252-15-#0-300 283563
A802 A803 A804 A805 A806	1854-0092 1854-0365 1854-0365 1854-0365 1854-0365		TSTREST MFN TSTREST NPN TSTREST NPN TSTREST NPN TSTREST NPN	80131 80131 80131 80131	201503 20403C 20403C 2043G 2044S
ABQ7 ABQB ABQB ABQ10 ABQ11	1854-03' ; 1854-0-65 1854-0692 1854-0692 1859-0092		ISTRIS) NPN TSTRIS) NPN TSTRISI NPN TSTRISI NPN TSTRISI NPN	60131 60131 60131	28441C 28441C 284583 284583 284583
AGR L AGR 2 AGR 3 AGR 4 AGR 5	0443-1125 0483-1045 0483-1045 0483-1025 0483-1255	5	PEFED COMP 1170 OHM 57 174W REFED COMP 100K OHMS 57 174W REFED COMP 100K OHMS 52 174W REFED COMP 100C OHM 57 174W REFED COMP 1.2 MEGOHN 58 174W	01121 01121 01121 01121	CM 1125 CH 1045 CH 1045 CH 1025 CM 1255
及りたも 人名称です 人名ので 人名ので 人名ので 人名ので	C683-1255 C683-1255 C683-1255 C683-1255 C683-1255		HIPRO COMP 1-2 MEGOHN 5T 1/AW RIPRO COMP 1-2 MEGOHN 5E 1/AW	C1121 01121 01121 01121 01121	CF 1255 CR 1255 CR 1255 CR 1255 CR 1255
AURIL ADRIZ ADRIZ ADRIZ ADRIS	0683-2425 C683-1015 Q683-1025 C683-2715 G683-4725		RIFXO CUMP 2400 UHM 5% 1/4W RIFXO CUMP 100 UHM 5% 1/4W RIFXO CUMP 1000 UHM 5% 1/4W RIFKO CUMP 270 UHM 5% 1/4W ZIFKO CUMP 4700 UHM 5% 1/4W	01121 01121 01121 01121	CH 2425 CR 1415 CR 1625 CB 2715 Ch 4725
ASPIG AURIT AURIU AURIU AURIU AURIC	G603-1025 G603-4725 G603-5115 G603-1045 G603-1045		REFEC CUMP EGGC FIRM SE 1/4W REFEC CUMP 4700 CHR SE 1/4W REFEC CUMP 510 CHM SE 1/4W REFEC CUMP EGGC CHMS SE 1/4W REFEC CUMP EGGC CHMS SE 1/4W	G1121 91121 91121 91121 91121	CR 1025 CB 4725 CR 5115 CR 1045 CB 1045
AUR21 AUR22 AUR23 AUR24 AUR25	C083-2725 C063-5115 C0F1-1045 C043-2723 C083-1535	ı.	RIFKD COMP 2700 OHM 5% 1/4W RIFKD COMP 510 OHM 5% 1/4W RIFKD COMP 1000 OHM5 5% 1/4W RIFKD COMP 2700 OHM 5% 1/4W RIFKD COMP 15% UNM 5% 1/4W	01121 01121 01121 01121 01121	CP 2725 CR 5113 CR 1045 CR 2725 CR 1335
ABRZO ABUZ ABUZ ABUS ABUS	G661-2225 1820-0094 1820-0307 1820-0143 1820-0102	1 1 1	REFED COMP 2-2K OHM 5% 1/4W 15EDTL QUAD 2-IN:UT GATE ICEDTL HEK INVEKTER INTEGRALED CIRCUITIAC COUPLED JK F/F ENTEGRALED CIRCUITIAC COUPLED JK F/F	01121 04713 04713 04713 04713	CB 2225 SC6903PK MC836P MC1027P MC1013P
A845 A646	1820-0107 1820-0102		INTEGRATED CIRCUITIJ-K PLIP FLOP INTEGRATED CIRCUITIJ-K FLIP FLOP	04713 04713	MC1013P MC1013P
A 9	05326-6700F	l.	DISPLAY ASSY ISERIES 1224A) ILOADED ON 05326-20008/25 MLANK BOARD).	28470	05326-60008
A9051 A9052 A9053 A9054 A9055	1970-00/2 1970-00/2 1970-00/2 1970-00/2	15	TURESMUNEPICAL BNOSCATOR TURESMUNERICAL BNOSCATOR TURESMUNERICAL BNOSCATOR TURESMUNERICAL BNOSCATOR TURESMUNERICAL BNOSCATOR	#3594 #3594 #3594 #3594 #3594	8-5750-5 8-5750-5 2-5750-5 8-5750-5 8-5750-5
A9056 A9057 A9R1 A9R2 A9R3	1970-90+2 1970-0942 083-1025 066-843; 0483-17:25		TUBERNUMERICAL INDICATOR TUBERNUMERICAL INDICATOR RIFKO COMP 2003 OHM 5% 1/4w RIFKO FLM 7.5% OHM 5% 1/6w RIFKO COMP 2000 OHM 5% 1/4w	83594 83594 61121 28486 61121	B-5730-5 B-573C-5 CB 1025 060B-8431 CB 1025

Table 6-1. Replaceable Parts (Continued)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
ATR4 ATR5 ATR6 ATR7 ATR8	0698-8431 0698-8431 0698-8431 0698-8431		FIFED FLM T.OK CHAM 2% 1/8W FIFED FLM T.OK CHAM 2% 1/8W RIFKD FLM T.OK CHAM 2% 1/8W RIFKD FLM T.OK CHAM 2% 1/8W RIFKD FLM T.OK CHAM 2% 1/8W	28480 28480 28480 28480 2848	0698-8431 0698-8431 0698-8431 0698-8431 0698-8431
AUFT AGREC AGREE AGREE AGREE AGREE	0698-8431 0893-1006 0698-8431 0693-1006 1620-0275	3	RIFRO FLM 7.5K UMM 22 1/9W RIFRO CUMP 10 UMM 5K 1/4W RIFRO FLM 7.5K UMM 2K 1/8W RIFRO CUMP 10 UMM 5K 1/4W ICHECL TO THE CUMP 2K 1/4W IR TRANS.	2848G OLLZI 2848C OLLZI O4713	0668-8431 CB 1006 0698-8431 CB 1005 HC1035P
A9U2 A9U3 A9U4 A9U5 A9U6	1820-0119 1820-0119 1820-0119 1820-0119 1820-0119 1820-0119	13	ICETTL HEARKING DECADE COUNTER ICETTL BLANKING DECADE COUNTER ICETTE BLANKING DECADE COUNTER ICETTL BLANKING DECADE COUNTER ICETTL BLANKING DECADE COUNTER	2848C 2848C 2848C 2642C	1820-C119 1820-C119 1820-C119 1820-C115 1820-C115
A9U7 A9U9 A9U10 A9U11 A9U12	1820-0119 1820-0116 1820-0116 1820-0116 1820-0116	15	CATTL SLANKING LICADE COUNTER CGA-BIT BUFF STORE GATED OUTS CGA-BIT BUFF STORE GATED OUTS CGA-BIT BUFF STORE GATED OUTS CGA-BIT BUFF STORE GATED OUTS	2648C 2648C 2648C 2648C 2648C	1620-C119 1820-C116 1620-C116 1820-C116
A9013 A9014 A9035 A9017 A9018	1820-0116 1820-0116 1820-0116 1820-0129 1820-0092	l 14	IC:4-BIT BUFF STORE GATED DUTS IC:4-BIT BUFF STORE GATED DUTS IC:40-BIT BUFF STORE GATED DUTS IC:00CCOOPR-DIVIOER BYTEGRATED CIRCUIT:00CCOPR-DIVIOER	28480 28480 28480 28480 28480	1820-0116 1820-0116 1820-0116 1820-0729 1820-0092
A7017 A9020 A9021 A9022 A9023	1820-0092 1870-0092 1820-0092 1820-0092		INTEGRATED CIRCUIT:DECODER-DIVIDER INTEGRATED CIRCUIT:DECUDER-DIVIDER INTEGRATED CIRCUIT:DECUDER-DIVIDER INTEGRATED CIRCUIT:DECODER-DIVIDER INTEGRATED CIRCUIT:DECODER-DIVIDER INTEGRATED CIRCUIT:DECODER-DIVIDER	26480 26480 26480 26480 26480	1820-0092 1820-0092 1820-0092 1820-0092 1820-0092
A9XU8 A9XU26 A9XU24	3200-0477 1200-0427 1200-0477	6	MOCKETHIC BOCKETHIC BOCKETHIC	2848C 2848C 2848C	1200-0477 1200-0477 1200-0477
A9XD53 A9XD52 A9XD53 A9XD54 A9XD56	1200-0405 1200-0405 1200-0405 1200-0405 1200-0405	16	SOCKET: TUBE, FOR 8700 SERIES FOCKET: TUBE, FOR 8700 SERIES BOCKET: TUBE, FOR 8700 SERIES E" 'NET: TUBE, FOR 8700 SERIES BC 'KET: TUBE, FOR 8700 SERIES	83694 83694 83694 83694 83694	5K207 5K207 5K207 5K207 5K207
ABXD56 ABXD57	1200-0405 1200-0406		SOCKET: TUBE FOR 6700 SERIES SOCKET: TUBE FOR 6700 SERIES	83594 83594	6K207 5K207
Alo	05326-60036	1	BOARD ASSYERIGHT READOUT LEGRES 1036A) ELGADED ON 05326-20036 BLANK BOARD)	28480	05326-60034
	05376-00009 05376-8008 05326-80009	3 1 1	BRACKET:READOUT !NOICATOR:MASK (U.N.S.) !NOICATOR:MASK (V.N.HE)	28480 28480 28480	05326-00009 05326-60006 05326-80009
Alonsi Alonsi	C5326-8001C 05330-40002 G510-0207 2140-0313 2140-0313	1 5 6 11	INDICATUREMAIK (*,K,G) BLUCKEANMUNCIATUR NUTICAPTIVE 4-40 K G-188 LG LANPINEUN GLOW FROSTEO 1-9 MILLIAMPS LANPINEUN GLOW FROSTEO 1-9 MILLIAMPS	2848G 2848G 2848G G88G6 G88G6	03326-80010 03350-40002 0310-0207 C2A-B C2A-B
A10053 A10055 A10055 A10056 A10057	2140-0313 2140-0313 2140-0313 2140-0313 2140-0313	:	LAMPINEUM GLUM FRUSTEO 1.9 MILLIAMPS	0880 60880 60880 60880	C2A-B C2A-B C2A-B C2A-B
A10056 A10Q1 A10Q2 A10Q3 A10Q4	2140-0313 1854-0009 1854-0009 1854-0009 1854-0474	12	LAMPINEON GLUM FROSTED 1.9 MILLIAMPS TSTRISI NPN TATRISI NPN TSTUSSI NPN TSS1/1531 NPN	08806 80131 80131 80131 28467	C2A-B 2N7O9 2N7O9 2N7O9 1859—C474
A1095 A1096 A1097 A1098 A1009	1854-0474 1854-0474 1854-0474 1854-0474 1854-0474		TSTRIS; APH TSTRISE NPN TSTRISE APH TSTRISE NPN TSTRISE NPN	2848C 2848C 2848C 2848C 2848C	1854-C474 1854-0474 1854-C474 1854-C474 1854-C474
Alogio Alogii Alimi Aloma Aloma	1854-0474 1854-0474 0683-5125 C683-5125 0683-5125		TSTRESE NPN TSTRESE NPN REFXO COMP 5100 ONN 5% 1/4W REFXO COMP 5100 ONN 5% 1/4W REFXO COMP 5100 ONN 5% 1/4W	2448C 2648C 01121 01121 01121	1854-C474 1854-C474 CR 5125 CD 5125 CB 5125

Table 6-1. Replaceable Parts (Continued)

Reference Designation	HP Part Number	Ωtу	Description	Mfr Code	Mfr Part Number
	2003-1025		FIFRO CUMP 3000 DHN SR 3/4W	(112)	CH 1035
ALCRY Alcro Algro Algro Algro	CAN3-2025 CAN3-2025 CAN3-3025 CAN3-3135	5	HEERD COMP 2003 JUN 58 BYAM HEERD COMP 2003 DIN 58 BYAM HEERD COMP 35JC DIN 58 BYAM PERFO COMP 51K JUN 57 BYAM	C1171 C1151 C1151	CH 2625 CP 1625 CP 1625 CH 5135
ALURG ALCUI ALCUI ALCUI ALCUI	CAR3-5135 1870-0274 1520-0274 1820-0273 1820-0310	9 1	FIFRO CUMP 51K OHM 5K 1746 ICIDTL QUAD 2+1NPT DR GATE ICIDTL QUAD 2-1NPT DR GATE ICIDIL QUAD 2-1NPT AND GATE ICIDIL THIPLE 3-1NPUT NAND GATE	C1121 26480 26480 28440 04713	CF 5135 1821-6274 1820-6274 1820-6273 566516PN
A1005 A1006	1870-0274 1870-0274	,	ICINTL QUAN SHIPT ON GATE ICINTL QUAN SHIPT OR GATE	2848C	1650-C514 1650-C514
All	05328-60035	ı	UMARD ASSYPTIFT ACADOUT ISENIES BRIBOA) ILDADED UN USRO-2003, FLANK BRARD)	JHARC	65720-60035
	05326-00004 05326-80011	1	PRACKETS READEUT INDICATORIMASK (ERT, C, UF)	2548C	03126-00009 05326-00011
Alici Alici	05330-40002 0160-2930		HEUCKEANNUNCTATUR NUT ASSIGNED CIFED CER DICE UF +80-207 ECOVOCH	284FC 5141E	C533C-400G2 TA
ALICI ALIDSI	C14C-22C0 2110-0313	l l	CEPRO MECA 43 PF 5% LAMPENEUM GLOW FRUSTED 1.9 MELLEAMPS	72336 00106	RDM15E43CJ3C CZA-B
AllD52 AllD53 AllO1 AllO2 AllO3	2140-0313 2140-0313 1854-0071 1854-0474 1854-0474		LAMPINEUN GLUM FINDSTED 1.9 MILLIAMPS LAMPINEUN GLUM FRESTED 1.5 MILLIAMPS TSTRIS] NPNISELECTED FIUM 2M37C4) TSTRISS NPN TSTRISS NPN	0860L 0880A 2848C 2848C 2849C	C2A=0 C2A=0 1854-CC71 1854-C474 1854-C474
Aliqa Aliri Alirz Alira Alira	1854-C474 C683-1035 C683-2035 C683-1025 G683-1525	,	TSTRESE APN ALFRO COPP ICK OHM ST 174W ALFRO COPP 20K DIM ST 174W ALFRO COPP 100 DHM ST 174W PLEND COPP 100 DHM ST 174W	2848C 01121 01121 01121 01121	1854-C474 CM 1035 CM 2035 CR 1625 CM 1525
Alles Alles Alles Alles Alles	CA83-2C25 GA83-2C25 GA83-2O25 GA83-5135 GA83-5135		RIFRO CUMP 2000 CHM ST 1/AW RIFRO COMP 2000 CHM SF 1/AW RIFRO COMP 2000 CHM ST 1/AW RIFRO COMP S1K CHM ST 1/AW RIFRO COMP S1K CHM ST 1/AW	G1121 G1121 U1121 G1121 C1121	CB 2025 CB 2025 CB 2025 CB 3135
Alipio Alipii Aliui Aliuz Aliuz	0683-5135 0683-1025 1820-0175 1820-0274 1820-0274	1	ROFED CUMP SER OHM SE E/AM REFED CUMP 1000 UHM SE E/AM ECETTE HER ENVEREER, OPEN CULL. ECEDTE QUAD 2-INPT OR GATE ECEDTE QUAD 2-INPT ON GATE	01171 01171 01295 28487 28487	CP 5135 CB 1025 5076050 1820-0274 1820-0274
ALIUA ALIU5 ALIU6 ALIU7 ALIU8	1020-3274 1820-0274 1820-0274 1820-0273 1820-0273		COUNT QUAD 2-INPT OR GATE ICIDTL QUAD 2-INPT OR GATE ICIDTL QUAD 2-INPT OR GATE ICIDTL QUAD 2-INPT AND GATE ICIDTL QUAD 2-INPT AND GATE	24490 20400 20400 20400 20460	1820-0274 1820-0274 1820-0274 1820-0273 1820-0273
Allus	1820-0054		SCEFFE QUAD 2-SHPT NAND GATE	01295	SN740CN
A12			NOT ASSIGNED	,	
ALS ALS			NOT ASSIGNED NOT ASSIGNED		
AL5	05327-60020	1	HOARD ASSYLPHWER SUPPLY ISERIES 1312A) (LUADED DIE 05326-20020 RLANK BUAHD)	2848°	C5327-60-120
A1501 A1502	951C-02C7 220C-0145 504C-0409 G169-0103 010C-0114	3 1	NUTICAPTIVE 4-40 X 7.188 LG SCREMEPAN HO POZI DR 4-40 X 0.438 SPACERISHIELD CIFRO NY 6.033 UF 108 2GOVDCM CIFRO ELECT 4.0 UF +100-108 25VDCM	28480 00000 28480 56289 28480	0910-0207 080 5090-6469 192933392-975 0180-0114
A15C3 A15C4 A15C5 A15C6 A15C7	C180-0114 0180-0114 0180-0114 0180-0175 C180-0975	2	C:FRD ELECT 4-3 UF +103-1CE 25VDCM C:FRD ELECT 4-0 UF +1C3-1CE 25VDCM C:FRD ELECT 4-0 UF +1C3-1CE 25VDCM C:FRD CER 0.CGL UF 20% 75VDCM C:FRD CER 0.GGL UF 20% 75VDCM	26480 28483 28480 32574 12574	0180-0114 0180-0114 0180-0114 55%-001-98

See introduction to this seet an for ordering information

Table 6-1. Replaceable Parts (Continued)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Numbe
A15Cd - A35CP - A35CR3 - A35CR2 - A35CR3	C160-3277 C160-3277 1502-3002 1502-3551 1502-0551	2 2 2	CIFED CEP 0.GL UF 20% SOVOCH CIFED CFP 0.GL UF 20% SOVECH OLUDE HREAKDUNESSIFV SW DIUDE HREAKDUNESSIFV SW DIUDE HREAKDENDSSIFV SW	90733 90733 2848C 2848C 2848C	G504H %1G3 >> G504H X1U3 >> 1402 >> 1502 1502 >= 1551 1542 >= 6551
#155#5 #150#5 #150#6 #150#6 #150##	1902-3002 1903-0046 1902-3094 1902-3094	4	DIODE ORFANDEWNES-37Y 5T DIODE-5861CON 5G MA 3C MV DIODE-6868COWNES-18Y 2T DIODE-6868COWNES-18Y 2T DIODE-5868COWNES-18Y 2T DIODE-5868COWNES-18Y 5T DIODE-5868COWNES-18Y 5T DIODE-5868COWNES-18Y 5T	2846C 2848C 2846C 2846C C447C	1902-3002 FUG1044 1502-3794 1502-3094 FUG1088
ABBCHY ABBCHLL ABBCHLL ABBCHLZ ABBCHLB	1907-1094 1502-3094 1902-3394 1902-3429 1901-0033	1 2	DIOUE B-EARCOWNES-119 28 DIOC CAEARCOWNESS V 28 DIOC B-EARCOWNESS V 28 DIOCESSILICUM 100MA	08485 28485 28485 28485 08487	1902-1094 1902-1094 1902-144 1902-1429 FN3369
ALSCRIA ALSCRIA ALSCRIA ALSCRIA ALSCRIA ALSOI ALSOI ALSOI ALSOI ALSOI ALSOI ALSOI ALSOI	1901-0033 1401-0044 1501-0044 1501-0044 1901-0044 2110-0460 1854-0300 1275-7018 1853-0073 1205-0039	1 22 1	Oliderblicon icoma leuby Didderblicon zoma/ly Didderblicon zoma/ly Didderblicon zoma/ly Didderblicon zoma/ly Didderblicon zoma/ly Fise: 1/22 AMP Tithesi APN HEAT SINKISEMICONDUCTOR TSTRESI PPP HEAT SINKISEMICONDUCTOR TSTRESI PRP	C7263 2648C 2648C 2848C 2848C 2848C 2848C 2848C C582C C582C	FU3369 1501-0044 1501-0044 1501-0044 1701-0044 2110-0460 1854-0360 203-08 1855-0073 203-08 283053
A1503 A1504 A1504 A1505 A1505	1205-0033 1053-0012 1205-0033 1654-0_12 1205-0061	2 	HEAT SINY SEMICONDUCTUR TSIMISI PNP HEAT SINKISEMICONDUCTOR TSIMESI NPHISELECTED FRUM 2N3440) HEAT SINKISEMICONDUCTOR	C582C 80131 U582C 28480 C582C	707-CR 2N2Y04A 2C7-CR 1854-C232 209-CM
A15Q6 A15Q7 A15Q8 A15Q9 A15Q1Q	1853-CC2C 1854-CC71 1854-0474 1854-0071 1853-0020	2	TSTRESI PAPISELECTEU FROM 2N3702) TSTRESI APRISELECTEO FROM 2N3704) TSTRESI APRI TSTRESI APRISELECTEO FROM 2N3704) TSTRESI PAPISELECTEO FROM 2N3702)	28480 28480 28480 28480 28480 28480	1 #53-C02C 1 #54-0071 1 #54-C674 1 #54-0071 1 #53-0020
A15F1 A15F2 A15F3 A15F4 A15F5	0683-2735 0683-1015 0683-1015 0683-1925 0683-1925	3	.IFRO COMP 27K UNH 5% 1/4W RIFRO COMP 100 CHM 5% 1/4W RIFRO CYMP 100 CHM 5% 1/4W RIFRO COMP 1930 CHM 5% 1/4W RIFRO COMP 1900 CHM 5% 1/4W	01121 01121 01121 C1121 C1121	CB 2735 CH 1G15 CB 1G15 CB 3925 CB 3925
A1586 A1587 A1588 A1589 A1589	0685-1305 0643-6815 0A63-6815 0A63-1325 210C-2793	1 2 2	NIFKO COMP 13 OHM 5% 1/2W RIFKO COMP 660 DHM 5% 1/4W RIFKO COMP 660 IHM 5% 1/4W RIFKO COMP 1300 OHM 35% 1/4W RIFKO COMP 230 OHM 30% LIN 1/6W	C1121 01121 01121 C1121 28450	EB 1306 CB 6815 CB 6815 CB 1325 2100-2053
A15F11 A15F12 A15F13 A15F14 A15F15	C683~6815 C683~6815 2170~2093 C683~1325 7683~0275		RIFRD COMP AND OWN SE 1/4W RIFRD COMP AND OWN SE 1/4W RIFRD COMP 200 OWN 30E LIN 1/8W RIFRD COMP 2370 OWN 31 1/4W RIFRD COMP 237 OWN SE 1/4W	01121 01121 28490 01121 C1121	CB 6815 CB 6815 21C0-2073 CD 1325 CB 2765
A15816 A15817 A15816	0683-0275 9683-0275 9683-0275		RIFKO COMP 2.7 DHM 5% 1/4W RIFKO COMP 2.7 DHM 5% 1/4W RIFKO COMP 2.7 DHM 5% 1/4W	G1121 G1121 G1121	CB 2765 CB 2765 CB 2765
Alo	35327-60028	1	ROARD ASSYLCUNNECTOR ISERIES 1224A) ELDADED ON G5327-2002A BLANK ROARD)	2848C	C5327-6UC28
A16C1 A16C2 A16C3 A16C4	0180-2352 0180-2396 0180-1462 0180-2382	1 1 2	C1FXU ELACT 6000 UF +75-10% 15VDCW C1FXD ELECT 4000 UF +75-10% 15VDCW C1FXD AL ELECT 10 UF +50-10% 250VDCW C1FXD ELECT 700 UF +75-10% 30VDCW	26480 56289 56269 28480	0160-2352 350167-058(5E) 3501565250EJ4-058 0180-2382
ALSC5 AlsC6 AlsCR1 AlsCR2 AlsCR3	0180-2182 C160-2204 1910-0516 1910-0016 1910-0016		CIFXD ELECT 709 UF +75-10% 30YDCM CIFXD MICA 1GOPF 5% DIUDEIGE 60 WIY DIUDEIGE 60 WIY DIUDEIGE 60 WIY	78480 72136 26486 26486 28480	0180-2382 RUM15F101J3C 1510-0018 1510-0018 151C-0018
Albera Albers Albers Albers Albers Albers	1901-0028 1901-0028 1901-0029 1901-0029	2	DIGUEFSILICON 0.75A 400PJV OIDDEFSILICON 0.75A 4COPJV DIUDEFSILICON 600 PJV DIUDEFSILICON 600 PJV OIDDEFSILICON 600 PJV	04713 04713 28480 28480 28480	\$A1358-9 \$#1358-9 1901-0029 1901-0029 1901-0029

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Table 6-1. Replaceable Parts (Continued)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
Albery Alberie Alberie Alberie Alberie	1901-0029 1901-0415 1901-0415 1901-0415 1901-0415	٨	Uthuetsilicch bou Piv Olumetsilicch 50 Piv Ja Dindessilicch 50 Piv Ja Dindessilicch 50 Piv Ja Dindessilicch 50 Piv Ja	2848C 2848C 2848C 2848C 2848C	1901-0919 1901-0415 1901-0415 1901-0415 1901-0415
AISCRIATHRU AISCRIA AISCRIB AISPI AISPI	1910-4024 5565-0115 1854-0469	ì	hut (FSIGHED hot Assighed Diggergemanium 28V Cunhectur Assylls Contact Istrasi nen	80131 58480 58400	1919-0034 5060-0115 2N709
Alopi Alori Alori Alori Alori	0812-0021 0811-1732 0880-2645 0883-0825 0883-1025	1 1	HEFFO WW 0.47 NIM BE 3M REFFO COMP 200K DIM SE .5M REFFO COMP 8.2, DIM SE .5M REFRO COMP 8.2, DIM SE 1/4M REFRO COMP 1000 DIM SE 1/4M	28480 28480 01121 01121 01121	0812-0021 0812-1732 EP 2045 CB 8265 CR 1025
Aloro Alor7 Alors Alou1 AloxAl	QAB3-5115 C698-3153 Q757-Q439 182C-C196	ł	PEFRO CUMP 310 OMN 5F 1/AM REFRO MET FLM 3.83K UMN 1E 1/8M REFRO MET FLM 6.83K UMM 1E 1/8M ICELINEAR VULTAGE REGULATORI INPUT) NOT ASSICAFO	01121 28480 28480 28480	CN 0115 0098-1153 0757-0419 1820-0146
Aloxaz Aloxaz Aloxas	1251-1886 1251-1886	•	CUNNIPC IC-CONTACT (2×15) NOT ASSIGNED CONNIPC IG-CONTACT (2×15)	71785 71785 71785	252-15-30-340 252-15-30-3\" 252-15-30-340
AloxA5 AloxA6	1251-1486 1251-2134 1251-2134	•	CONNECTOREC (2318)36 CONTACTS CONNECTOREC (2318)36 CONTACTS	71785 71785 71785	252-18-30-340 252-18-30-340 252-15-30-340
BAXAIA PAXAIA DIAXAIA IIAXAIA	1251-1886 1251-2134 1251-2134		CONNERC 10-CONTACT (AA15) hOT ASSIGNED CONTECTORIPC (2X18)36 CONTACTS CONTECTORIPC (2X18)36 CONTACTS	71785 71785	252-18-30-340 252-18-30-340
ALSYAL2 ALSYAL3 ALSYAL4 ALSYAL5 ALSYALS	1251-1846		HOT ISSIGNED HOT ASSIGNED HOT ASSIGNED CONNERC 30-CONTACT (2x15) NOT ASSIGNED	71785	252-15-30-340
Aloxald Aloxald	1251-1886 1400-0776	ı	NOT ASSIGNED CONNERS 30-CONTACT (2XL5) CLAMPICABLE 4.0" DIA	71.785 2648C	252-15-30-340 1400-0776
A17	05327-40033	ı	NOT ASSIGNED BOARD A 1 THIS IS SENSITIVITY PRESCALER TSERIE: 14 ABA ILOADER A 5327-20033 BLANK BOARD).	28480	05327-60033
A18C1 A16CZ	0180-0228 C16C-2C49	2	CIFKO ELECT 22 UF 10E 15VOCH CIFKO CER FEED-THRU 5700 PF +80-20E	56285 2848C	1500226#901592-045 0160-2049
A18C3 A18C4 A18C5 A18C6 A18C7 THRU	0160-3879 0160-2049 0160-3228 0160-3879	3	CIFRO CER O.CL UF 20% LOQUOCM CIFRO CER FEED-THRU 5000 PF +80-20% CIFRO ELECT 22 UF 10% 15VOCM CIFRO CER O.OL UF 20% LOQUOCM	72982 2848C 50289 72982	8121-8112-X7R-103M 01c0-2044 15C0226X4015B2-0Y5 8121-8112-X7R-103M
Aleczy Alecze Alecze Alecze Alechi Alechi	0160-3878 0160-3879 0160-3878 1901-0050	21 2	CIFKD CER 1000 PF 20% LCCVDCH CIFKD CER 0.CL UF 20% LCCVDCH CIFKD CER 13C0 PF 20% LCCVDCH OIDDETS1 200 MA AT 1Y OLUDETS1 200 MA AT 1Y	80031 72987 80031 07263 07263	CV2057X7A102M 8121-h112-x7M-103M CV2057X7R102M FUA 6308 FUA 6308
ALBCR3 ALBCR4 ALBCR6 ALBCR6 ALBCR6 ALBCR6	1901-0040 1901-0040 1901-0040 1901-0040 2110-0430	2	DIDDEFSILLON 50 HA 30 MV DIODEFSILLON 50 HA 30 MV DIODEFSILLON 50 HA 30 MV DIODEFSILLON 50 HA 30 MV FUSEIL/IO AMP 125V	C7263 C7263 C7263 C7263 C7263 2848C	FDG1088 FDG1088 FDG1084 FDG1088 Z110-0436
A18F2 A18J1 A18L1 A18L2 A18G1	2110-0436 1250-0836 9100-1786 9100-1788 1854-5345	1 2	FUSE: 1/10 AMP 125V ISPARE) CONNECTORERF SUS-HINIATURE COILECHORE COILECHORE TSTRESS NPN	26180 98291 02114 02114 60131	2110-0436 5C-053-0000 VR200-10748 VR200-10748 ZM5175
A1002 A16R1 A1042 A10K3 A10K4	1854-0092 0883-1025 0683-1025 2698-5996 2100-2683 0683-3925	2	TSTRIST NPN RIFKD COMP ICOU OHM SE 1/4M RIFKD COMP ICOU OHM SE 1/6M RIVAL CENNET IK OHM ICE LIN 1/2M FIFKD COMP 3400 DIN SE 1/4M	80731 01121 28480 28480 01121	2h3563 CM 1C25 CA98-3596 21CO-2633 CB 3925

Table 6-1. Replaceable Parts (Continued)

Reference Designation		Qty	Description	Mfr Code	Mfr Fart Number
Aldro Aldro Aldro Aldro	COVE-1378 COVE-1318 COVE-111 COS-1625	5	PERKO CAMPON SE THM SE E/AM REFRO CARBON SE OHM SE E/AM REFRO COMP SO OHM SE E/AM REFRO COMP ECOL OHM SE E/AM	28480 24480 28480 01121	1698-378 6598-3378 6598-3111 60 1025 60 2015
ALURIO ALURIO ALURIZ ALURIZ ALURIS ALURIS	0683-2016 2100-2413 0692-6283 0683-1505 069 -3374	2 2 4	RIPRO COMP 200 DHM 5E L/4W RIVAR PLM 200 DHM 10E LTM L/2M RIPRO COMP 10 UMM 5E 1/8M RIPRO COMP 15 UMM 5E 1/4M RIPRO CARBON 20 DHM 5E 1/4M NUT ASSIGNED	26480 G1121 G1121 28480	7100-24' % 66 1005 C8 1305 0658-3374
Aldris Aldris Aldris Aldris Aldris Aldris	0648-5180 6498-3378 6698-3374 6683-4325 6198-5180		RIFRO CUMP IN OHM ST 1/86 RIFRO CARBON S1 UNM ST 1/8M RIFRO CAPBUN 20 UNM ST 1/6M RIFRO COMP 430 UMM ST 1/4M RIFRO COMP 2K UMM ST 1/6M	28480 2 * * * 2840 C1121 28480	QA94-51 JG QA58-33 78 QA58-37.74 CB 431 J QA58-F180
Almp7C Almp7l Almp22 Almp23 Almp24	0678-3111 C678-5996 C698-4131 C698-4131 C698-3111	4	RIPND COMP 30 DHM 3% I/8W RIFND COMP 360 DHM 3% I/8W RIFND COMP 56 DHM 5% I/8W RIFND COMP 56 DHM 5% I/8W RIFND COMP 30 UHM 5% I/8W	26490 2848G 2848G 2848G 2848G	0494-3111 0498-5596 049/-131 0464-4131 0494-3111
ALDR25 ALDR26 ALGR27 ALGR26 ALGR26	CAR3-1025 CAM/+1015 2100-2413 CAPE-6283 0608-5177	2	REFXD COMP 1000 OHM ST 1/4W B:FXD COMP 100 OH'3 5% 1/4W FACTORY SELECT REVAR FLP 200 OHM 10T LIN 1/2W REFXD COMP 10 OHM ST 1/4W REFXD COMP 820 OHM ST 1/6W	01121 01121 25480 01121 28480	F,B 1025 CB 1015 2100-2413 BB 1005 0008-8177
ALEP30 ALEP31 ALEP33 ALEP33 ALEP34	CAMP-5103 CAMS-1505 CAMS-3374 0698-8073	ì	REFED CUMP 430 GMP 5% 1/8W REFED CGMP 15 GMM 5% 1/4W NOT ASSESSED FEFFD CARBON 20 GMM 5% 1/8W REFED CUMP 1800 GMM 5% 1/8W	28480 01323 26490 28480	0698-5103 \$B 1505 0698-1374 0608-8073
ALRF35 ALBF36 ALBH37 ALBF38 ALBF39	C098-3378 C098-3311 U048-3378 C698-3374 C683-4313		RIFXD CARRON 31 CHM 38 1/RW RIFXD CLMP 30 CHM 38 1/BH RIFXD CARRON 31 CHM 38 1/BH RIFXD CARRON 25 CHM 38 1/BW FIFXD COMP 430 CHM 38 1/4W	28480 28480 28480 28480 01121	0698-3378 0698-3111 0698-3316 0698-3174 0898-3174
A18F41 A18F41 A18F42 A18F43 A18F44	0698-4131 C698-5503 C698-4131 C698-3111 0698-3113	ı	REFEC COMP 56 OHM 5% 1/8W REFEC CARNON 180 UHM 5F 1/8W REFED CUMP 56 OHM 5% 1/8W REFED COMP 36 OHM 5% 1/8W REFED CANBON 180 UHM 5% 1/8W	24480 28480 28480 28480 28480	0698-4131 0698-5563 0698-313 0698-3113
A10F45 A10F46 A10F47 A10F48 A10F49	C68'-1025 C683-3975 O683-8215 G683-1025	1	NOT ASSIGNED FEFRU CUMP ICOO ONN 5% 1/4W REFRO COMP 39 ONN 5% 1/4W REFRO COMP 82C OMM 5% 1/4W REFRO COMP ICOC ONN 5% 1/4W	01121 01121 01121 01121	CB 1025 CB 3905 CB 8215 CB 1025
Alurbo Albrbl Albrb2 Albrb3 Albul	C683-4315 C683-4315 C683-4315	1	REFEC COMP 43G OHM 52 1/4W RIFED COMP 43G OHM 52 1/4W RIFED COMP 43G OHM 52 1/4W NOT ASSIGNED ICH INTER	01121 01121 01121 28480	CB 4315 CB 4315 CB 4315
A18U2 A16U3 A18U4 A18U4OR A18U5	1826-0084 1826-0085 1820-0736 1820-0558 1820-0514	 	ICELINEAR ICELENEAR ICEOIGITAL ICEOUAL-BINARY ICEOIGITAL QUINARY DEVENER	2648C 2848C 2648C 2848C 2848C	1826-0085 1820-0085 1820-0736 1820-0736 1820-0714
VIENA VIENA VIENA	182G-G489 1221-CC01 1821-CC01 182C-UEC2	2	ICIECL TRANSISTOR ARRAPISE NPM TRANSISTOR ARRAPISE NPM ICIEC, QUAD 2-INPT NOR GATE	28480 02735 02735 04713	1620-6487 CA3346 CA3046 MG10107L
BL BL BL GL	3140-0030 3150-0039 3160-0033 52124-128 0160-3043	1 1 1	CHASSIS PARTS MOTORISMORED POLE FILTERIAIR FANITAPELLER AKTAL 2-1/4 DIAM BRACKETIFAN CITKU CER 2 X D.005 UF 20X 250VAC	28480 28480 04870 28480 56289	3140-0030 3150-0039 2 1/4 MHF 125 5 52124-128 2901478-00H
F1 F1 J4 J5	2110-0020 2110-0304 1400-0084 1250-1253 1250-1257	1 1 3	FUSEEC. BA 250V SLOW-BLOW FUSEECAPTRICGE 1.5 AMP 250V SLOW-BLOW FUSEHOLOBERETRACTOR PUST TYPE CONNECTURINE BNC MOUNT JACK CUNNECTORERE BNC MOUNT JACK	75915 71400 75915 24931 24931	313,4005 MDX-1-1/2A 342014 26JR194-1 28JR194-1
J) J7 J8 J11 Q1	1250-1243 1251-7357 1250-0212 1653-0233	1	CONNECTORING AND MOUNT JACK PARY OF 05927-60011 PRESCALER CABLE ASS SOCKETS3-91A MALE POWEP RECEPTACLE CONNECTORIJACK CHASSIS BNC TSTRISI PAP	24931 82389 95712 01295	28JR144-1 EAC-301 30469-1 TIP 32

See introduction to this section for ordering information

Table 6-1. Replaceable Parts (Continued)

Reference HP	Part Number	Qty.	Description	Nifr Code	Mfr Part Numbe
	327-20024	,	HEAT BINK FOR US AND US	26+80 21-480	0532/-20024 1854-0570
ii 1 21	54-0420 30-2901 160-67403	1 2	TSTRESE NPA. HEVAR COPP L MEGONN LCS TO CLOG 174M NNOH ASSY DAKY CF PL	25460 25460	3100-5491
	01-1327	2	SMITCH-SLILE OPIT 0.5% 125V AC/INC	79727	41285-444n
l l	01-1216	ı ı	SHITCHEPUSH FOON SPS).	82385	#S=\$4354
A	01-1327		SWITCHISLINE DOST 0.5A 126's AC/UC (632/0-IAPUT SELECT)	19727	G1205-0001
	326-6018 77-0104	1	L SHETCH ASSNETEME BASEEW MEDD NAUBEHLK BUR WYAFROW EVAM SHAFT	25480 26486	05326~60018 0370~6134
	37 >- 60038	1	SHIFT BASE! SHIFT BASE!	20480	02326-60038
	170-0104		KNOBEBLE BIR MYARROW E/44 SHAFT	28486	G 370-C1:34
7 31	101-1311		SELECTIONS	19721	G126+7020
n N	121-1211		EDSC≃1MEZEZZE SWITCH:SLIDH DPDT 7,8A 125V AC/LC (STOHAGC)	19721	G120-0020
ig 31	101-1234	1	SWITCH SLIDE UPDT 15ELICIDE 1157/25CV)	82385	114-1742
·	100~1026 100~1147 140-0765	1 1 2	TRANSFORMER POWER BUSHING: TRANSISTOR INSULATOR: TRANSISTOR	28460 28460 28480	9100-3020 1200-0147 0340-0718
			OTHER CARENET PARTS		
4	160-0114	1	TAPETPOLYUNETHANE 1-374 IN WIDE	85471	TL SAMCLE +2
	190-0030 120-1254	l l	(FOR TEP COVER) STANDSFILT THADEHARM	284HG 284EC	1490-6030 7120-1254
ļ sie	100-0050	2	THE CACH	28485	5000-6450
	760-3729 760-3767 1821-67401	, 5	FFAME ASSYLS & LLUSTOLI FOUT, ASSYLFM ANCHITHIGGER LEVEL	2848C 2048C 2848C	5060-5729 5060-0767 61821-67461
2:	3326-99632	ı	PANEL SIGRAM	2848G	U5327-C0G32
23	326-0000	ı	(ASULATOR TUN BOTTON COVER)	28480	22759-30709
01	11200-056	2	FLATEICIBNECTUP, LUNG 190 A JIO COVER!	28480	C3326-30011
c:	120-0014	l.	PANEL FF GOTT	28486	05326-03014
יַנ	326-20039	l i	43 NCW, (532ec)	28480	05326-20029
0;	5)27-20001	,	#\$#DC# (\$327C)	28480	99127-21001
			PAINTED CABINET PARTS		
) 1 C 6 - C 0 C 2 7 3 2 6 C 0 0 2 8	1	LEFT FRUNT PANEL TRIN(5326C) RIGHT FRONT PANEL TRIM	26480 28480	G5326-00027 06325-00028
0	326-CC924 5326-00030	2	SIDE COVER TOP COVER	2849G 28480	65326-00039 05326-00030
\ \	5726-00046	l l	KITEFACK MOUNT LUNSISTING UP:	26460	05326+66046
			2370-0012 8-32 X .250 t.G. (3) 2510-004 8-32 X .436 t.G (4) 5020-7619 LEFT BRACKET (1) 5020-7620 RIGHT BRACKET (1)		
· ; :	5327-02010	1	05326-40003 FILLER STPIP (1) LEFT FACHILPASEL TRING 2270)	26460	05327~3031F
	2330-00023	1	BOTTCH COVER	2846C	05330-00033
i '		•			
			:		
•	,		'	1	1

Table 6-1. Replaceable Parts (Continued)

Reference Designation	HP Part Number	Qty.	Description	Mfr Code	Mfr Part Number
			ENTERNIC AND OTHER PARTS		
	d120-1378 3040-017J	1 1	CARLE ASSYLAC POWER CUPD GUIDELPLUG-IN PC BDARD (ALS ROARD SUPPORT) CONNECTIALLS CONTACTS	70903 26460 28460	KH-708) 5C40-017C 5060-0109
	9060-0107 25326-00010 05326-00010 05326-20046 45327-60037	! !	SHELDINERE CHASES HORDINEANK EMEAR PANEL INTERCONNECTS CABLE AS: PRESCALER	2848C 28480 2848C 2848C	05376-CCClu 05376-C0018 05376-20046 05377-60037
	05327-00057	•	OPTION OULTS DIGIT DISPLAY) DELETE AY 05326-6000% AND REPLACE		
19	U5326-64023	ı	MITH A9 05326-600216 DISPLAY ASSY (SERIES 1724A) ILOADED ON 05520-70008/75 BLANK (IOARD)	26480	13326-61175
19051 19052 19053	1570-0042 1970-0042 1970-0042		TUBERNINERSCAL INDICATOR TUBERNINERSCAL INDICATOR TUBERNINERSCAL INDICATOR	83594 83594 83594	H-5794-5 B-3756-5 R-5756-5
19054 19055 19056 19057 19058	1470-0042 1970-0042 1970-0042 1970-0042 1970-0042		TUNEEMUNERICAL L'IDICATON TUBEEMUNERICAL INDICATOR TUBEEMUNERICAL INDICATOR TUBEEMUNEFICAL INDICATOR TUBEEMUNERICAL INDICATOR	83594 83594 83594 83594 83594	8-9790-5 8-5790-5 8-5750-5 8-9750-5 8-9750-7
1983 1982 1983 1944 1985	0683-1026 0767-0946 0683-1026 0767-0945 0767-0946	6	R: FXD COMP 1000 OH/A 5% 1/4W R: FXD FLM 7500 OHM 2% 1/4W R: FXD COMP 1000 OHM 5% 1/4W R: FXD FLM 7500 OHM 2% 1/4W R: FXD FLM 7500 OHM 2% 1/4W	C1121 28480 01121 29480 28480	CB 1025 0767-0946 CB 1025 0767-0946 0767-0946
1986 1987 1988 1988 1981	0757-0945 0787-0945 0757-0945 0757-0945		R: FXD FLM 7500 OHM 2% 1/4W R: FXD FLM 7500 OHM 2% 1/4W R: FXD FLM 7500 OHM 2% 1/4W R: FXD FLM 7500 OHM 2% 1/4W - ,T ASSIGNED	29450 78460 26460 28460	0767-0946 0767-0945 0767-0946 0767-0948
ASF11 ASR12 ASU1 ASU2 ASU3	0757-0945 0763-1005 1820-0275 1820-0119 1820-0119		R: FXD FLM 7500 OHM 2% 1/4W R: FXD COMP 10 OHM 5% 1/4W IC: FCL 1G TIL QUAD 2-INPT UR-TRANS. IC: FTL BLANKING DECADE COUNTER IC: FTL BLANKING DECADE COUNTER	28480 01121 04713 28486 28480	0757-0046 CB 1905 HC1035P L02C-0119 182C-G119
A9U4 A9U5 A9U6 A9U8 A9U8	1820-0119 1820-0119 1820-0119 1820-0119		ICETTE BEARKING DECADE COUNTER	2848C 2848C 2848C 2848C 2848C	1820-0119 1820-0115 1820-0115 1820-0115 1820-0119
A9010 A9010 A9011 A9012 A9013	1820-0116 1820-0116 1820-0116 1820-0116		IC44-BIT BUFF STURE GATED OUTS IC14-BIT BUFF STORE GATED OUTS	2848C 2848C 2848C 2848C 2848C	1820-::116 1820-:116 1820-:116
A9014 A9015 A9016 A9017 A9018	1820-0116 1820-0116 1820-0116 1820-0729 1820-0092		IC:4-BI: BUFF STORE GATED DUTS IC:4-BIT BUFF STORE GATED DUTS IC:4-BIT BUFF STORE GATED OUTS INTEGRATED CIRCUITIDECOURN-DIVIDER INTEGRATED CIRCUITIDECODER-DIVIDER	2848C 2848C 2848C 2848C 2848C	1820-0116 1820-0116 1820-0126 1820-0272 1820-0052
A9019 A9020 A9021 A9022 A9023	1#20-0092 1#20-0092 1#20-0092 1#20-0092		INTEGRATED CIRCUITIDECODER-DIVIDER INTEGRATED CIRCUITIDECODER-DIVIDER INTEGRATED CIRCUITING OBER-DIVIDER INTEGRATED CIRCUITING OBER-DIVIDER INTEGRATED CIRCUITING OBER-DIVIDER	2648C 2548C 2648C 2648C 2648C 2648C	1620-0052 1270-0052 1270-0092 1620-0052

Table 6-1. Replaceable Parts (Continued)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
A9U. 4 A92051 A92052 A92053 A92054 A92056 A92056 A92056 A9206 A9206	1#20-cq92 1200-cq95 1200-cq95 1200-cq95 1200-cq95 1200-cq95 1200-cq95 1200-cq95 1200-cq95 1200-cq97 1200-cq77		INTEGRATED CINCUITEDECUCEP-DIV.DER SOCHETTURE FOR 5700 SERIES SOCHETTURE	2648C 83594 83594 83594 83594 83594 83594 28480 28480	1#20-092 5# 207 5# 207 5# 207 5# 207 5# 207 5# 207 5# 207 1200-0477 1200-0477
J10 W2P1 W2P2 W2P2	06327-80013 1261-0065 5060-0113 1200-0063 06326-00033 1261-0067 1261-2262	1 1 1 1 2	OPTION 002 (REMOTE PROGRAMMING) DELETE 5060-0109 15 CONTACT CONNECTOR AND ADD 06327-50013 CABLE ASSY CABLE ASSY: FROGRAMMING CONSISTING OF: CONNECTOR: FEMALE 3C-PIN MINAT CONNECTOR: A5 CONTACT LUG: CRIMP ADAPTER: CONN OPTION 003 (CIGITAL OUTPUT) ADD 06326-60312 CABLE ASSY CONNECTOR: FEMALE 50-PIN MINAT CONNECTOR: FEMALE 50-PIN MINAT CONNECTOR: PC 12 X 101 20 CONTACTS CONNECTOR: PC 12 X 101 20 CONTACTS	78480 28480 28480 28480 28480 28480 76530 76530	06327 60013 1751-0065 5060-0113 1200-0063 06326-00033 1751-0087 261-10-30-400 261-10-30-400
	0060-0318		CPTION 010 (HIGH-STABILITY CSCILLATOR) DELETE AA OSCILLATOR ASSY (6526-60002) AND ADD 05327-80036 0PT 010. (LOAL *D ON 06327-20036 BLANK BOARD) TCXO OPTION 011 (HIGH-STABILITY OSCILLATOR) DELETE A4 OSCILLATOR ASSY (06326-60002) AND ADD 10644-60011, OPT 011.	28480	0060-0318

Table 6-2. Manufacturers Code List

HFP			219
NO.	MANUFACTUPER NAME	ADDRESS	CODE
	NO H/F DESCRIPTION FOR THIS MFG NUMBER NO M/F DESCRIPTION FOR THIS MFG NUMBER U.S.A. COMMON ALLEN MRADLEY CO. TENAL TRETRIBUTES INC. SENICONQUICTOR COMPONENTS DIV.		
ยอกดอ	NO MIF DESCRIPTION FOR THIS HFG NUMBER	AND CHARLES OF U.S.A.	
00000	U.S.A. COMMON	ANT SUPPLIER UP U.S.A.	E2 104
01121	ALLEN BRADLEY CO.	HILWAUKEE, HID.	75127
01295	15442 M214045412 Met 25446000000100 com outure 1114	A A	12177
02114	FERROXCUBE CORP. RCA SOLID STATE & RECEIVING TUBE DIV. MOTOROLA SENICONDUCTOR PROP.INC. P M HOTOR CO. WAKEFIELD ENGINEERING INC. FAIRCHILD CAMERA & INST. CORP. SEMICONDUCTOR DIV.	CAUGERILES, N. V.	00074
02735	RCA SOLID STATE & RECEIVING TUBE DIV.	WHERVILLE, N.J.	05010
04713	MOTOROLA SENTCONDUCTOR PRODIENC.	PHUENIX AKIZ.	40144
04870	P M HOTOR CO.	MESICHESTER, ILL.	00130
05820	WAKEFIELD ENGINEERING INC.	WAKEFIELD, MASS.	01907
07263	FAIRCHILD CAMERA & INST. CORP. SEMISONDUCTOR DIV.	HOUNTAIN VIEW, CALIF.	74940
08806	G.E. CO. MINIATURE LAMP DEPT. NATIONAL SEMICONDUCTOR CORP. GULTON IND. INC. DATA SYSTEM DIV. CORNELL DUBLIER ELECT. DIV.FEDERAL PACIFIC ELECT. CO.	CLEVELAND, OHIO	44112
12040	NATIONAL SEMICONDUCTOR CORP.	DANBURY, CONN.	06810
12574	GULTON IND. INC. DATA SYSTEM DIV.	ALBUQUERQUE, N.M.	87108
14655	CHRNELL DUBLIER ELECT. DIV. FEDERAL PACIFIC ELECT. CO.	NEWARK, N.J.	07105
17856	STLICONIX INC.	SUNNYVALE, CALIF.	94086
24931	SPECIALTY CONNECTOR CO. INC.	INDIANAPOLIS, IND.	46227
28480	HEMLETT-PACKARD CO. CORPORATE HQ	YOUR NEAREST HP OFFICE	
56289	SPRAGUE ELECTRIC CO.	N. ADAMS, MASS.	01247
70903	BELDEN COPP.	CHICAGO, ILL.	60644
7140C	RUSSMANN HEG. DIV. HC GRAN-EDISON CO.	ST. LOUIS, MO.	63017
71785	CINCH HEG. CO. DIV TRW INC.	ELK GROVE VILLAGE, ILL.	•
72136	FLECTRO MOTIVE MFG. CO. INC.	WILLIMANTIC, CONN.	06226
72982	ERIE TECHNOLOGICAL PROD. THC.	ERIE, PA.	16712
75915	LITTELFUSE INC.	DES PLAINES, ILL.	60016
79727	CONTINENTAL-WIRT ELECTRONICS CORP.	WARMINSTER, PA.	18974
80031	MEPCO DIV. SESSIONS CLOCK CD.	HORRISTOWN, N.J.	07960
80131	FLECTRONIC INDUSTRIES ASSOCIATION	WASHINGTON D.C.	20006
B2142	AIRCO SPEER ELECT. COMP.	OU BOIS, PA.	15801
82369	SWITCHCRAFT INC.	WARMINSTER, PA. HORRISTOWN, N.J. WASHINGTON D.C. DU BOIS, PA. CHICAGO, ILL.	60630
83594	BURROUGHS CORP. ELECT. COMP. DIV.	PLAINSFIELD, N.J.	07061
85471	CMRNELL DUBLIER ELECT. DIV.FEDERAL PACIFIC ELECT. CO. STLICONIX INC. SPECIALTY CONNECTOR CO. INC. HEWLETT-PACKARD CO. CORPORATE HQ SPRAGUE ELECTRIC CO. BELDEN CORP. BUSSMANN HFG. DIV. HC GRAW-EDISON CO. CINCH HFG. CO. DIV TRW INC. ELECTRO MOTIVE MFG. CO. INC. ERIE TECHNOLOGICAL PROD. THC. LITTELFUSE INC. CONTINENTAL-WIRT ELECTRONICS CORP. MEPCO DIV. SESSIONS CLUCK CO. ELECTRONIC INDUSTRIES ASSOCIATION AIRCO SPEER ELECT. COMP. SWITCHCRAFT INC. BURROUGHS CORP. ELECT. COMP. DIV. BOYD A.B. CO. PADIO MATERIALS CO. BENOIX CORP. THE MICROWAVE DEVICE DIV. SAN FERNANDO ELECT. MFG. CO. SEALECTRO CORP.	SAN FRANCISCO, CALIF, CHICAGO, ILL. FRANKLIN, IND.	94103
91418	PADID MATERIALS CO.	CHICAGO. ILL.	60646
95712	RENOIX CORP. THE MICROWAVE DEVICE DIV.	FRANKLIN. IND.	46131
96733	SAN FERNANDS ELECT. NEG. CO.	SAN FERNANDO, CALIF.	91341
98291	Die i Fullum Prenie ill de Ann	MANAGER N U	10544

CECTION VII

OPTIONS AND MANUAL CHANGES

7-1. INTRODUCTION

7-2. This section contains information necessary to adapt this manual to older instruments. Also included is the operating and installation information for available options. Refer to Section II for remote programming requirements.

7-3, MANUAL CHANGES

7-4. This manual applies directly to Model 5326C and Model 5327C having serial prefix 1312A (refer to Paragraph 1-4).

7-5. Newer Instruments

7-6. As changes are made, newer instruments may have serial prefixes that are not listed in this manual. The manuals for these instruments are supplied with a manual change sheet, containing the required information. Contact the nearest Hewlett-Packard Sales and Service Office for information if this sheet is missing.

7-7. Older instruments

7-8. To adapt this manual to instruments having a serial prefix prior to 1312A for the 5326C or the 5327C, perform the backdating that applies to your instrument's serial prefix, as listed in the table below.

Soria	l Prelix	Perform Change
5326C	5327C	
	1236A	11
1224A		10
	129/A	11, 10
1204A		10, 9
	1220A	11, 10, 9
1140A		10, 9, 8
	1136A	11, 10, 9
1136A		10, 9, 8, 7
	1124A	11, 10, 9, 8, 7, 6, 5, 3
1120 A	-	10, 9, 8, 7, 6, 5, 4, 1
	1040A.	11, 10, 9, 8, 7, 6, 6, 3, 2, 1
1036A		10, 9, 8, 7, 6, 5, 4, 2, 1

CHANGE 1

Page 6-7, Table 6-1: Delete A7R24 from parts list.

Page 8-25, Figure 8-10:

Delete A7R24 from schematic and component

Change series number on A7 schematic to read 1040A. Rev. B.

CHANGE 2

Page 6-7, Table 6-1: Delete A7R23 from parts list.

Page 6-10, Table 6-1: Replace A15 parts list with Table 7-1.

Page 6-14, Table 6-1:

Change part numbers to read:

COVER-SIDE 3 x 11 5000-0729, 2 ca. COVER-TOP 05325-00008, 1 ca. INSULATOR (Q1 & Q2) 0340-0162, 2 ca. (Replaces Heat Sink)

Page 8-25, Figure 8-10:

Delete A7R23 from schematic and component

Change series number on A7 schematic to read 1036A, Rev. A.

Page 8-35, Figure 8-15:

Replace A15 schematic with Figure 7-1.

Replace A15 component locator with Figure 7-2.

CHANGE 3

Page 6-11, Table 6-1:

Change A16 Bd. Assy. Connector P/N to read 05327-60014, series number 1040A.

Page 8-35, Figure 8-15:

Change A16 Bd. Assy. Connector P/N (top of schematic) to read 05327-G3014, series number 1040A.

CHANGE 4

Page 6-11, Table 6-1:

Change A16 Bd, Assy. Connector P/N to read 05326-60037, series number 1036A.

Page 8-35, Figure 8-15:

Change A16 Bd, Assy. Connector P/N (top of schematic) to read 05326-60037, series number 1036A.

CHANGE 5

Page 6-10, Table 6-1: Replace A1 parts list with Table 7-2.

Page 8-33, b., ure 8-14: Replace A11 schematic with Figure 7-3.

Replace All component locator with Figure 7-4.

CHANGE 6

Page 6-6, Table 6-1:

Change A6R19 to read 0683-2025, 2000 ohms 5% WW, Mfr. P/N is CB 2025. A6 series number is 1036A.

Page 6-7, Table 6-1:

Change A7C1 to rend 0160-2198, 20 pF, 300 VDCW

Change A7C2 to read 0160-0333, 15 pF, 300 VDCW

Change A7R10 to read 0683-2025, 2000 ohms 5% WW, Mfr. P/N is CB 2025. A7 series number is 1120A.

Page 8-21, Figure 8-9:

Change value of A6R10 to 2000 ohms.

Change series number (top of schematic) to rend 1036A.

Page 8-25, Figure 3-10:

Change value of A7C1 to 20 pF.

Change value of A7C2 to 15 pF.

Change value of A7R10 to 2000 ohms.

Change series number (top of schematic) to rend 1120A.

CHANGE 7

Page 6-14, Table 6-1: Change T1 9100-3020 to 9100-2888

CHANGE B

Page 6-11, Table 6-1:

Delete A15C8 part number and description. Change A15R17 and R18 to 0683-0395, 3,9 ohms, Mir. P/N is CB 39G5. Series number of board is now 1040A (page 6-10).

Page 8-35, Table 6-1;

Delete A15C8 from schematic.

Change value of A15R17 and A15R18 to 3.9

Change A15 series number (top of schematic) to rend 1040A.

CHANGE 8

Page 6-3, Table 6-1:

Change A1 05326-60048 to 05326-60030.

Change A1 blank board number to 05326-20030. Change A1R12 2100-3228 to 2100-2905, Series number is now 1036A.

Page 6-14, Table 6-1:

Change 05326-00032 (rear panel) to read

05326-00004.

Change quantity of 05326-60011 to 1 ea, and description to "Plate:Connector, Long (J9 cover)."

Add "05326-60012 Plate:Connector, Short (J10 cover)."

Page 6-15, Table 6-1:

Change 05326-20046 Blank Board part number to 05326-20028.

i ii.i.

Page 6-16, Table 6-1:

Delete part number and description of 053261 20033 Adapter.

Page 8-15, Figure 8-6:

Change AI part number (top of schematic) to 05326-60030, Series 1036A.

CHANGE 10

Page 6-8, Table 6-1: Change A9 series number to 1032A. Change blank board number to 05326-20008.

Page 6-9, Table 6-1:

Delete the following parts: A9R11, A9R12, A9XDS8, A9XU8, A9XU16, and A9XU24.

Pages 6-11 and 12, Table 6-1;

Change A16 series number to 1132A.

Delete the following parts:

A16C6, A16CR18, A16Q1, A16R4 through A16R8, and A16U1.

Pages 6-15 and 16, Table 6-1:

Change A9 (Option 001) series number to 1032A. Change blank board number to 05326-20025. Add A9R10 0683-1005, 10 ohms, 5%, 4W. Delete the following parts:

A9R12, A9XU8, A9XU16, and A0XU24.

Page 6-16, Table 6-1:

Delete all reference to Option 010 and Option 011.

Page 7-21, Paragraph 7-23: Add the following parts: 0683-7525, 7500Ω, 5%, ¼W

1200-0405 Display Tube Socket

Replace Step e. with the following statement:

"R10 carries the overflow information from the decades and can be placed in one of two positions. Move R10 to position B, as shown in the component locator. In this location, R10 connects to pir. 8 of U8. Replace boards in instrument." The component locator mentioned above is found in Section VII.

Page 8-29, Figure 8-12:

Replace A9 schematic with Figure 7-5.

Replace A9 component locator with Figure 7-6.

Page 8-39, Figure 8-17:

Delete Option 010 and 011 schematic and component locator.

CHANGE 11

Page 1-3, Table 1-3:

Change Range specifications to read:

Direct: 1 kHz to 50 MHz.

Prescaled: 0 to 550 MHz.

Change Sensitivity specifications to read:

Direct: 5 mV rms.

Prescaled: 100 mV rms.

Page 6-16, Table 6-1:

Replace A18 parts list with either Table 7-3 for A18 05327-60009 or Table 7-4 for A18 05327-Both assemblies verform the same function, and either assembly may be supplied with the instrument.

Page 8-37, Figure 8-16:

Replace A18 schematic and component locator with either Figures 7-7 and 7-8 for A18 C5327-60009 or Figures 7-9 and 7-10 for A18 05327-Both assemblies perform the same function, and either assembly may be supplied with the instrument.

Add Figure 7-11 - A19 schemutic, component

locator, and parts list.

Add Figure 7-12 and Table 7-4.

Table 7-1. A15 Replaceable Parts Lists

Reference Designation	HP Part Number	Qty	Description	Mf* Code	Mfr Part Numbe
ALS	05326-60001		REGULATOR ASSY	28480	05376-60001
AID	05320-20001	, ,	BOARD BLANK	1	
		h l	CIEXO NY 0.033 UF 10% 200VDCH	56247	197911197-915
A15C1	0160-0163	•	CLEAN FLECT 4.0 UF +100-10E 2540CM	24480	0180-0114
A15C7 A15C3	0160-0114		CIFED ELECT 4-0 UF +100-10% 25VOCW	28480	0140-0114
-	0.00-0.14		CIFED ELECT 4.0 UF +100-10% 25VDCW	28480	0180-0114
A15C4	0180-0114		CIFED ELECT 4.0 UF +100-10% 25VOCH	28480	C180-0114
A1505	0160-0975	2	CIFID CTR Q.QQL UF ZOT 75VOCM	12574	554-4001-99
A15C6 A15C7	0160-0975		CIFED CER G.OOL UF ZOW 75VOCW	12574	\$5M-,001-98 1402-3002
ALECAL	190%- 3002	2	DIOGE BREAKOGNAFZ.37V 38	28480	1405-1005
*	1502-0551	2	DI 30E BREAKDPWNIG.19V 5%	28480	1702-0551
ALSCAZ Alscas	1902-2551	•	DIODE BREAKDOWNIG. 194 5%	28480	1902-0551
ALICKI ALICKA	1405-2005		CIODE BREAKDOWNS2-374 5%	28480	1902-1002 FDG1088
ALSCRS	1901-0040		DIDDEESILICON SOMA SOMY	01763	1907-3094
ALSCRO	1902-1094	4	DIGGE BREAKDOWN:5-11V 2%	28480	1746-1414
	1902-3054		DICOF BREAKDOWNES-119 2%	28480	1902-1094
ALSCR? Alscrb	1901-0040		DIDDEISILICON TOMA TOMY	07763	FOGLONA
#15C#9	1902-3094		DIDDE BREAKDOWN:5-11V 28	28460	1902-1994
ALSCRIO	1902-3094		DIDDE BREAKDOWN:5.11V 2%	28480	1902-1094 1902-1394
ALSCRIL	1902-3394	1	DIODE BREAKDOWNETS V 2%	78480	1702~131-
	1902-3429	ı	DIODE BREAKDOWN:100 V 2%	28489	1907-3479
A15CR12 A15F1	2110-0331	;	FUSEIG. JA	71400	GMW 1/10
ALSEZ	2110-0131	-	FUSTEO, 3A	71400	GMH 3/10
WESTER	1251-1636	4	SOCKET CONNECTOR FOR FI and F2	60131	2N1051
-1901	1854-0039	2	TSTREST NPN	40131	2H2904A
A1502	1853-0012	2	TSTRIST PHP	70,,,	•
A1503	1854-0039		ESTRESE NPN	40133	2N3053 2N2404A
A1504	1853-0012		TSTREST PNP	80133	1854-0212
A1505	1854-0232	ı	ISTRIST NPHISEL*CTFD FROM 2N34401	29480	1674-0717
-,500	1205-0033	4	HEAT SINK FOR Q1, Q2, Q3, Q4		
	1205-0061	1	HEAT SINK FOR CO TSTREST PROTESTED FROM 2N37021	28480	1653-0070
A1506	1653-0020	2	TSTRIST PROFESERACIED FROM 2037021	28460	1854-0071
A1507	1034-0071			28480	1854-0071
A1508	1854-0071		ISTRES HPHISELECTED FROM 2N3704)	28480	1854-9071
A1509	1854-0071		TETRES NPMISELECTED FROM 2N3704) ISTRES PNPISELECTED FROM 2N3702)	26489	1853-0770
A15010	1853-0020		RIFKD COMP ZON DHM 5% 1/4W	61121	CB 2035
ALSR1 AlsR2	0683-2035 0683-1015		REFED COMP 100 DIM 5% 1/4W	01121	CR 1015
- 1 / · · · ·				01121	CB 1015
ALSRI	Q683-1015	_	RIFKO COMP 100 DHM 5% 1/4W RIFKO COMP 3900 DHM 5% 1/4W	01121	CH 3924
ALSR4	0663-3925	2	RIFKO COMP 3900 DHM 5% 1/4M	oiizi	CH 1925
ALSES	0683-3925	1	RIFRO COMP 15 DHM 5% 1/4M	01121	CB 1505
ALSRO ALSR7	0683-1025	•	RIFED COMP 1000 OHM 5% 174W	01171	CB 1075
•			RIFED COMP LOOD OHM 5% 1744	01121	CB 1025
ALSKS	0683-1075	2	RIFED COMP 1300 DHM 5% 1/AM	01171	CB 1325
ALSES	0683-1325 2100-2093	5	REVAR COMP 200 OHM 30% LIN 1/4H	28480	\$100-5041
ALSRIO Alsrii	0683-6815		RIFED COMP AND DHM 5% L/4M	61151	CB 6015
AISRIZ	0683-6815		RIFED COMP 680 DHM 5% 1/4W	01151	CB 4815
	· ·		REVAR COMP 200 OHM 30% LIN 1/8H	28480	2100-2093
ALSRIS ALSRIA	2100-2093		RIFED COMP 1300 DHM 5% 1/49	01171	CE 1125

Table 7-2. All Parts List

The same and the s							
A11 A11 A11 A11 A11C1	06326-60035 06326-60009 06326-80011 05330-40002	1	BOARD ASSY: LEFT READOUT (Series 1136A) BRACKET: READOUT INDICATOR: MASK (EXT,C,OF) BLOCK: ANNUNCIATOR NOT ASSIGNED	28480 28480 28480 28480 28480	05326-60035 05326-00009 05326-80011 05330-40002		
A11C2 A11C3 A11DS1 A11DS2 A11DS3	0160-2930 0160-2200 2140-0313 2140-0313 2140-0313	1	C:FXD CER 0.01 UF +80-20% 100VDCW C:FXD MICA 43 PF 5% LAMP: NEON GLOW FROSTED 1.9 MILLIAMPS LAMP:NEON GLOW FROSTED 1.9 MILLIAMPS LAMP:NEON GLOW FROSTED 1.9 MILLIAMPS	91418 72136 08806 08806 08806	TA RDM15E430J3C C2A-B C2A-B C2A-B		
A11Q1 A11Q2 A11Q3 A11Q4 A11R1	1854-6-071 1854-0474 1854-0474 1854-0474 0683-1035		TSTR:SI NPN (SELECTED FROM 2N3704) TSTR:SI NPN TSTR:SI NPN TSTR:SI NPN R:FXD COMP 10K OHM 5% ½W	28460 28480 28480 01121	1854-0071 1854-0474 1854-0474 CB1035		
A11R2 A11R3 A11R4 A11R6 A11R6 A11R7 A11R8	0683-2035 0683-1025 0683-1525 0683-2025 0683-2025 0683-2025 0683-5135	2	R:FXD COMP 20K OHM 6% ¼W R:FXD COMP 1000 OHM 6% ¼W R:FXD COMP 1500 OHM 6% ¼W R:FXD COMP 2000 OHM 6% ¼W R:FXD COMP 2000 OHM 6% ¼W R:FXD COMP 2000 OHM 6% ¼W R:FXD COMP 51K OHM 6% ¼W R:FXD COMP 51K OHM 6% ¼W	01121 01121 01121 01121 01121 01121 01121 01121	CB 2035 CB 1025 CB 1525 CB 2025 CB 2026 CB 2025 CB 5135 CB 5135		
A11R1C A11R11 A11U1 A11U2 A11U3 A11U4 A11U6	0683-5135 0683-1025 1820-0175 1820-0274 1820-0274 1820-0274 1820-0274		R:FXD COMP 51K OHM 5% %W R:FXD COMP 1000 OHM 5% %W IC: TTL HEX CONVERTER INTEGRATED CIRCUIT:DIGITAL INTEGRATED CIRCUIT:DIGITAL INTEGRATED CIRCUIT:DIGITAL INTEGRATED CIRCUIT:DIGITAL INTEGRATED CIRCUIT:DIGITAL	01121 01121 01295 04713 04713 04713	CB 5135 CB 1025 SN8200 MC 1808P MC 1808P MC 1808P MC1808P		
A11U6 A11U7 A11U8 A11U9	1820-0274 1820-0273 1820-0273 1820-0054	1	INTEGRATED CIRCUIT: DIGITAL IC: DIGITAL INTEGRATED CIRCUIT: DIGITAL IC: TTL QUAD 2-INPUT NAND GATE	04713 04713 04713 01295	MC1808P MC1806P MC1806P SN7400		

Table 7-3. A18 Parts List (05327-60009)

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
ALB	05 327- 10009	l	PRESCALER ASSY ISERTES BOADA)	28480	05327-60005
			(URADED ON OSSET-ECOLOS BLANK BOARD)		
ALUC1	3050-0067 0140-0397	2	MASHERIFLAT, BAKELTTE CIFRO ELECT 2.2 UF LOW ZOVOCH	00000 56289	080 1500725×4070A2-0Y5
A18C2 A18C3 A18C4 A18C5 A18C7	0160-0975 0140-010à 0160-0975 0160-0106 0160-0975		CIFRO CER O.OOE UF 20% 75VDCW CIFRO ELECT AO UF 201 AVDCW CIFRO CER O.OOE UF 20% 75VDCW CIFRO ELECT AO UF 201 AVDCW CIFRO CER G.OOE UF 20% 75VDCU	12574 28480 12974 28480 12574	\$\$M-, QQ}-98 Q RQ-Q Q6 S\$M-, QQ}-98 Q RQ-Q Q6 S\$M-, QQ}-98
ALECE ALECTO ALECTO ALECTI ALECTZ	0140-0225 0160-0975 0160-0975 0150-0197 0150-0197	2	C:FRD NICA 300 PF BE C:FRD CER 0:001 UF 20E 75VDCW C:FRD CER 0:001 UF 20E 75VDCW C:FRD ELECT 2:2 UF BUE 20VDCW C:FRD ELECT 2:2 UF BUE 20VDCW	28480 12574 12574 56289 56289	0140-0225 55M-,001-98 15002251902JA2-DY5 150022514902JA2-DY5
ALECLS ALECLA ALECRI ALECRI ALECRI ALECRI	0140-2049 0140-2049 1902-3002 1912-0007 1902-3048	2	CIFXO CER FRED-THRU 1000 PF «80-208 CIFXO CER FEED-THRU 3000 PF «80-208 OIDDE BREARDOWN'2-379 38 DIGOTE:TUNNEL EIA TYPE 1N3714 DIGOTE BREARDOWN:SILICON 3-489 38	28^80 28480 28480 03508 28480	0160-2049 0160-2049 1902-1002 191714 5PEC 1902-1048
Aleji Alelz Alegz Alegz Alegg	1250-0814 9106-2251 1859-0015 1854-0092 1854-0092	2	CONNECTORERF SUB-MINIATURE COILEFED AF 0.22 UH LOR TSTRESI PAP TSTRESI PAP TSTRESI NPN	78271 28480 80131 80131 80131	10~053~0000 9100~2295 243640 2*3363 243663
Alani Alanz Alanz Alans Alans	2100-2833 2100-2521 0663-5105 0683-5105 0683-1045		RIVAR CERMET 1K DHM 10E LIM 1/2W RIVAR FLM 2000 DHM 10E LIM 1/2W RIFKO COMP 51 DHM 5E 1/4W RIFKO COMP 31 DHM 5E 1/4W RIFKO COMP 100K OHMS 5E 1/4:	28480 28480 01171 01171 01171	2100-2633 2100-2521 CB 3103 CB 3103 CB 1043
ALGRA ALGRY ALGRA ALGRY ALGRID	0683-1025 0698-3376 0683-1815 0683-1825 0683-2215		RIFKO COMP 1000 DHM 5% 1/4W RIFKD CARBON 51 DHM 5% 1/8W RIFKD COMP 180 DHM 5% 1/4W RIFKD COMP 1800 DHM 5% 1/4W RIFKD COMP 220 DHM 5% 1/4W	01121 28480 G1121 G1121 G1121	CB 1025 0698-3376 CB 1615 CB 1625 CB 2215
ALURII ALURIZ ALURIX ALURIA ALURIA	0683-1825 0683-1825 0683-1825 0683-2015 0683-2015		RIFED COMP 1800 CHM 5% 1/AW RIFED COMP 1800 CHM 5% 1/AW RIFED COMP 1800 CHM 5% 1/AW RIFED COMP 200 CHM 5% 1/AW RIFED COMP 200 CHM 5% 1/AW	01121 01121 01121 01121 01121	CB 1025 CB 1025 CB 1025 CB 2015 CB 2015
ALBRIA FIGHL7 ALGRIB ALGRIP ALBRZO	0683-1025 0683-1515 0683-4315 0683-3315 0683-8205	2	RIFRO COMP 1000 DHM 5E 1/4W RIFXD COMP 180 CHM 5E 1/4W RIFXD COMP 430 DHM 5E 1/4W RIFXD COMP 330 DHM 5E 1/4W RIFXD COMP 82 DHM 5E 1/4W	01121 01121 01121 01121 01121	CB 1025 CB 1517 CD 4315 CB 3315 CB 8205
Alekzi Alekzz Alekza Alekza Alekza	0483-1018 0483-2025 0483-3315 0483-2405 5088-7002		RIFED COMP 300 OHM 5E 1/4W PIFED COMP 2000 OHM 5E 1/4W RIFED COMP 330 OHM 5Z 1/4W RIFED COMP 24 OHM 5Z 1/4W ICILIMITER	01121 01121 01121 01121 01121 26460	CB 1015 CB 2025 CB 3315 CB 2405 5088-F002
A18U2 A18U3 A18U4 A18U5 A18U5	5085-7089 1820-0736 1820-0714 1820-0489 1820-0147	L	CLEARP AND TRIG ICEDIGITAL ICEDIGITAL QUINARY DIVIDER ICECL TRIPLE 3-INPT NUR GATE	28480 28480 28480 28480 04713	6086-7089 1820-0736 1820-0714 1820-0489 MC1007P
ALEUT Altruz	1858-0004 1251-1576	45	TSTR ARRAYISI HPM DUAL DIFF, AMPL. COMMECTORISINGLE CONTACT	28480 00779	1856-0004 2-330808-8

Table 7-4. A18 Parts List (05327-60029)

HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
05327-84029	i.	RUARD ASSYLPRESCALER (SERIES 1116A) (LUADED ON USB27-20029 BLANK BUAPO)	284 PO	P\$DDA-15E60
0180-010- 0160-010- 0160-010- 0-60-010-	•	CIFAD ELECT 2,2 UF 10E 20VOCM CIFAD ELECT AO UF 20E AVDCM CIFAD CER 0,001 'F 20E AVDCM CIFAD CER 0,001 'F 20E AVDCM CIFAD CER 0,001 'F 20E AVDCM	55289 12574 28480 12574 28480	1500225x702022-075 584-,001-98 0180-0106 554-,001-98 0180-0106
01 60=097' 0160=097' 01 40=022' 0160=097' 0160=097'		CIFRO CER 0.001 UF 20% 75VDCW CIFFO CER 0.001 UF 20% 75VDCW JFFO MICA 300 FF 30% CIFFO CER 0.001 UF 20% 75VDCW CIFFO CER 0.001 UF 20% 75VDCW	12574 12574 28480 12574 12574	5EM++001+9P 55M++001+9P 01AU+02P5 5FM++001+9# 55M++001+9P
0180-019 0180-0191 0160-2049 0160-7049 1407-300		CIFRO ELECT 2.2 UF 1/E 30VDCM CIFFO ELECT 2.2 UF 10E 20VDCM CIFFO CER FEED-TINU 5000 PF +80-20E CIFFO CER FEED-TINU 5000 PF +80-20E DIODE BREAKDOMNI2.33V 5E	36289 36289 28480 28480 28480	1500225x4020A2-0\\$ 1500225x4020A2-0\\$ 0160-2044 0160-204\ 1402-2002
1912-0007 1902-1048 1250-0816 9140-0178 9100-22-11	2	DIOGESTUNNEL EIA TYPE INSTIA DIODE BREAKDOWNSSILICON 3.48V 5K CONNECTORIFF BUB-MINIATURE COILSFRO RF I UM 10K COILSFRO RF 0.22 UM 10K	03508 284F0 9829} 99800 28480	149714 5PF 1 1902-1048 50-051-0000 1024-20 9100-2741
1853-0015 1854-07A5 1854-07A5 2100-2433 2100-2433	2	TSTRISE PHP TSTRISE NPN TSTRISE NPN RIVAR CERRET IK OHM LOW LEN 1/2W RIVAR FLM 2000 OHM LOW LEN 1/2W	80131 80131 80131 28480 28480	741640 245177 243567 2100-2633 2100-2521
06#3-6105 06#3-9105 06#3-1045 06#3-1025 06#4-337#	,	RIFRO COMP 31 OHM 38 1/4W RIFRO COMP 31 DAM 58 1/4W RIFRO COMP 100K OHMS 38 1/4W RIFRO COMP 100D OHM 58 1/4W RIFRO COMP 100D OHM 58 1/4W RIFRO CARBON 51 OHM 58 1/4W	01121 01121 01121 01121 78480	CB 5105 CB 5105 CB 1045 CB 1025 U698-2378
C ;3+1815 C ;8h-1925 (687-2215 (687-1825 (683-1825	2	REFXD COMP IBO CHM SE 1/4W REFXD COMP 1800 CHM SE 1/4W REFXD COMP 220 CHM SE 1/4W REFXD COMP 1800 CHM SE 1/4W REFXD COMP 1800 CHM SE 1/4W	01121 01121 01121 01121 01121	CB 1815 CB 1825 CB 2215 CB 1825 CB 1825
0643-1425 0683-2015 0043-2015 0483-1025 0683-1515		RIFKD COMP 1800 OHM 5% 1/4W RIFKD COMP 200 OHM 5% 1/4W RIFKD COMP 200 OHM 5% 1/4W RIFKD COMP 1000 OHM 5% 1/4W RIFKD COMP 150 OHM 5% 1/4W	01121 01121 01121 01121 01121	CB 1025 CB 2715 CB 2015 CB 1025 CB 1915
0683-4315 0683-5115 0683-8205 0683-1015 0683-2025		RIPKO COMP 430 OMM 58 1/4W RIPKO COMP 310 OMM 58 1/4W RIPKO COMP 82 OMM 58 1/4W RIPKO COMP 100 OMM 58 1/4W RIPKO COMP 100 OMM 58 1/4W	01121 01121 01121 01121	CB 4315 CB 5115 CB 8205 CB 1015 CB 2025
0683-3315 0683-2403 0698-3374 0683-3615 0683-2715	1 1	RIFXD COMP 330 ONN SE 1/4W RIFXD COMP 24 ONN SE 1/4W RIFXD CARBON 20 ONN SE 1/8W RIFXD COMP 360 ONN SE 1/8W RIFXD COMP 270 ONN SE 1/4W	01121 01121 28480 01121 01171	CB 3315 CB 2405 0848-3374 CB 3615 CB 2715
2100-2670 5088-7002 5086-7089 1420-0736 1820-0784	1 2 2 2 1	REVAR CERMET 20 DHM 30% LIN 1/7W ICELINITER ICEANP AND TRIG ICEDIGITAL ICEDINARY—QUINARY	28480 28480 28480 28480 28480 28480	2100-2670 5086-7002 5086-7069 1820-0736 1820-0784
1620-0489 1820-0147 1858-0004 1820-0790 1251-1556	ž i	ICIECL ECIECL TRIPLE 3-ENPT NOR GATE ISTR ARRAYISE NPN DUAL DEFF. AMPL. ECIDIGITAL CONNECTORESENGLE CONTACT	78480 04713 28480 28480 00779	1820-0489 MC1007P 1858-0004 1820-0790 2-310804-8
1251-1556 1205-0243 1205-0244 0570-0129 0610-0001	1 1 2 2	CONNECTORISINGLE CONTACT HEAT DISSIPATOR REFAIMER SCREWIPAN HD POZI DR 2-56 x 0.312° LG NUTCHEX 2-56 x 0.188°	00779 28480 28480 00000 00000	2-33000+8 1203-0243 1203-0244 080 080
3050-0062 3050-0079	2	WASHERPRYLON G.1875* OD	00000	GRD GRD
	03327-8/D29 0180-0197 0140-0107 0140-0107 0140-027 0140-027 0140-027 0140-027 0140-027 0140-027 0140-027 0140-027 0140-027 0140-027 0140-0197 0140-0197 0140-0197 0140-0197 0140-0197 0140-0197 0140-0197 0140-0197 0140-243 1250-08/6 9140-01/8 8100-22/1 1403-0015 1834-017 18	03327-84029 0180-0147 0180-0147 0180-0101		

A18 PRESCALER OPERATION

The prescaler board serves as a direct amplifier-trigger or as a divide-by-ten amplifier-trigger, with the function controlled by the front-pane! INPUT SELECTOR switch. With the switch in the PRESCALE position, the circuit performs as follows:

The signal is fed into the $50\,\Omega$ input, of J1. U1 limits the input level to about +1 V and provides protection up to 3.5 V rms. There is about 2 dB loss through U1. The signal is passed to U2 amplifier Schmitt Trigger, which is biased for sensitivity by R1. The Schmitt Trigger threshold is about 70 mV and triggers independently of frequency. The square wave from U2(11) is divided by two and again by five in U3 and U4, respectively. Q2 translates the signal to ECL levels, before presenting it to the data switch.

At the same time, U2 amplifier is supplying the direct triggering network with the input signal. C1 ac couples the signal and eliminates the effect of U2 amplifier drift. U7 consists of a dual-differential amplifier, which converts the input voltage to an output current which is driven through tunnel diode CR2. The diode performs a Schmitt Trigger function and squares the output. R2 is used to vary the bias current through CR2 to control the sensitivity, Q1, Q3, C8, and R 8 differentiate the signal and pass the negative pulses at an ECL level to U5 data switch.

The data switch is controlled by the INPUT SELECTOR switch (TTL high at U5(2) = Direct; TTL low at U5(2) = Prescale) and connects either the direct or prescaler signal to A7 Function Control by way of the one-shot, U6. The one-shot output goes high (U6(4)) when the input goes low. The output goes low again after about 12 ns when the level changes have propagated through the gates in a domino effect.

A18 TROUBLESHOOTING

Check for operation by placing the INPUT SELECTOR switch in both B and B \div 10 positions. If the counter does not work with the switch in either position, the trouble is probably in U1, U2, U5, or U6. In the direct mode, start by checking in the tunnel diode (CR2) area; check for proper waveforms of 5 and 6. For the prescale mode, start by checking the output of divide-by-two circuit, U3(6).

Figure 7-1. A15 Schematic

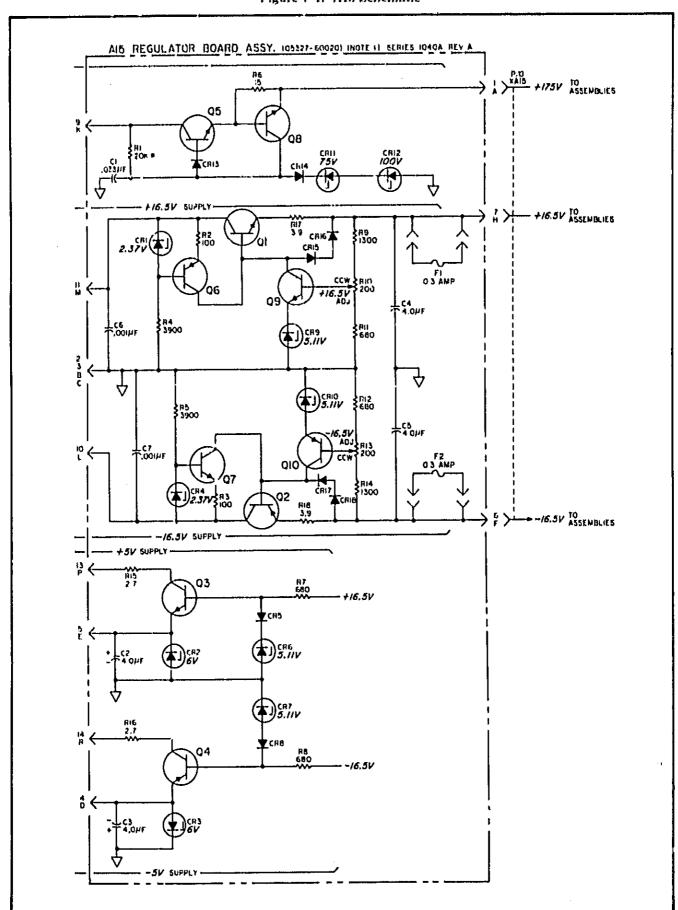
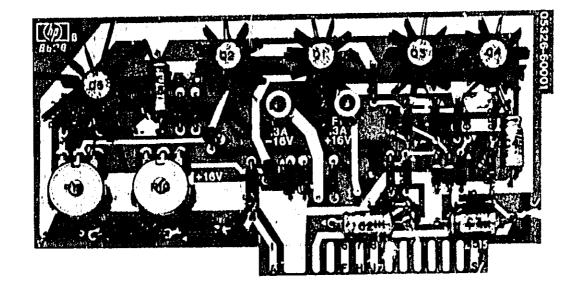


Figure 7-2. A15 Component Locator



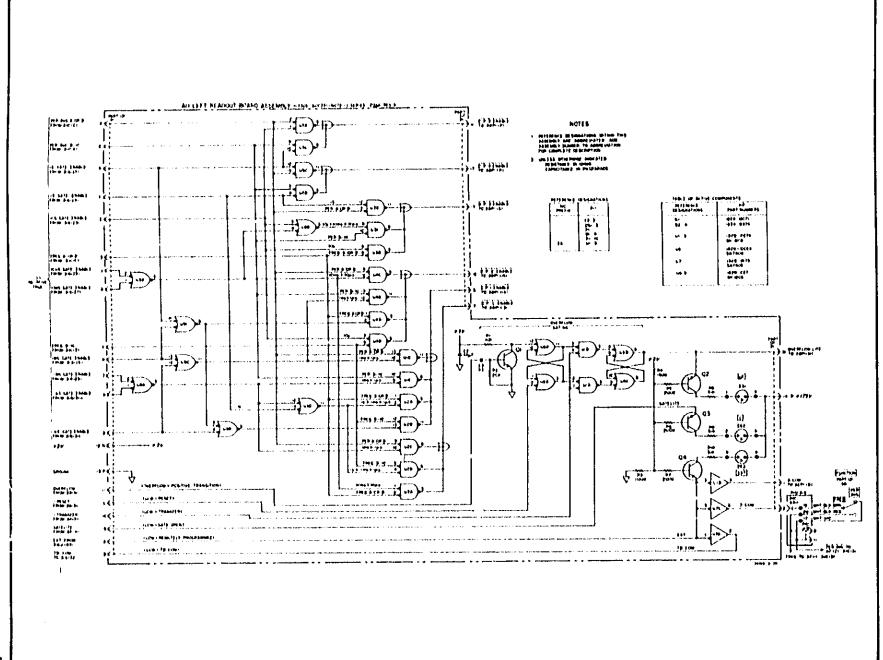
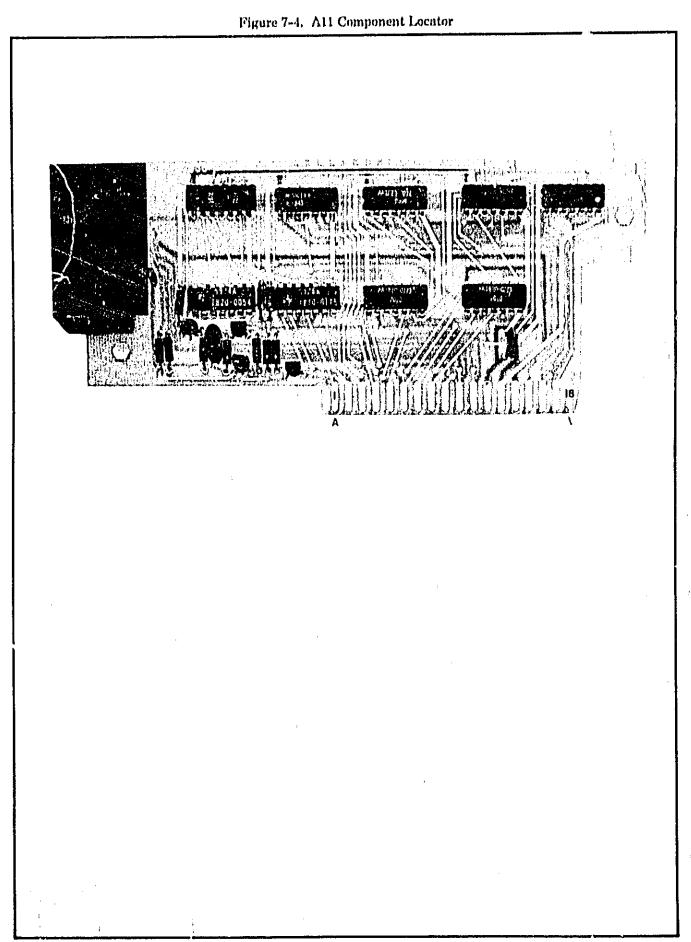


Figure 7-3. All Schematic



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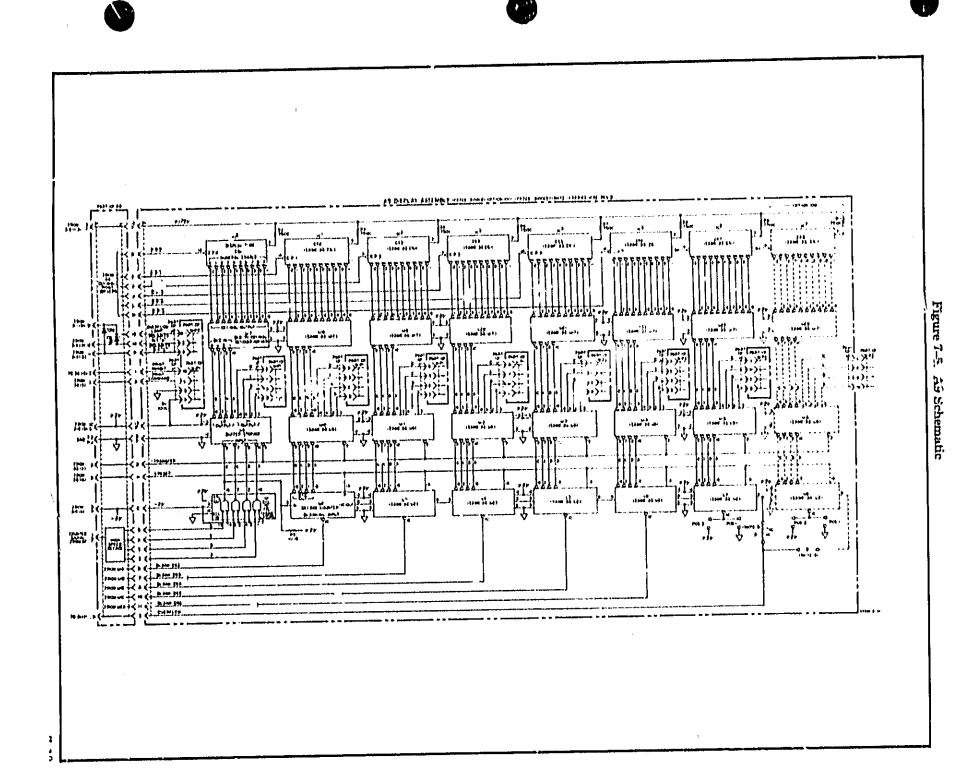


Figure 7-6. A9 Component Locator OPT COL POSITION NO. 2 BLANKING DEFEAT(SEE SECTION II) POSITION B TABLE OF ACTIVE COMPONENTS REFERENCE DESIGNATIONS PART NUMBERS AB CRI CR2 A9 UI 1901 - 0040 1901 - 0016 1820 - 0275 MC1039P 1820 - 0119 1820 - 0116 1820 - 0092 1820 - 0729 U2-7 U9-15 U15-23 U17

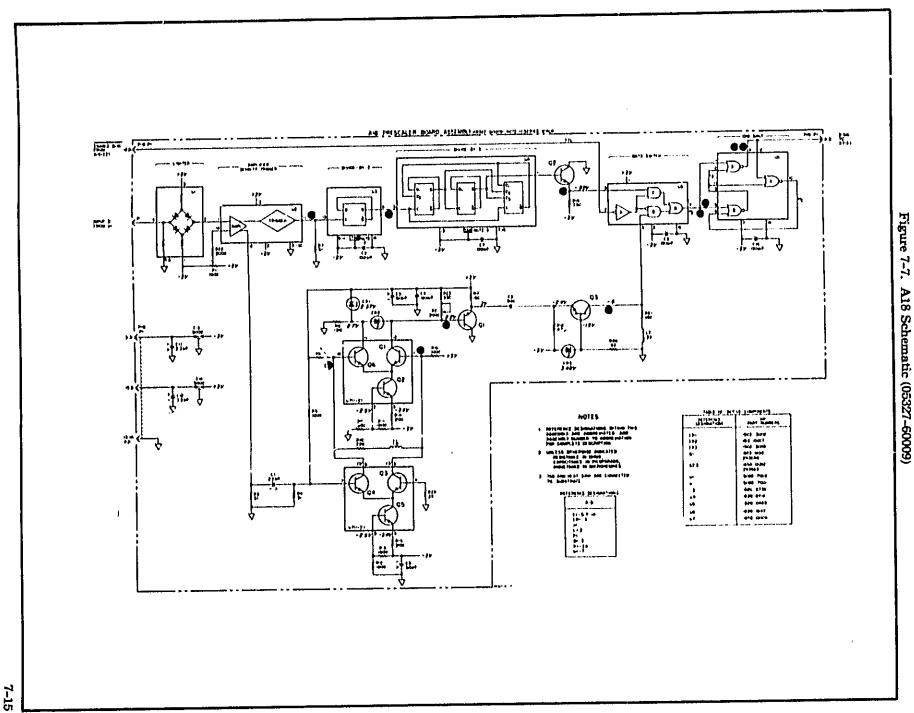
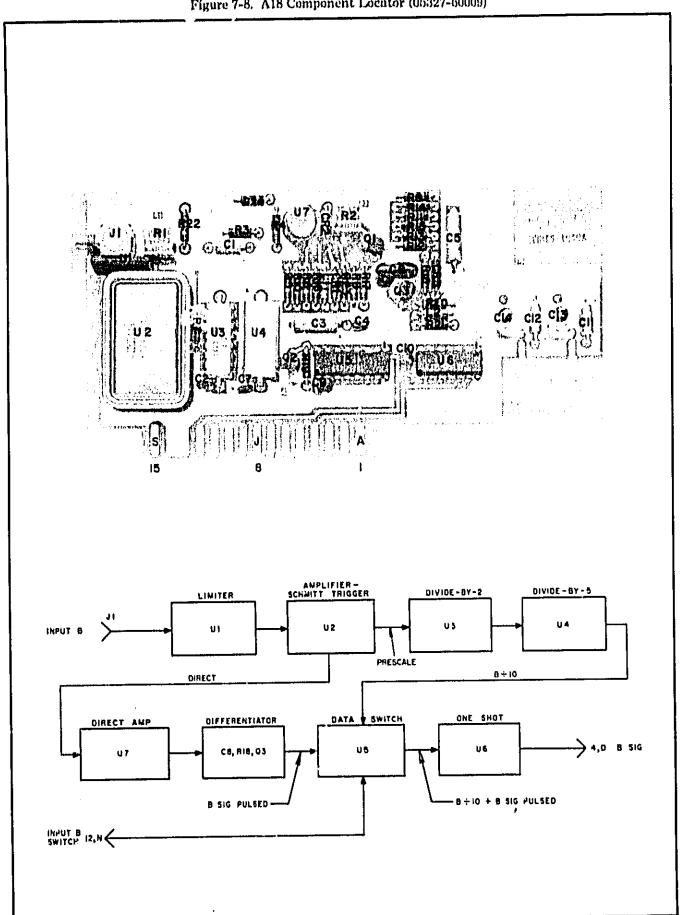


Figure 7-8. A18 Component Locator (05327-60009)



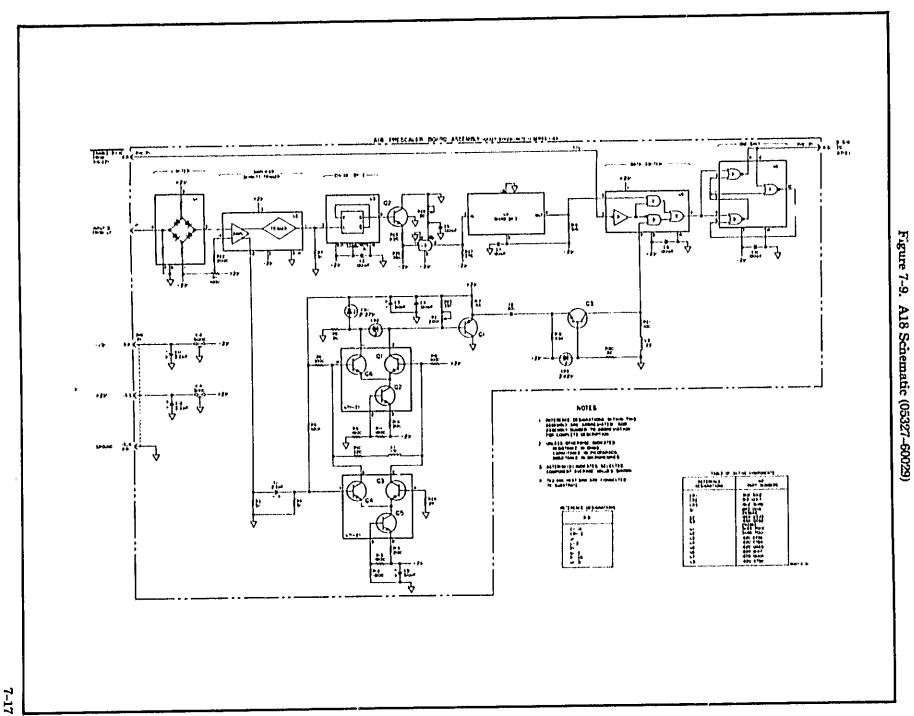
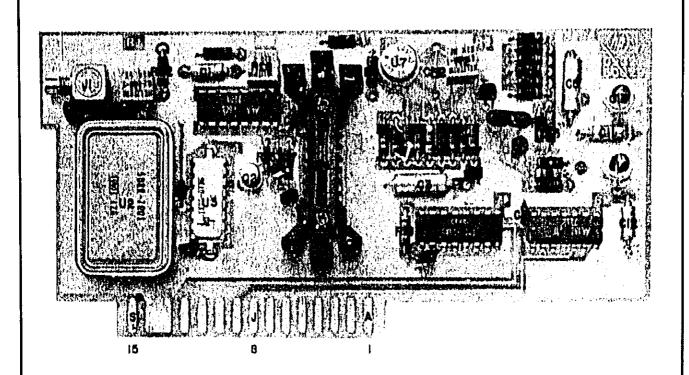
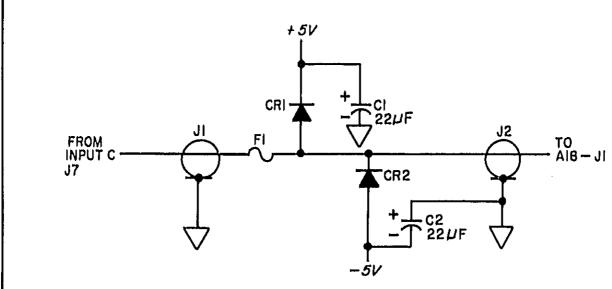


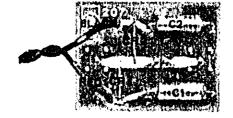
Figure 7-10. A18 Component Locator (05327-60029)

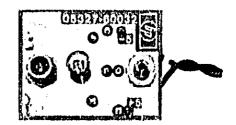


The 5327C model may be supplied with either A18 Prescaler Assembly. Both assemblies perform the same function. Note that separate schematics, component locators and parts lists are supplied for each assembly.

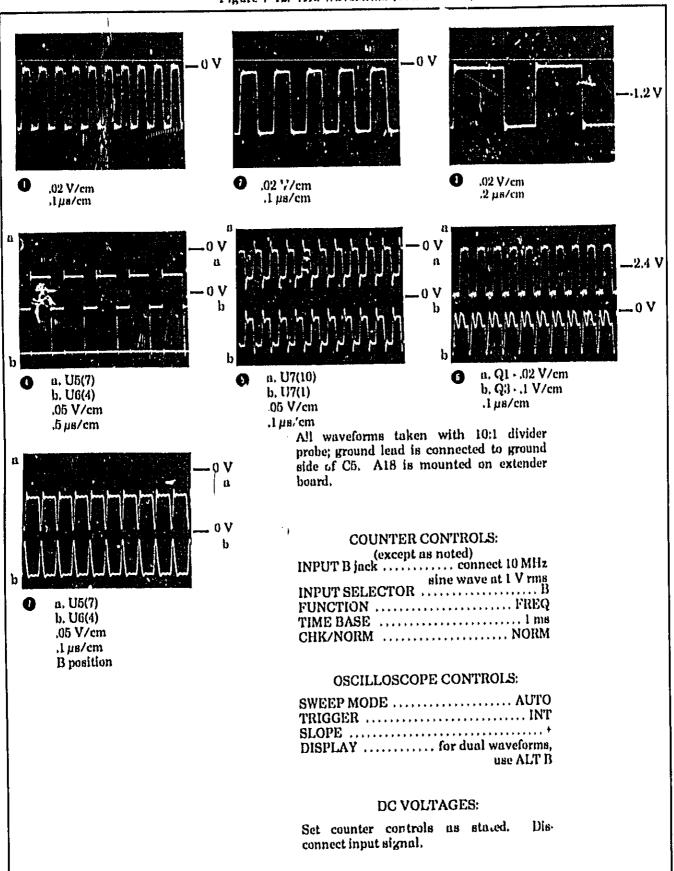
Figure 7-11. A19 Schematic, Component Locator, and Parts List







A19 05327-60032 Protection Board
A19 05327-20032 Blank Board
A19C1, 2 0180-0228 C:FXD TANT 22 UF 10% 15V
A19CR1, 2 1901-0050 DIODE:SILICON
A19F1 2110-0436 FUSE: 0.1 AMP
A19J1 1250-1408 CONNECTOR:RF SUBMINIATURE
A19J2 1250-0835 CONNECTOR:RF SUBMINIATURE
A19XF1A/B 1251-3205 SOCKET:MINIATURE (2)



7-9. OPTIONS

7-10. Options 001 through 003 and Options 010 and 011 are available for the 5326C and 5327C and models. The following paragraphs describe each option's purpose and installation procedure.

7-11. Option 001, 6-Digit Display

7-12. Option 001 is the addition of an eighth digit to the display assembly. This addition becomes the most significant digit and extends the counter's display at higher frequencies. The digit is always blanked if the reading is "0".

7-13. Option 002, Remote Programming

7-14. Option 002 allows the counter to be computer controlled from a 36-pin connector on the counter's rear panel. Programming instructions are given in Section II. A schematic diagram is included in Section VIII, Interconnect Diagram.

7-15. Option 003, Digital Recorder Output

7-16. The data displayed on the counter's front panel can be permanently recorded by connecting a printer to the counter via Option 003. The necessary signals are coupled to an HP 5055A or 5050B Digital Recorder through J9 on the counter's rear panel (also, see Table 1-3 for specifications and interconnect diagram for pin references).

7-17. Opilon 010, Oscillator Assembly A4 (05327-60036)

7-18. Option 010 is a temperature-controlled crystal oscillator. This option replaces the standard A4 Oscillator Assembly and provides increased measurement accuracy. Specifications are given in Table 1-1. Schematic diagram is included in Section VIII.

7-19 Option 011, Oscillator Assemby A4, (10544-60011)

7-20. Option 011 is an oven-controlled crystal oscillator. This option replaces the standard A4 Oscillator and provides the highest stability available in the 5326/5327 Counters. Specifications are included in Table 1-1.

7-21. FIELD INSTALLATION OF OPTIONS

7-22. Installation of Option 601, 8th Digit

7-23. Parts required to install this option are:

1820-0119 Decade Counter U8 1820-0116 Buffer Storage U16 1820-0092 Decader Driver U24 1970-0042 Display Tube DS8

- a. Remove right and left readout be rds, A10 and A11.
- b. Remove two screws holding display tube shield and remove shield.
- c. Remove display board A0 and display support board A8 from the counter by pulling up on the display support board A8. Separate A8 from A0.
- d. Install parts on A9 as shown in the component location photo on Figure 8-12 of this manual.
- e, Remove R10 to prevent overflow information from U7, R12 carries the overflow when Option 001 is installed, Replace boards in instrument.
- f. Perform Self-Check in Figure 3-3. Especially note that the OF (overflow) lamp lights when the leftmost digit changes from 0 to 0.

7-24. installation of Option 002, Remote Programming

- 7-25. To install remote programming capability in units not so equipped, order remote cable assembly HP Part No. 05327-60013, two 4-40 x M-inch machine screws, and one 6-32 x M-inch machine screw with hex nut.
- n. Remove the plate covering the lower opening in the rear panel for Option 002.
- b. The rear-panel interconnect board containing the wiring for the rear-panel BNC's and switches must be removed. To accomplish this, remove the nutr holding the rear-panel BNC's.
- c, Remove two screws holding P1A, the 1%-inch long, black, pressure connector to the motherboard A16.
- d. Remove side covers and six screws holding rear panel. Loosen one side frame. Pull rear panel away from the instrument.
- e. Remove the rear-panel interconnect board from the instrument and separate it from PIA by removing two screws.
- f. Feed the pressure connector through hole in rear-panel and mount rear-panel connector J10, with screws removed earlier. Position J10 with pin 1 near the side frame.

- g. Assemble the rear-panel interconnect board and the new 5-inch long pressure connector P1 with three 6-32 x %-inch screws and hex nuts. Be certain that proper contact is made between interconnect board and P1.
- h. Attach P1 to the motherboard using four 4-40 x 1/2-inch screws. Do not tighten screws. Route cable as shown in the top internal photo of instrument, Figure 8-3.

CAUTION

Screws longer than 14-inch will damage P1.

- i. Gently reinstail rear panel. Install BNC lock nuts so that the board is still moveable.
- j. Observe the alignment of the connector in the motherboard. Tighten the four screws holding P1 to the motherboard, making sure to maintain proper contact.
- k. Check contact alignment of P1 with motherboard and with the rear-panel interconnect board. If necessary, loosen the screws in P1 and shift slightly to obtain proper terminal contact.
- I. Tighten BNC lock nuts and reassembly instrument.
- m. Run a complete performance check on the unit to verify that remote programming is working properly.

7-28. Installation of Option 003, Digital Recorder Output

7-27. Order digital recorder cable assembly HP Part No. 05326-60012.

- a. Remove the plate covering the upper opening in the rear panel.
- b. Remove right and left readout boards A10 and A11. Remove two berews holding the display tube shield and remove saield. Remove display support board A8 and the display board A0 by pulling up on A8.
- e. Feed the two pressure connectors of the recorder cable through the rear panel and mount J9 on the rear panel, using the screws previously removed. Position J9 so pin 1 is near the side frame.
- d. Slide the connectors on the A9 board, shown in the photo on Figure 8-3. The connector with the long wires attaches to J1 and is positioned so that pin 1 is toward the front of the instrument. The other connector attaches to J2, and pin 1 is toward the rear of the instrument.
- e. Position the P1 cable so it passes between A8 and A11, completely clearing A8. Reinstall A8 and A9.
- f. Route the cable around TI as in the top internal photograph, Figure 8-3.
- g. Reassembly unit and run a proof-ofperformance check of the digital output to verify that the option is installed properly.

7-28. Installation of Option 010 and 011, Oscillator Assemblies.

7-29. Remove the standard A4 oscillator and insert the option into the XA4 connector. The Option 011 assembly must be mounted to the interconnect board with two 6 x 32, %'' screws. Place the fiber washers on the underside of the board.

SECTION VIII CIRCUIT DIAGRAMS

This section contains the following:

- a. Schematic dingram notes.
- b. Component locators.
- c. IC outline drawings.
- d. Waveforms.
- e. Simplified block diagrams.

Figure 5-1, Schematic Diagram Notes

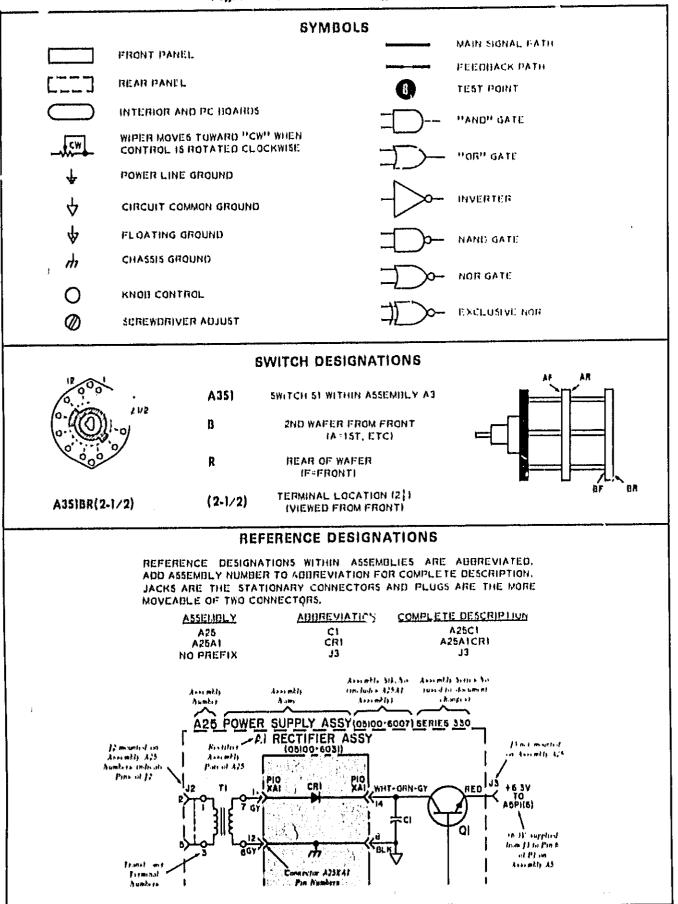


Figure 8-2. Front and Rear Panel Designations

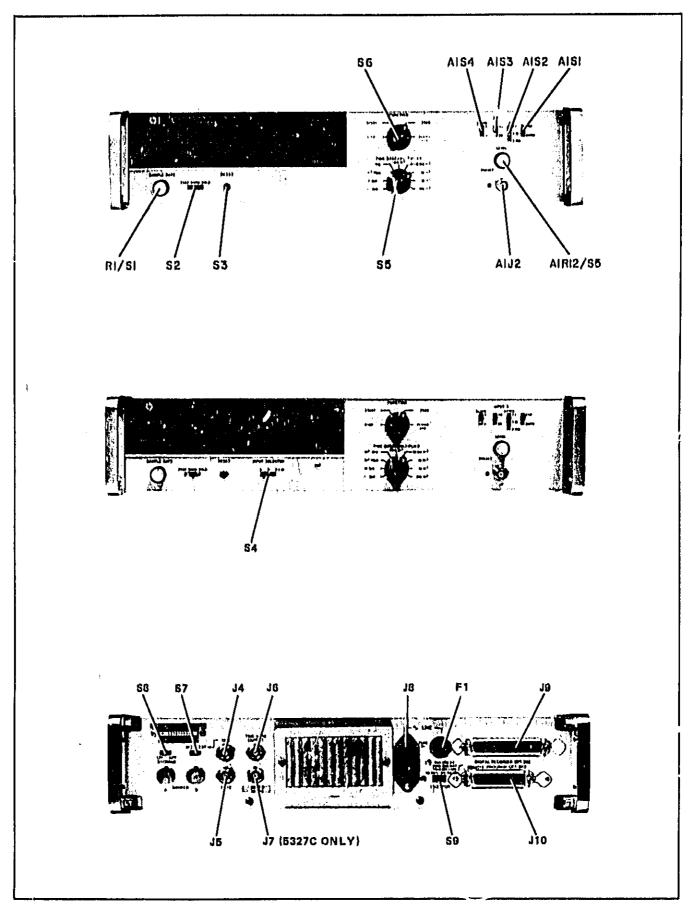


Figure 8-3. 5327C Top Internal (with Options)

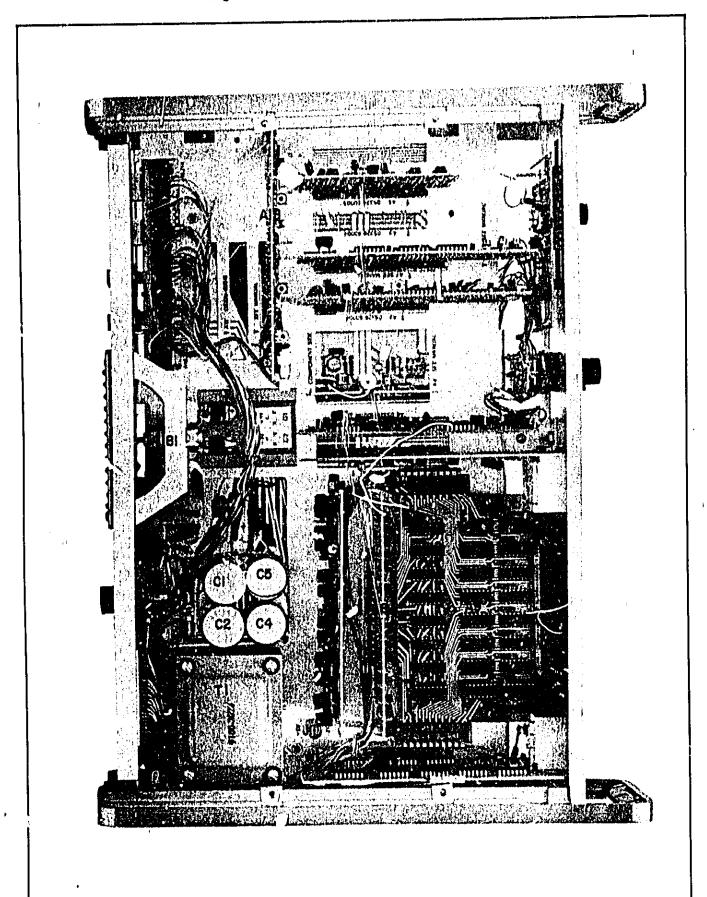


Figure 8-4. 5326C Bottom Internal

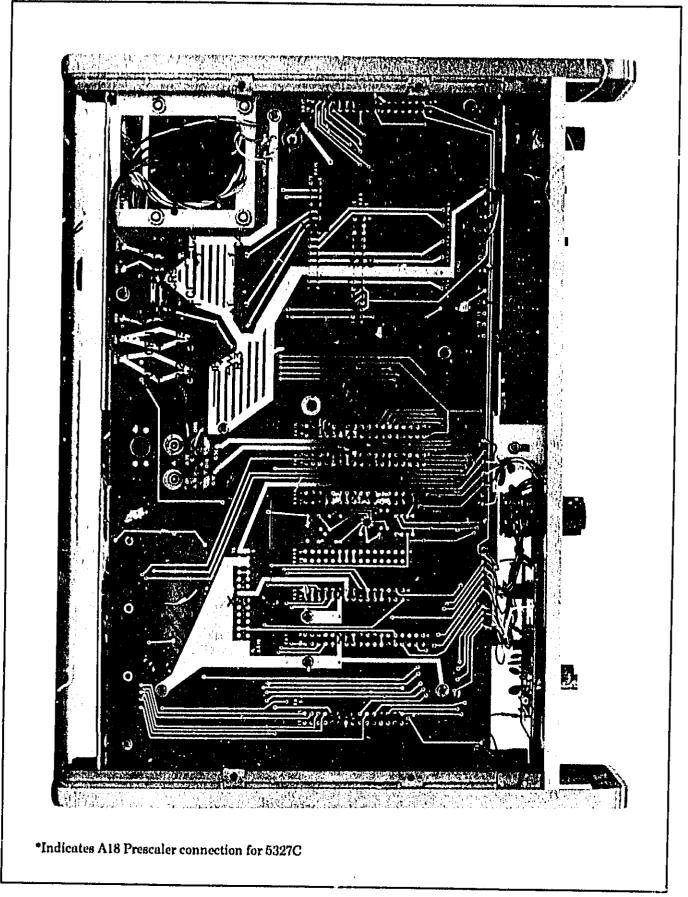
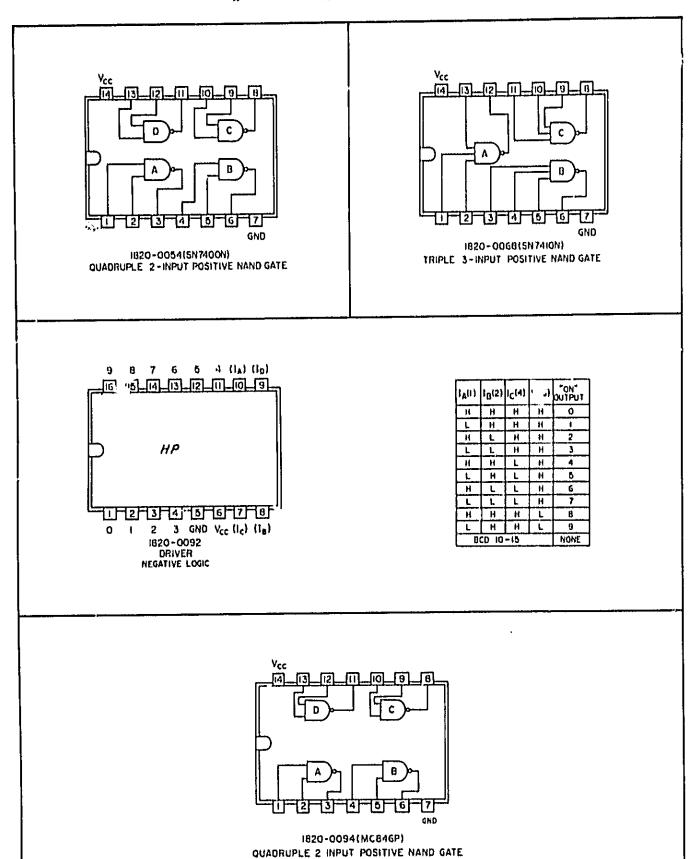


Figure 8-5. Integrated Circuit Diagrams



J_D · R_D TRUTH TABLE CLOCKED J R R-S TRUTH TABLE 5 Qhei 2 13 ik ε_D G^h Jo 12 13 · 13 φ φ 0 0 0 Qⁿ 1 Qn Qn 0 0 0 0ⁿ Ġ 0 丁 ø ī Ü 0 1 100 0 1 ði 0 -0 םא ו ו ī $\overline{}$ 1 ALL OTHER J K INPUTS AND THE R-S INPUTS ARE AT A "O"LEVEL Qħ. 1 1 ALL JR INPUTS ALL OTHER J K ARE STATIC INPUTS AND THE R-S INPUTS AND AT A "O" LEVEL 7 GND IL 20 - 0102(MCI0I3P) 1 Bowlet | J.-R FLIP-FLOR ZD ZD ZA ZA GND GR LA LD [6_[6_[4]_[3_[2]_[1]_[0]_9 HP 1234567 Z_B Z_C Z_C GI V_{CC} I_C I_B

1820-01IG

4-BIT BUFFER STORAGE
(POSITIVE LOGIC) TRUTH TABLE ZD ZA RESET NO VCC NO RESET INPUT OUTPUT A B C D PAREL н н н н POSITIVE PULSE APPLIED TO RESET WILL: L H H H A. RESET A.B.C & D TO LOW IF RESET CONTROL IS LOW. 2 H L нн HP B. RESET A,B,C B D TO HIGH IF RESET CONTROL IS HIGH LLHH C. THE + 10 OUTPUT WILL ALWAYS BE RESET TO HIGH STATE H H L Н 4 LHL Н 5 1 2 3 4 5 6 7 Z_B Z_C NC NC NC GND NC HL 6 7 LLL 1820-0117,0119,0232 нин В BLANKING DECADE COUNTER LHH 9

2011

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н | н | н

H

NEGATIVE LOGIC 1: LOW

O. HIGH

Figure 8-5. Integrated Circuit Diagrams (Continued)

Figure 8-5, Integrated Circuit Diagrams (Continued)

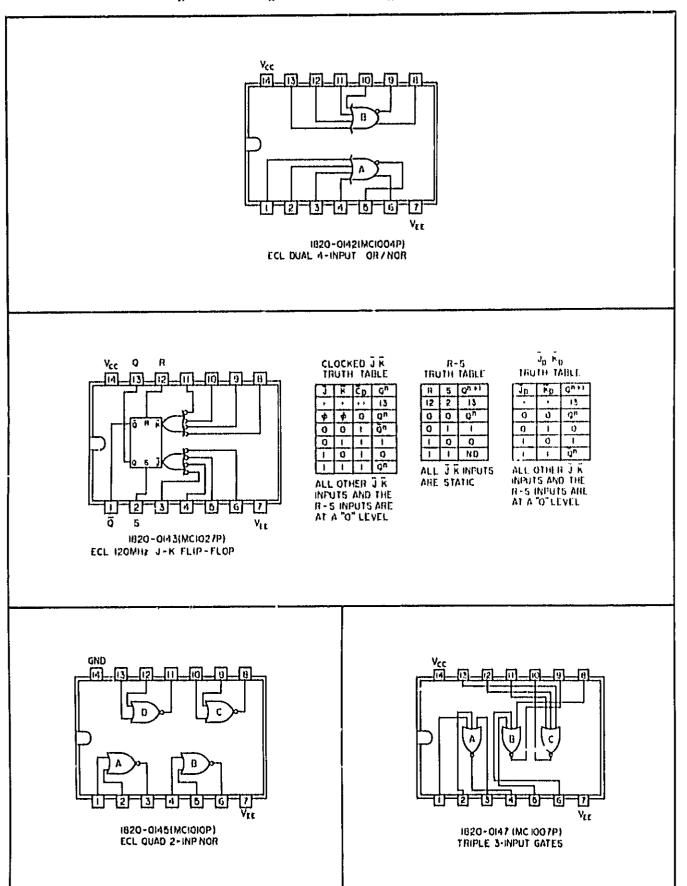


Figure 8-5. Integrated Circuit Dingrams (Continued)

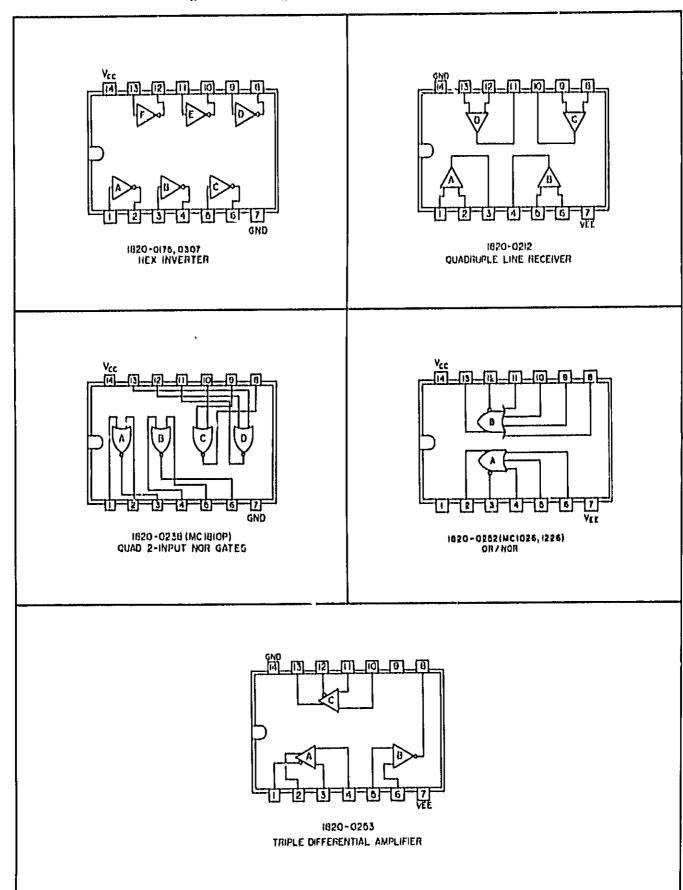
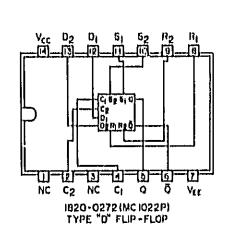


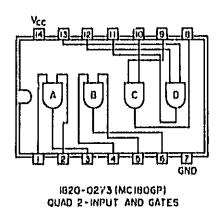
Figure 8-5. Integrated Circuit Diagrams (Continued)

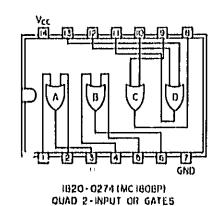


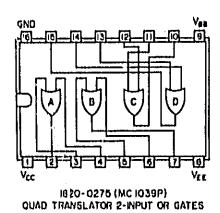
	CLOCI	KED TA	aith t	
	D	c	Q^{n+1}	Qn+I
IN No.	12 0#13	2 on 4	5	G
	0	υ	Qn	٥n
		0	Q ⁿ	٥n
	0	1.	0	
		-	1	0

"A"I" IN CLOCK INPUT IS DEFINED FOR THIS FLIP-FLOP AS A CHANGE IN LEVEL FROM A LOW INPUT TO A HIGH INPUT.

	R-S TRUTH TABLE			
		6	qn+1	Ön∔İ
PIN No	1) OH 9	I) no Oi	5	G
	0	0	Qn	Ön
	0	1	ı	0
	1	0	O	_
	1	1	N.D.	N.D.
	N.C. L.NO	PERM	h	







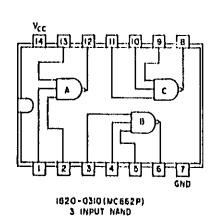


Figure 8-5, Integrated Circuit Diagrams (Continued)

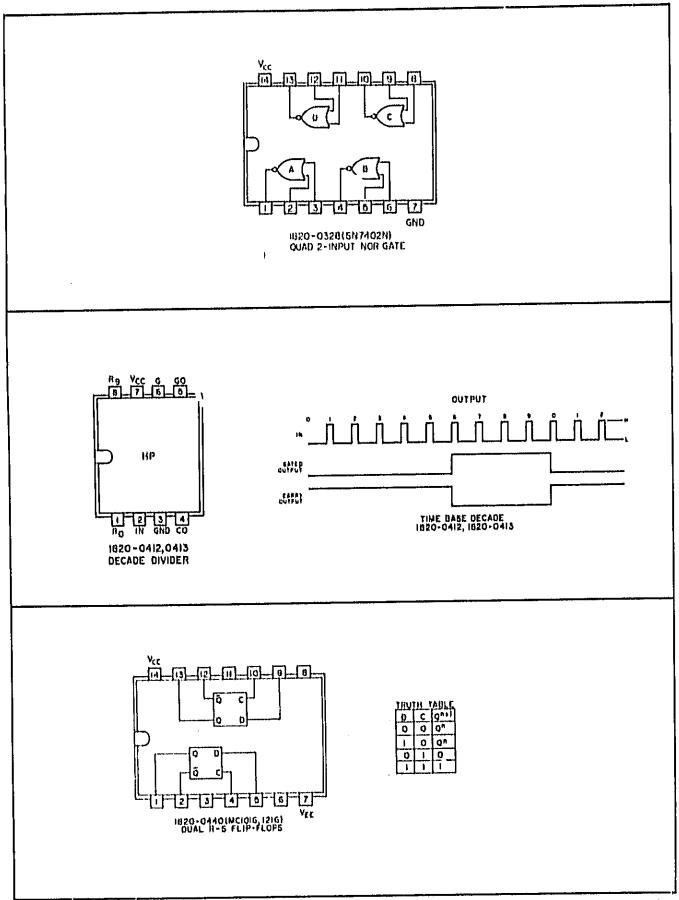
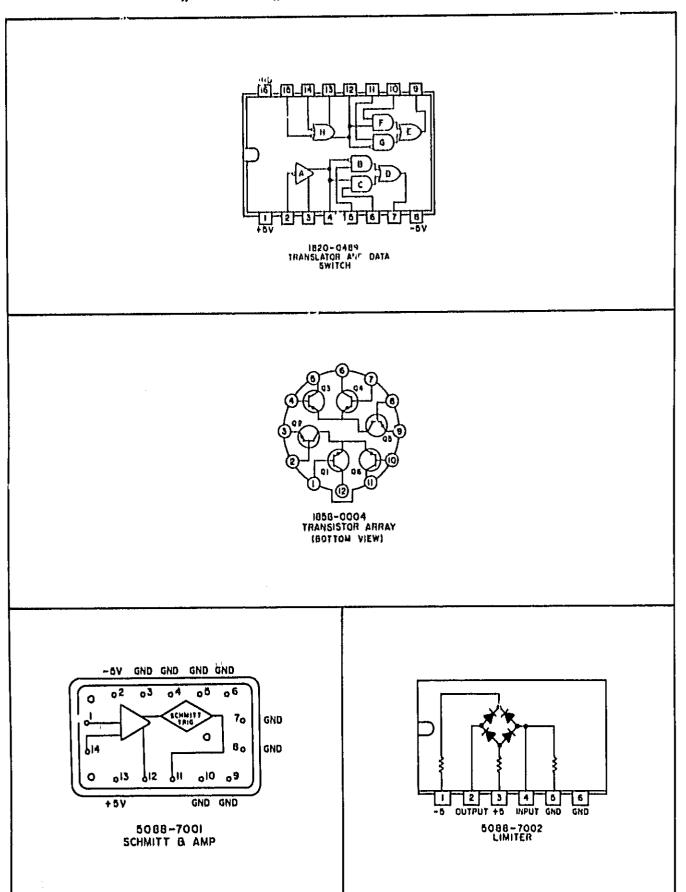


Figure 8-5. Integrated Circuit Dingrams (Continued)



AI ATTENUATOR OPERATION

Channel A input signals are routed through the front-panel input jack to the attenuator network. With the ATTEN switch in X1, the full input signal is fed to the gate of Q1A. When the ATTEN switch is set to X10, R2, R5, C1, and C3 serve as a 10:1 voltage divider. In the X100 position, the 100:1 voltage divider consists of R2, R4, C1, and C2. R3 provides dumping.

For the check mode, the NORM-CHK switch grounds P1 via CR1. This programs A2 and A7 to disconnect the input signal and connect the 10 MHz internal oscillator signal.

When the AC/DC switch S4 is set to AC, C4 is in series with the signal path. CR3 and CR4 limit the input amplitude to Q1A to approximately ±3.8 V. R7 and R8 provide current limiting. C5 compensates Q1A input capacitance.

QIA and QIB form a differential amplifier, connected as source followers. Outputs are fed to A2 via pins 5 and 6 of J1. LEVEL potentiometer R12 determines the trigger level on QIB gate. The trigger level can be preset to zero volts or varied from -3 to +3 volts. In addition, an external trigger level can be applied at J10 to A1J1(D) for remote programming. Diodes CR5 and CR6 develop a stable 5-volt reference for the input protection circuits and for the trigger level control. R11 lowers the impedance of Q1B gate circuit to limit stray charges and false triggering. R10 and C8 form a filter to prevent noise from triggering the differential amplifier.

When the SLOPE switch is set to , a Low is supplied to J1 pin A via CR2. This sets amplifier trigger A2 to trigger on the negative slope of the input signal. When remote programming is used, J1(C) is held High to disable the SLOPE switches.

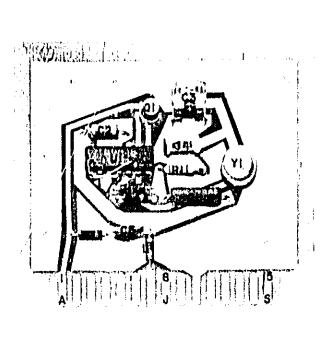
A4 OSCILLATOR OPERATION

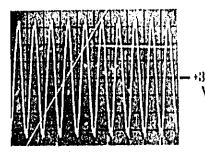
The 10 MHz oscillator assembly consists of oscillator U1A, buffer U1B, and level shifter Q1. U1A operates like an amplifier with positive feedback. The positive feedback path is from the noninverted output of U1A(6) through 10 MHz crystal Y1, trimmer capacitor C3, and C4 to U1A(4). Negative feedback is used to establish the input bias for U1A. The negative feedback path consists of R1 and R2. The inverted output of U1A(5) connects to buffer U1B(10). The buffer provides isolation between the oscillator and the output. The outputs of U1B(8) and (9) switch from approximately 3.5 to 4.25 volts. When one output is 3.5 volts, the other output is 4.25 volts. Level shifter Q1 converts the output of U1B to an approximate square wave of 0 to +4 volts.

(see Figure 8-6)

Part of Figure 8-6, A1/A4 Attenuator/Oscillator Board Assembly TRIGGER LEVEL-X I X IO X IOO SLOPE OCHECK

SLOPE

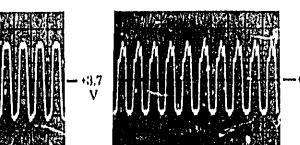




01 V/cm ,1 μs/cm uc coupled



,1 μs/cm ne coupled



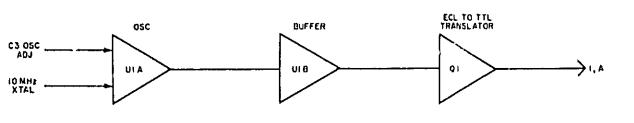
02 V/em .1 μs/cm ne coupled

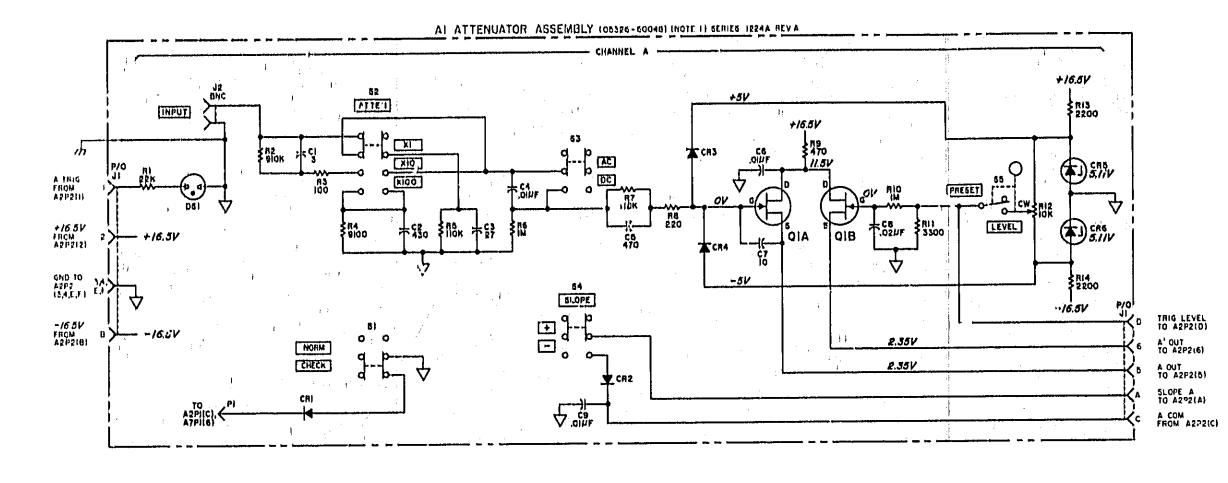
All waveforms taken through 10:1 divider probe. Divider probe's 8-1/2" ground lead is connected to ground side of C5.

COUNTER CONTROLS:

INT-EXT (rear panel) INT

0 .1 V/cm .1 μs/cm





NOTES

- I REFERENCE DESIGNATIONS WITHIN THIS ASSEMBLY ARE ABBREVIATED. ADD ASSEMBLY NUMBER TO ABBREVIATION FOR COMPLETE DESCRIPTION.
- 2. UNLESS OTHERWISE INDICATED: RESISTANCE IN OHMS, CAPACITANCE IN PICOFARADS; INDUCTANCE IN MICROHENRIES
- 3. ASTERISKI#) INDICATES SELECTED COMPONENT, AVERAGE VALUES SHOWN
- 4 UI IS ECL LOGIC TYPE, HOWEVER!
 - VCC . +BVOLTS VEE . GROUND
- H ≥ +4 I5YOLTS L = +3.60VOLTS

REFERENCE DESIGNATIONS

Àί	A4
21-9 CRI-6 DSI J1,2 PI Q1 RI-14 51-5	C1-6 L1 01 R1-4 U1 Y1

A4 OSCILLATOR ASSEMBLY 108326 - 600021 INOTE 11 SCHIES 1032A, REV C

TABLE OF ACTIVE COMPONENTS

REFERENCE DESIGNATIONS	PART NUNGERS
Ai	
CRI	1901 - 0040
CR2	1910 - 00.6
CR3,4	1901 - 0376
CR5,6	1902 - 0041
QI	1855-0334
A4	
QI	1850 - 0158
ขเ	1820 - 0142
YI	0410 - 0405

A2 AMPLIFIER/TRIGGER OPERATION

The input signal and the trigger level are received from A1 via P2(5) and P2(6), respectively. Potentiometer R2 is adjusted to cancel offset voltages that are due to imbalances in the circuit.

The differential omplifier (Q1 and Q8) serves to clip a small window out of the input signal waveform. The outputs of Q1 and Q8 drive another differential amplifier Q2 and Q6. Q2 and Q6 inject a current drive input to differential Schmitt trigger Q3, Q4, Q5, and Q7.

Q3 and Q7 are common base amplifiers, which present a low input impedance and high output impedance to Q4 and Q5. This arrangement allows for greater high-speed operation of Q4 and Q5. C2 and R15 reduce the hysteresis of the Schmitt trigger to give greater reliability at the high frequencies. Two out-of-phase signals from this circuit are routed to Q9 and Q10. The output levels shift from approximately +0.8 to +0.5 volts.

The SLOPE switch on A1 drives U1D(11) low for a *slope selection and U1A(3) low for a *slope selection. This allows either the in-phase signal or the out-of-phase signal to be switched to Q13 via Q10 and Q12 for *slope or via Q9 and Q11 for *slope.

The differentiator circuit consists of Q13 and feedback network L8 and R32. The circuit develops 10 ns pulses at the collector of Q13. CR3 and CR4 bias Q13 so that the collector circuit is compatible with ECL output driver U2B.

U2A(6) drives trigger-lamp driver Q16, Q17, Q18, Q19, and Q20. The circuit consists of RS FF Q16-Q17 and one-shot Q19-Q20. When U2A(6) is low, Q16 turns off and Q17 turns on. With Q17 on, Q18 cuts off to drive P1(1) high, which will light the trigger lamp DS1 on A1. As C8 charges, Q20 base goes positive. When Q20 base is approximately ground potential, the one-shot fires to turn off Q19 and Q17.

The marker circuit, Q15 and Q14, is a pulse stretcher that provides a low marker output at P1(12, N). When the input amplifier circuits trigger, U2B(8) provides a positive spike to Q14 base to drive Q14 collector below ground and allow CR5 to conduct. This makes the charge on C6 more positive. When U2B(8) returns to logical zero (approximately -1.6 V), Q14 is back biased and turns off, allowing Q15 to turn on to drive the marker output line low. After C6 has discharged through R36, Q14 turns on again, Q15 turns off, and the marker output line returns to the high state.

During the check mode, A1P1(C) is held high to disable U2B and enable U2A. With U2B disabled, the marker pulses are inhibited. With U2A enabled, the 10 MHz check signal at P1(4,D) connects to the amplifier output line P1(5,E).

A2 TROUBLESHOOTING

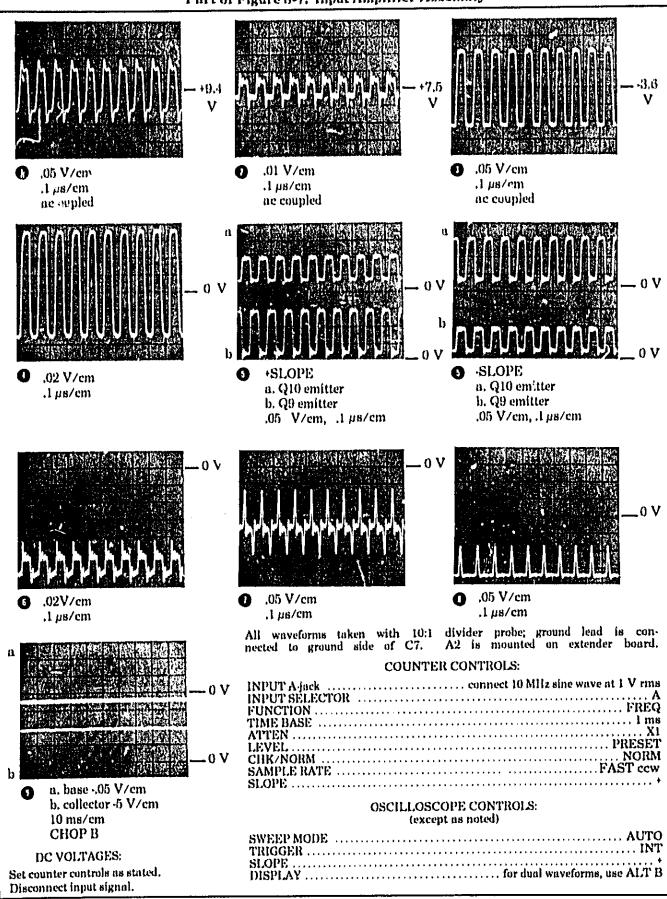
When tracing the signal through the amplifier assembly, a good starting point is the collector of Q4, test point 4. With a sine wave input and the LEVEL control set to zero, this waveform should always resemble a square wave, due to the action of the Schmitt Trigger. A second check would be test point 6. If no signal is available there, check the slope gates of U1 and transistors Q9-Q12. Make use of the waveforms that are provided on this page. Once the problem is confined to a general area, use de voltage checks to pinpoint the trouble.

Figure 8-6
A1/A4 ATTENUATOR/OSCILLATOR BOARD ASSEMBLY

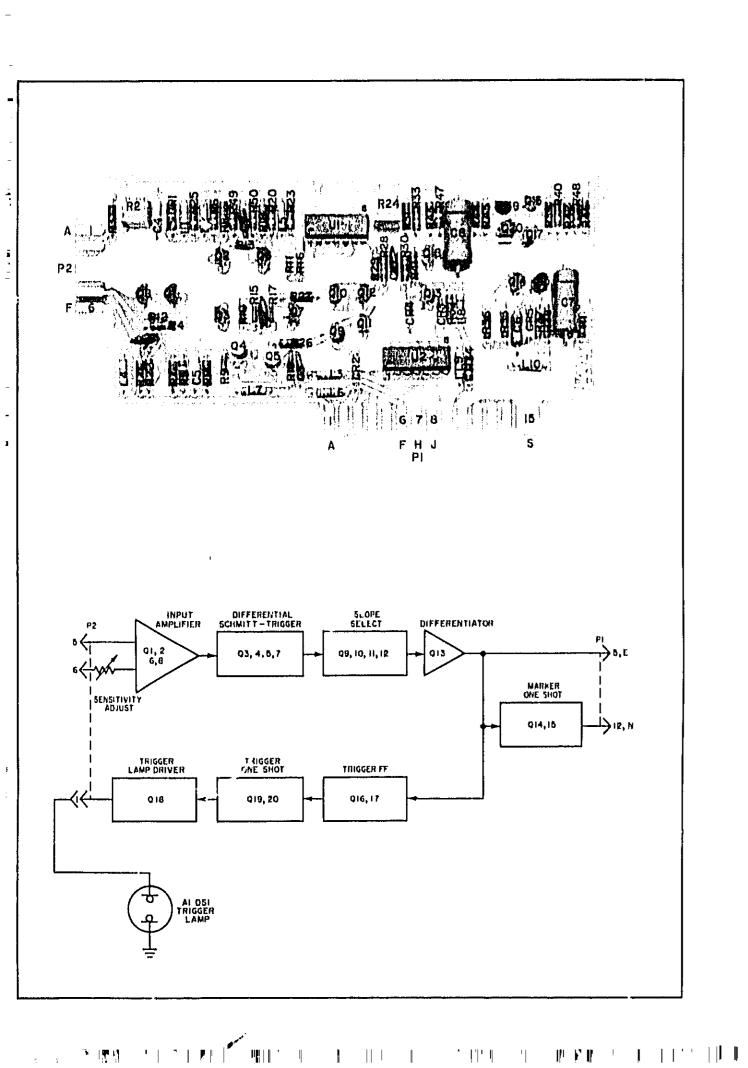
(See Page 8-15)

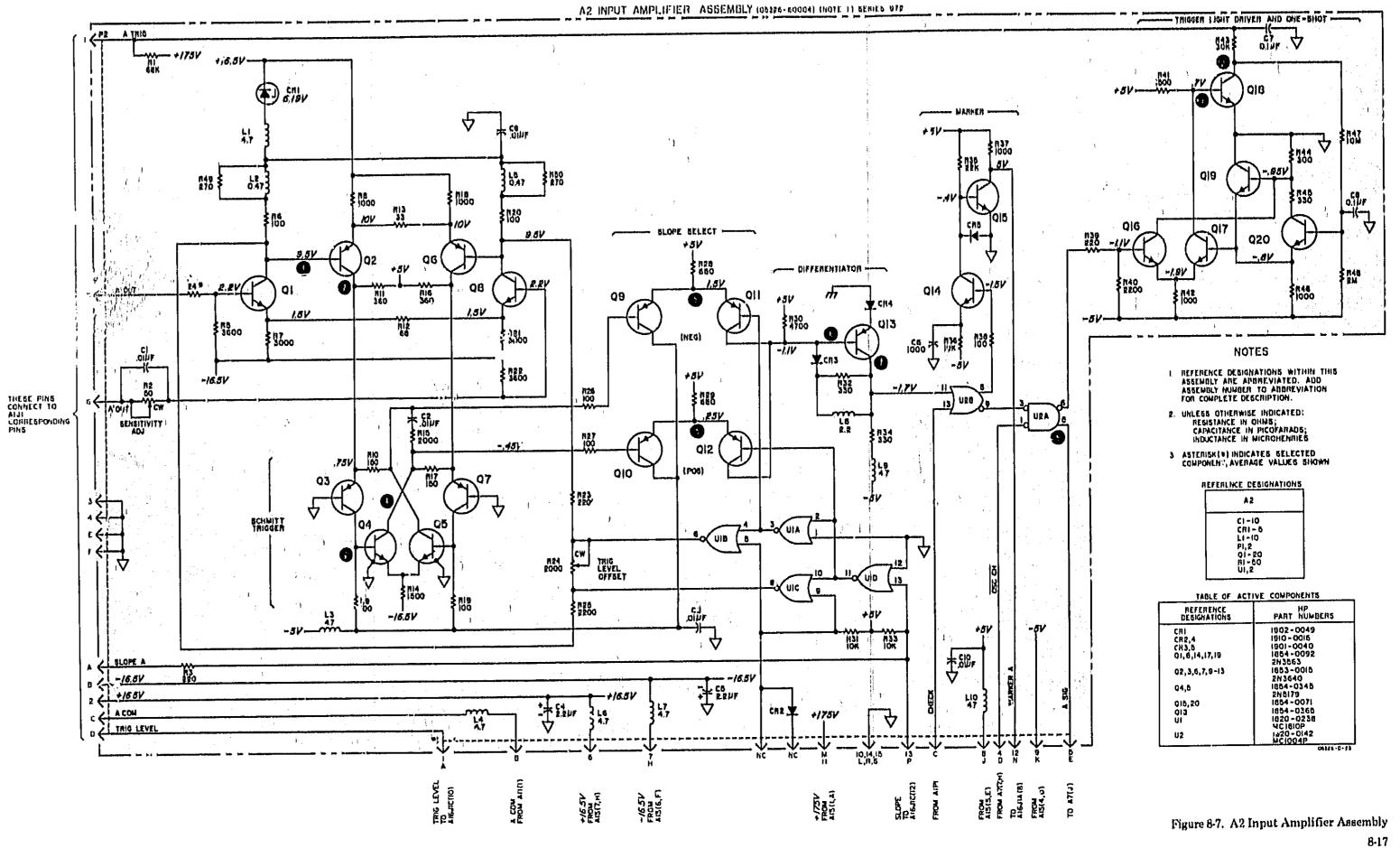
Model 5326/27C Circuit Dingrams

Part of Figure 8-7. Input Amplifier Assembly



MORE DATA UNDER FOLD





AB TIME BASE OPERATION

This assembly contains 8 decade dividers, which are controlled by TIME BASE switch 85. The input signal is 10 MHz for the frequency mode. For the totalize and period-average modes, the decade dividers receive INPUT A signals.

When a particular decade receives a gate-enable signal, the corresponding gated output line is enabled. For example, if 85 is set to .1 second U1(6) is grounded. This gates the divided signal out on U1(5). The gated outputs are connected together on a common line to C5. C5 differentiates the high to low transitions into approximately 100 ns pulses at U5C(8). When S5 is set to .1 µs, the input signal bypasses the decade dividers and passes through U10D and U5D. The output of U5C feeds illerigh U10C to A7 and also through U10E to the rear-panel TIME BASE OUTPUT jack J6.

Q1 and Q2 form an ECL to TTL translator. When the main gate opens (low is main-gate enable), Q2 turns on the start one-shot Q3/Q4. During short gate-length times, this holds the gate lamp enable line low for approximately 50 ms to extend the time the gate lamp is on. When Q1 collector goes high, a low is developed

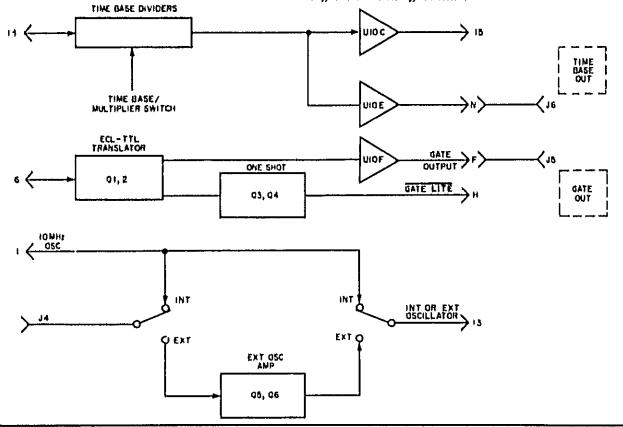
at U10F(12) and routed to the GATE OUT lack 45.

U5A and U5B select either the internal or external oscillator signal. When S7 is set to EXT, the internal oscillator signal is inhibited and the external oscillator signal passes through Schmitt trigger Q5 and Q6 to U5B and XA5(13).

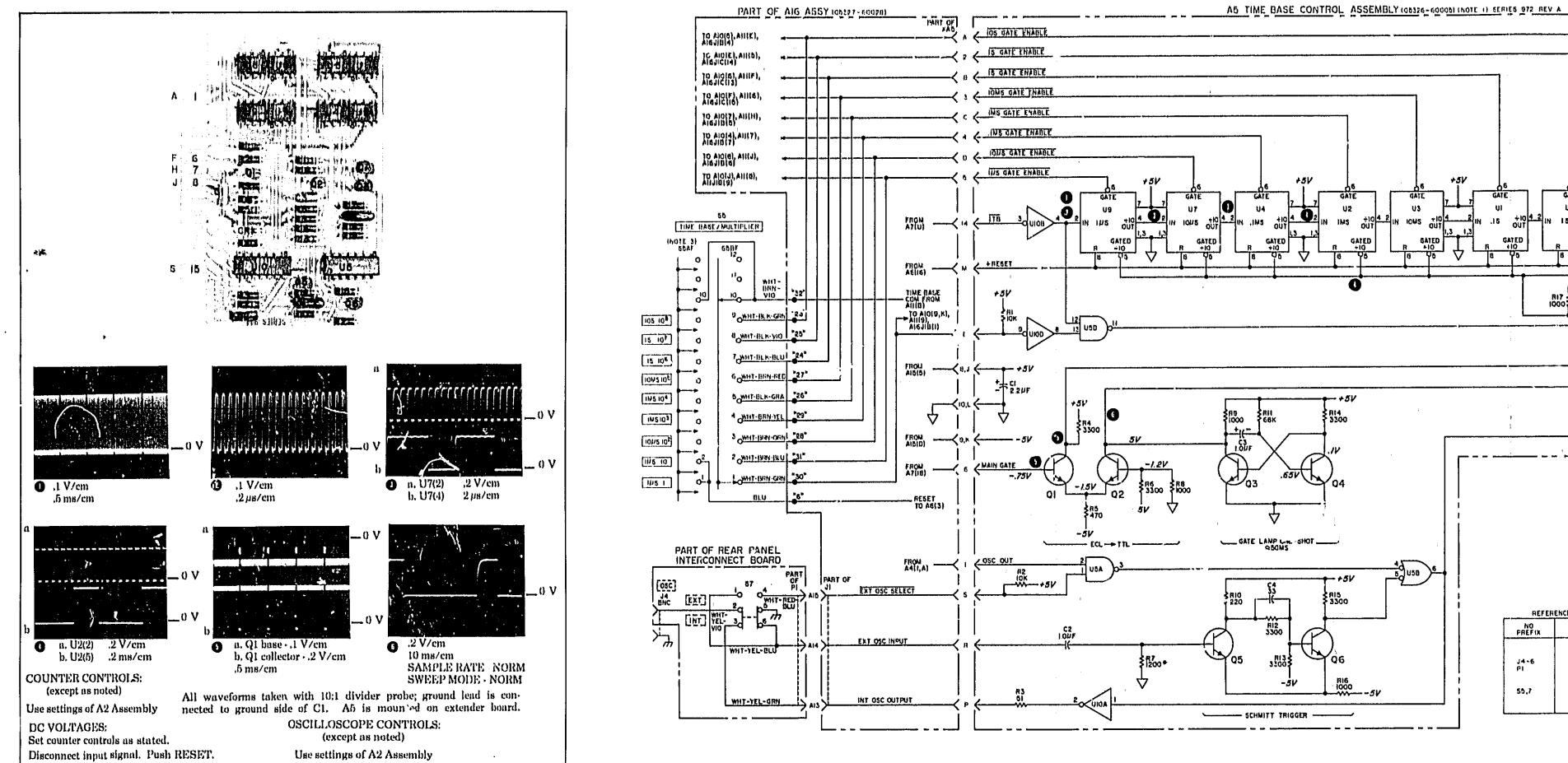
AS TROUBLESHOOTING

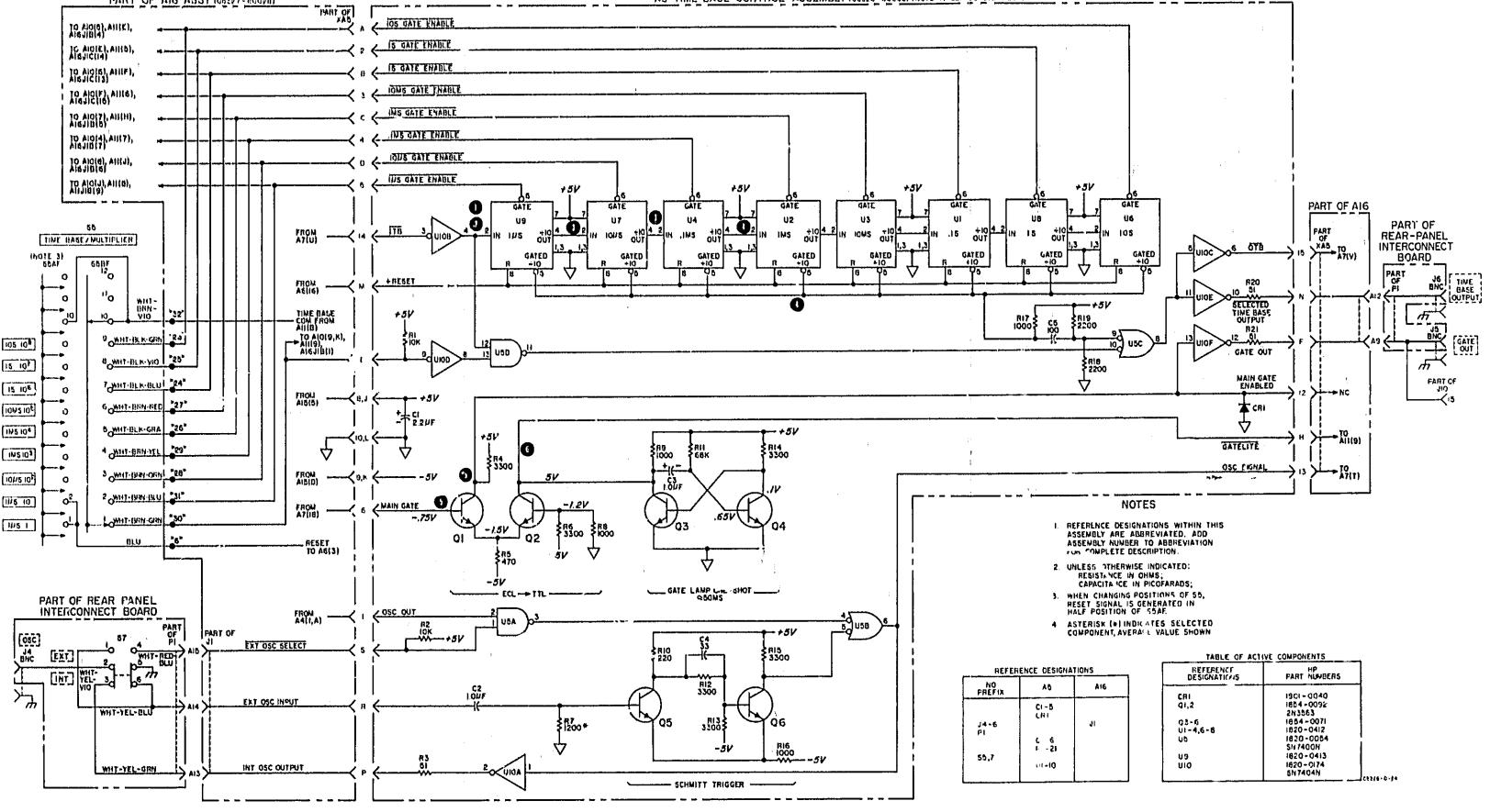
When troubleshooting the Time Base Dividers, place the FUNCTION switch to START and CHK/NORM to CHK. Step the TIME BASE switch through each position and note the counter's display. When the counter stops totalizing, check for a low on pin 6 of the selected decade. If the counter does not totalize for any position of the TIME BASE switch, the problem is in the circuitry of U10B, U10C, or U5C. Before the ented output is sent to the A7 Function Corcel, it is differentiated by C5 and R15. This produces extremely sharp pulses, which are best observed when the gate time is 0.1 µs (TIME BASE switch).

To check the operation of the Gate Lamp oneshot, check for waveform 5 and 6 with SAMPLE RATE switch to NORM. The Collector of Q3 should be Low for about 50 ms, regardless of the gate time.



1 t l





and the state of t

Figure 8-8, A5 Time Base Control Assembly

AG SAMPLE RATE OPERATION

The cample-rate circuits determine interrogation rates for the input signal and provide several functions for the various operating modes. These functions include generating reset, transfer, print command, and main-gate inhibit signals. In addition, the circuits receive computer inhibit, printer inhibit, and manual reset signals. The circuits also serve to control storage and display-hold functions.

As an example of operation, assume the following operating conditions: STORAGE to ON. SAMPLE RATE to I AST, no printer inhibit, no computer inhibit, no manual reset, and main gate open. At the end of the gate time, Pin 17 goes high, which sets inhibit flip-flop U2. This sends a signal to U6C to generate a high inhibit at U6B(9). In addition, a low is generated at Q4 collector to trigger the sample rate one-shot if no printer inhibit is present at U5B(6). The display time starts at this point, and the high at U5C(10) generates a low at U1D(11). The resulting high on UIC(8) turns on Q6, giving a low at the collector, which is the print, command. Also at this time, the low on UIB(6) activates U4C through differentiator C5 to generate the positive and negative transfer signals at pins T and K, respectively.

When the sample rate one-shot is set, U1B(6) goes low to turn off Q7, allowing the +5 V supply and R41 - R1 to charge C4 for the display time. C3 is also connected for the NORM position. R1 varies the display time by varying the time requi d to bring Q8 base to a sufficient plus value to trigger Schmitt Trigger Q8 through Q10. This gives a high at U1A(1). The reset will be delayed until there is no print inhibit. U1A(3) will go low, generating a high at U3B(6), which is fed out at A6(16). The negative reset at U3C(8) is fed out at pin 9 in addition to being used to reset the sample rate one-shot.

The positive reset is used on A6 after passing through level shifter CR7 and CR8. The positive reset turns on Q1 and applies an ECL high to clear U2 and also turns on Q2, which maintains inhibit approximately 200 ns after the end of the reset pulse, At this time, the inhibit goes low and the main-gate circuits are free to function.

Q11 circuitry is a reset one-shot that ensures a sufficiently long reset pulse. The reset pulse width is approximately 40 µs or 400 µs, as determined by the FAST/NCRM switch. For NORM sample rates. S2 switches C10 in parallel with C8. The sample rate disable line (pins 10, L) is low during START mode and maintains continuous transfer through CR2 and prohibits main-gate inhibit through U4D in addition to holding down Q8 base through CR2. This prevents the react from being generated,

When STORAGE is OFF. U5A is activated to maintain transfer through CR4. The manual reset (pin 3) holds the reset one-shot in the ON state as long as the RESET button is depressed (reset low). It also maintains the transfer during the same time to clear the display. In addition, it turns on the main-gate inhibit, even if the main gate is open. The manual reset signal is low if the RESET button is depressed or if the TIME BASE or FUNCTION switch is between positions. (No reset is generated between start and stop positions.)

AG TROUBLESHOOTING

Troubleshooting the Sample Rate board is best accomplished when the board is in a static state. The procedure given below examines each section separately when the circuit is in a working, but static, condition. Perform the tests in order listed. The schematic shows the circuit levels after RESET is pushed. These levels should be used

NOTE

Do not use an input signal when performing the tests below.

MAIN GATE INHIBIT, PRINT COMMAND DRIVER, and SAMPLE RATE ONE-SHOT. Before troubleshooting, perform the procedure

FUNCTION switch FREQ A
TIME BASE switch
SAMPLE RATE switch HOLD
SLOPE switch +
CHK/NORM NORM
STORAGE switch ON
LEVEL control full ew
Push RESET
(Note that trigger lamp fires)

The purpose of this procedure is to set these circuits to the point immediately after the main gate closes. Varying the LEVEL control triggers a pulse to open the main gate for 1-second, and pin 17 goes Low during the gate time. U2 sets when the gate closes (positive transition) and remains set with the SAMPLE RATE switch set to HOLD. Once U2 sets, check for a Low on U5C(8). This generates a High on UIC(8) and a Low on UID(6). Check that U4C(8) pulses High and Q6 collector sets Low. The main gate inhibit line at U6B(9) should now be High. The collector of Q7 is not now affected.

SAMPLE RATE INHIBIT. The sample rate inhibit gates are controlled by the FUNCTION and STORAGE switches and by a print inhibit signal. With the controls set as above, check for the levels shown on the schematic.

SCHMITT TRIGGER. The Schmitt Trigger and Q7 should be checked by using an input signal. Set the counter controls as listed under the waveforms. In waveform five, the repetition rate of the pulses changes with gate time, but pulse width remains the same. Pulse width changes with the SAMPLE RATE controls. but not spacing.

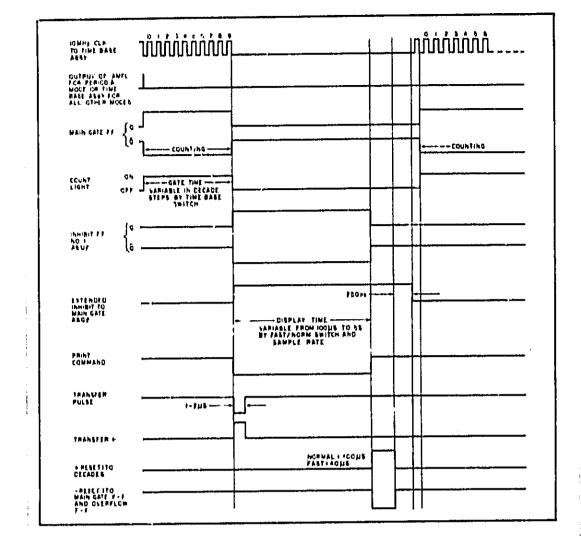
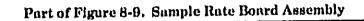
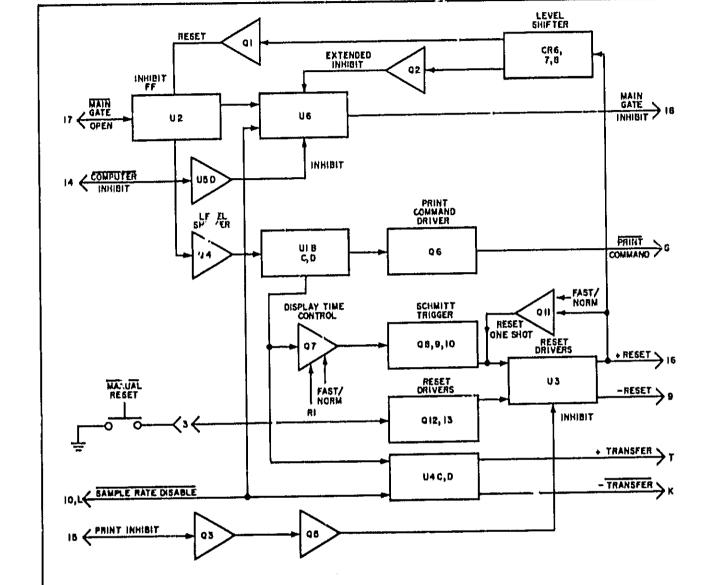
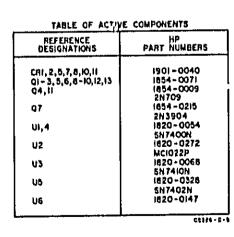


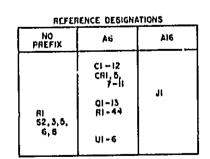
Figure 8-8 A5 TIME BASE CONTROL ASSEMBLY (See Page 0-19)

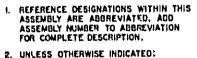
Model 5326/27C Circuit Diagrams







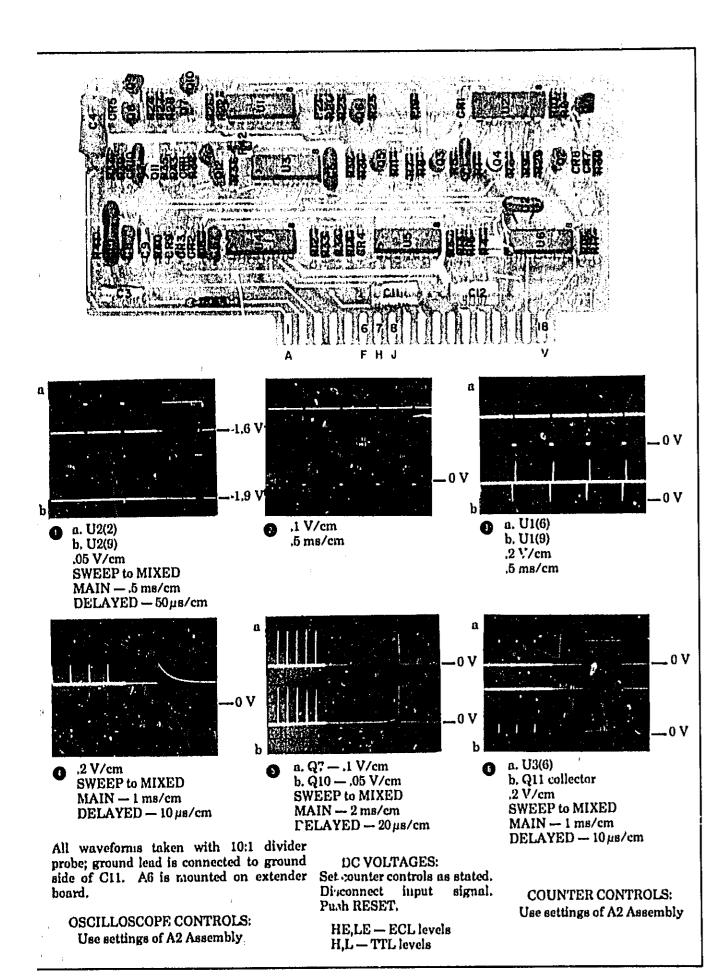




NOTES

- 3 ASTERISK () INDICATES SELECTED COMPONENT, AVERAGE VALUES SHOWN.
- AS BOARD WITH SERIES 1132A DOES NOT INCLUDE R44,

MORE DATA UNDER FOLD



SAMPLE RATE HOLD NEGATIVE RESET (TTL) - RESET ONE SHOY MAIN GATE INHIBIT (HIGH .) PART OF AIG CONNECTOR BOARD PART OF AIG CONNECTOR BOARD FROM JIQ(35) 52 SAMPLE RATE FUNCTION P/O SEEF 2 STOP POSITIVE RESET (OV - RESET TRATE POSITIVE RESET TO ASIM), ASPILE), ASPILH) FROM AII(2) FROM JIO(16) FROM JIO(34) P/0 53 P/O 55 FROM ABID) SAMPLE RATE FROM AIS(5,E) FROM A1512,3,8,C) FROM A15(4,0)

A6 SAMPLE RATE BOARD ASSEMBLY (08326-GOOIS) (NOTE I) SERIES 1224A REV. C

Figure 8-9, A6 Sample Rate Board Assembly 8-21

A7 FUNCTION CONTROL

Controlled by the front-panel FUNCTION switch, A7 directs the flow of the input, time-base output, and oscillator signals to the time-base decades (A5) and decade counter (A8). The scaled time-base signal (OTB) returns to A7 and opens the main gate for a time determined by the setting of the TIME BASE/MULTIPLIER switch and the period of the time base input signal.

Frequency Measurement (A or B)

The INPUT A frequency enters on P1(J) and passes through an enabled UGF to U5B(5). This gate is also enabled, since the FUNCTION switch pulls only the FREQ line low. The input signal passes through U5D and U6D to U7B(8, 9). At the same time, the input signal, along with a low from U4C(10), sets the oscillator arming flip-flop, which places a high at U2(5). The next positivegoing oscillator signal sets synchronizer U2 and allows the signal to pass through U1A and U5E to the base of Q1, which conducts, since U4C(10) is pulling Q2 base low. The time base input one-shot stretches the pulse width to about 50 ns before sending the signal to A5. The decade dividers on A5 scale the signal by a power of 10 and return it to A7 on P1(V). The main gate toggles with the first incoming negative pulse from the time-base dividers. The input frequency is then sent to the decade counters through U7B. The next time base output pulse toggles the main gate flip-flop closed, ending the measurement. The main gate inhibit line (from A6) goes high, locking closed U3A, resetting U2 and U1C&D, and turning off ITB (input to time base) gate by pulling Q2 base high.

Period Average

Period average measurements are similar to frequency measurements, except the input frequency is sent to the time-base dividers, and the oscillator signal is counted by the decade counters on A8 for subsequent display. The main gate is held open until A5 totals the number of periods selected with the MULTIPLIER switch.

Start

When totalizing the input signal, a high on U6(4) holds the main gate open (U3A set) as long as the FUNCTION switch is in the START position. The input signal is sent to the decade counters after being scaled by A5.

Check

When the CHK-NORM switch is in CHK, the oscillator signal (OSCCH) substitutes for the Channel A amplifier output. The check line is high, allowing the oscillator square wave from USC(13) to be differentiated by C1, R1, and R2. The negative-going pulses are sharpened by U4B and sent to A2 amplifier. These pulses return on A7P1(J) and are treated like any other amplifier signal in each of the above functions.

Levels

The incoming signals of A SIG, B SIG, check, and inhibit are ECL levels. All other input lines are TTL and are converted to ECL through translators. The output signals are ECL, except the ITB, which is TTL.

A7 TROUBLESHOOTING

Before troubleshooting A7 Function Control, note the Trouble Isolation Table in Section V; also, the flow diagrams of the A7 schematic provide signal paths and logic levels for a specific mode. Assuming the input signals and control lines are operating properly, troubleshooting the circuit can be simplified if tested as follows:

NOTE

Set SAMPLE RATE switch to FAST and control full cew. Use a 1 s gate time.

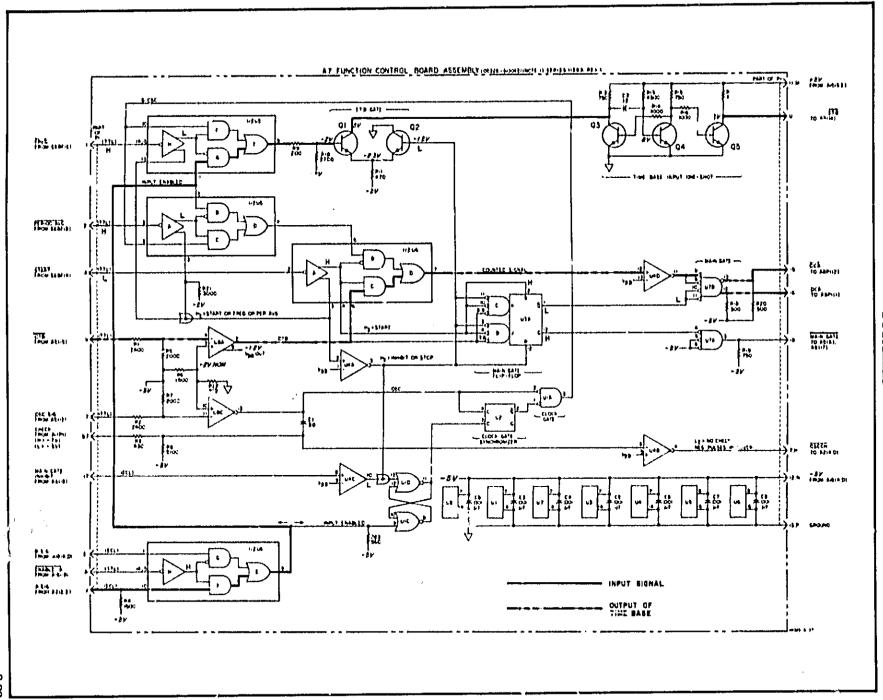
FREQUENCY	PERIOD AVERAGE	TOTALIZE
1. Check U4D(11) for counted signal	1. Check U4D(11) for gated oscillator signa	1. Check U5(9) for input signal.
2. Check U5(9) for gated oscillator signal	2. Check U5(9) for input signal.	2. Check A7(U) for ITB signal.
3, Check A7(U) for ITB signal.	3. Check A7(U) for ITB signal.	3. Check U4D(11) for counted signal.
4. Switch to fast gate_time. Check that OTB signal has set Main Gate flip-flop U3A (pin 1=L, pin 13=H).	4. Switch to fast gate time. Check that OTB signal has set Main Gate flip-flop U3A (pin 1=L, pin 13=H).	4. Check that High on U3A(2) has set the flip-flop (pin 1=L, pin 13=H).
5. Check for gated count signal at U7B(13 and 12).	5. Check for gated count signal at U7B (13 and 12).	5. Check for gated count signal at U7B(13 and 12).

Figure 8-9 A6 SAMPLE RATE BOARD ASSEMBLY

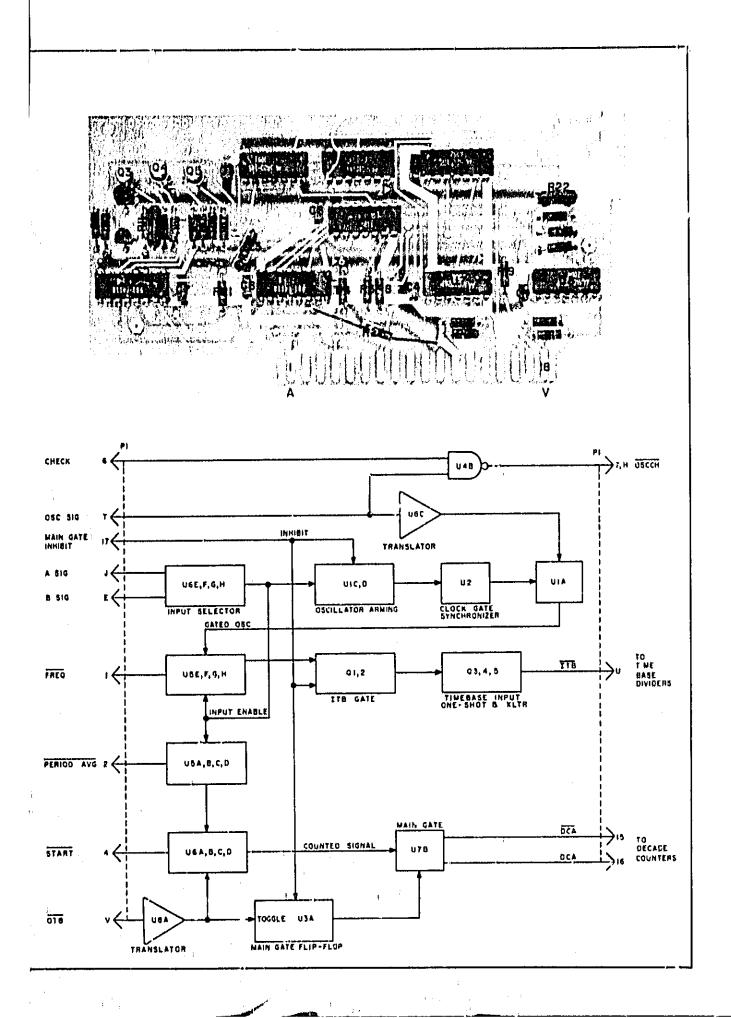
(See Page 8-21)

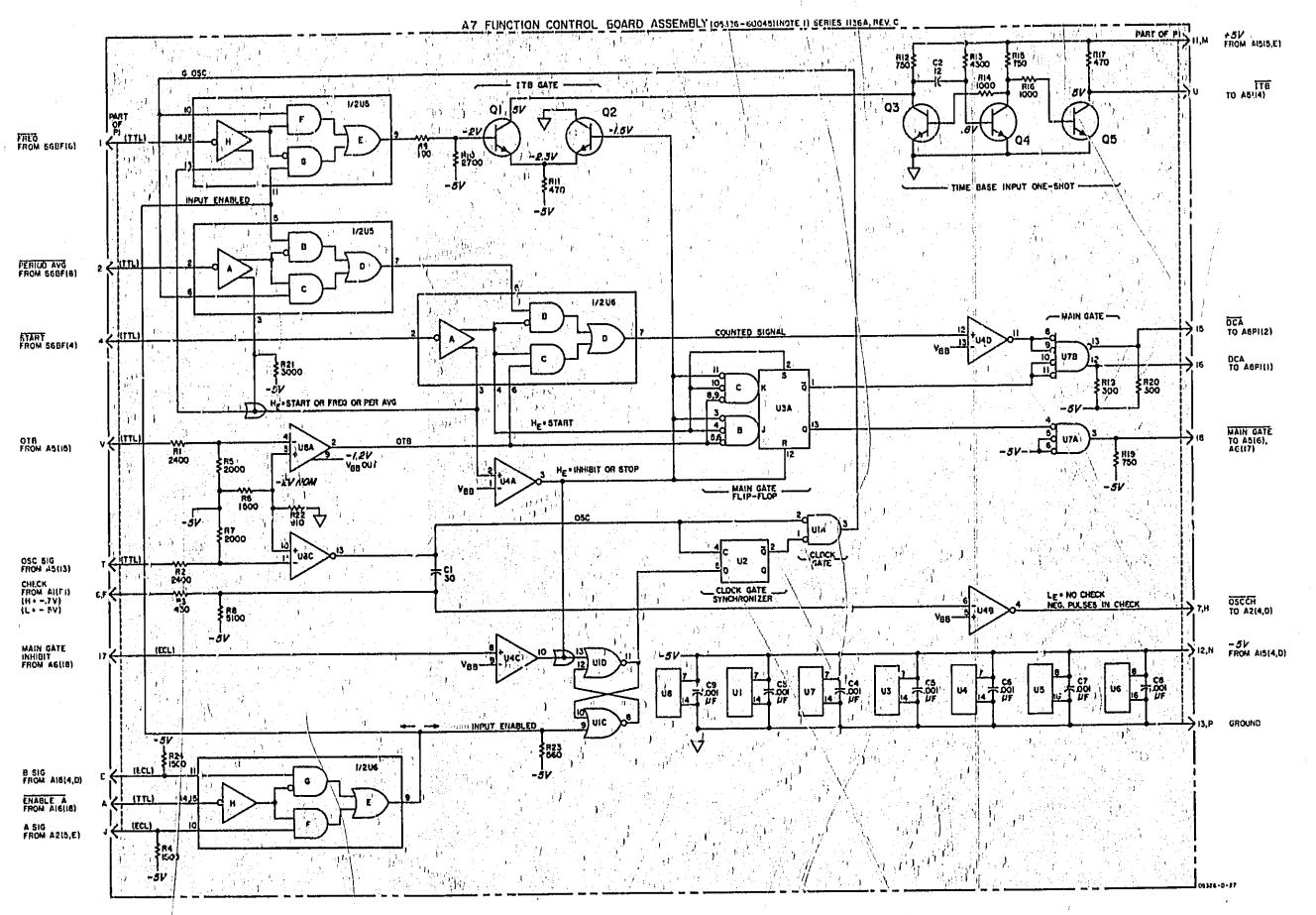
FREQUENCY

Model 5326/27C Circuit Diagrams



PERIOD AVERAGE





NOT

- I. REFERENCE DESIGNATIONS WITHIN THIS ASSEMBLY ARE ABBREVIATED. ADD ASSEMBLY NUMBER TO ABBREVIATION FOR COMPLETE DESCRIPTION.
- 2. UNLESS OTHERWISE INDICATED: RESISTANCE IN OHMS; CAPACITANCE IN PICOFARADS;

REFERENCE DESIGNATIONS

A7	
C1 = 9 P1 O1 = 5 V1 = 24 U\= 6	

TABLE OF ACTIVE COMPONENTS

REFERENCE DESIGNATIONS	PART NUMBERS
Q1,2	1854-0215
Q3,4,5	1854-0009
טו	1820-0145
••	MCIQIO
U2	1820-0440
~-	MC1016
U3	1620-0102
ŭ4	1820-0212
•	MCIOZO
U5.6	1820-0489
u7	1820-0252
	MC1026
UB	1820-0253
	MC 1035

TRUTH TABLE

GATE TOGGLE	DÇA	178
OTB OTB I	E G OSC OTB	G OSC I E I E O
	TOGGLE OTB OTB	OTB B COSC I OTB

Figure 8-10. A7 Function Control Board Assembly

A6 DISPLAY SUPPORT OPERATION

The display support assembly A8 serves to interconnect the display assembly A9 with the interconnect assembly A16. In addition, A8 contains a high-speed decade counter, decimal point drivers, and blanking (logic) circuits.

The high-speed decade consists of four JK flip-flops U3 through U6. The line receiver, Q2 and Q9, serves to reduce noise levels on the signal from A7 prior to driving U3(6, 9). U3 divides by two and the combination of U4 through U6 divides by five. The decade supplies BCD outputs to A9 via J1(5, 4, 2, 3) for the 10° display tube. The D output is also used as the carry output to the next decade counter on A9. Q1 translates the positive TTL reset signal to ECL levels to reset the high-speed decade to zero.

Decimal point drivers Q3 through Q8 work in conjunction with logic circuits on A11 to light the proper decimal points. R15 and R17 provide operating bias for Q3 through Q8, R19, R20, and R23 are current limiters. R2 and R3 provide 87.5 volts pre-bias for the OFF decimal points. R5 through R10 connect the off decimals to the pre-bias voltage to eliminate background glow.

As an example of operation, when a ground is received at P1(S) from A11, Q5 conducts. With Q5 on, decimal point enable line 3 (DP3) is pulled to ground to light the decimal point on A9DS4(10³). Also with P1(S) low, U1D(11) is high to unblank A9 U4. When U1D(11) goes high, U1B(6) and U1A(3) are also high to unblank A9U3 and U2. This unblanks A9DS4, DS3, and DS2. DS5 and DS6 remain blanked. DS1 is never blanked, and DS7 and DS8 (Option 901) will always be blanked.

CR2 and CR3 are included for use with the digital recorder Option 003. When overflow occurs, P1(M) and J1(15) go low. CR2 and CR3 cause J1(14 and R) to also go low. When J1(15, 14, R) are low, the recorder will print a zero on the annunciator line. R21 and R24 are pull-up resistors.

AS TROUBLESHOOTING

High Speed Decade

If a problem in the High-Speed Decade is not readily apparent when checking for the correct waveforms, a step-through method may be preferable. Set the counter as follows:

- 1. MULTIPLIER switch to 107.
- 2. CHK/NORM switch to CHK.
- 3. FUNCTION switch to START.
- 4. Press RESET.

The High-Speed Decade has four output lines that are binary weighted DCBA. Release the RESET button and note the counter's display. A typical problem is as follows: The display counts 1....2....3....0....1....2....3....0. When the display reads "u," set the FUNCTION switch to STOP and check the C line for a Low (refer to the table below). Check the input lines of the IC, since their levels depend on the state of other IC's in the circuit (note U4 pin 3 and U6 pin 13). The levels given below are ECL

	Λ	В	С	D
DISPLAY	Ų3(13)	U4(1)	U5(1)	U6(1)
1	L	Н	Н	Н
2	н	L	н	Н
3	L	L	Н	Н
4	Н	Н	L	Н
5	L	H	L	Н
6	Н	L	L	Н
7	L	L	L	Н
8	н	Н	н	L
9	L	Н	Н	L
10	H	Н	Н	Н
11	REPEATS			

Decimal Point and Blanking

Before testing the decimal point and blanking circuitry, set the CHK/NORM switch to NORM and disconnect the input signal.

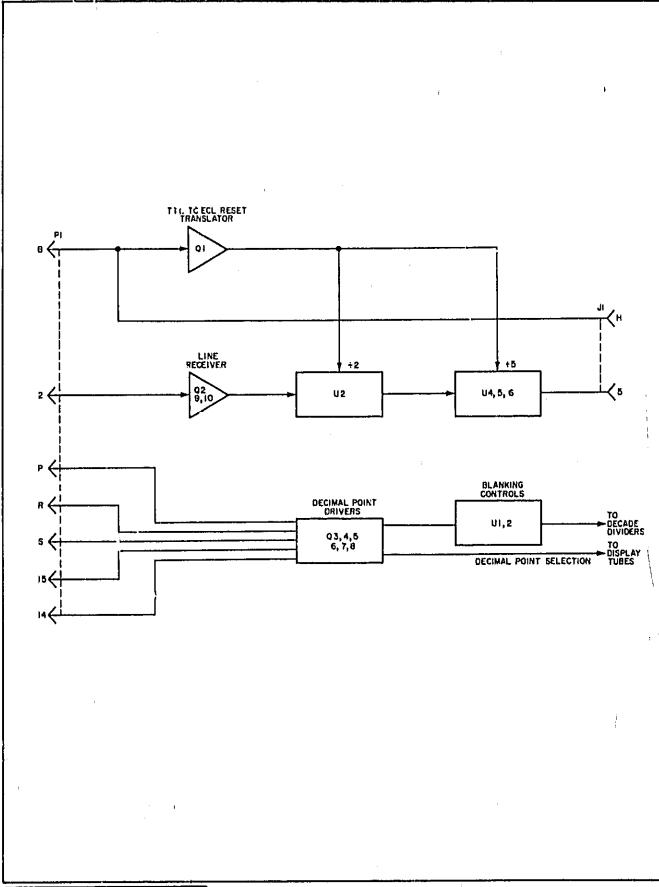
DECIMAL POINT. To check the decimal point circuitry, set FUNCTION switch to PERIOD AVG and position the TIME BASE switch to pull the required D.P. line Low.

LINE	MULTIPLIER POSITION	DRIVER
D.P.0	, '1	Q 8
D.P.1	10 1	Q7
D.P.2	102	Q6
D.P.3	106	Q5
D.P.4	10 ⁷	Q4
D.P.5	10%	ପ୍ତଃ

BLANKING. To check the blanking circuitry, set the FUNCTION switch to PERIOD AVG and MULTIPLIER switch to 1. All digits, except the first one, should now be blanked. If another digit is lit, check that line at A8J1 for a High level, which indicates a problem on that line.

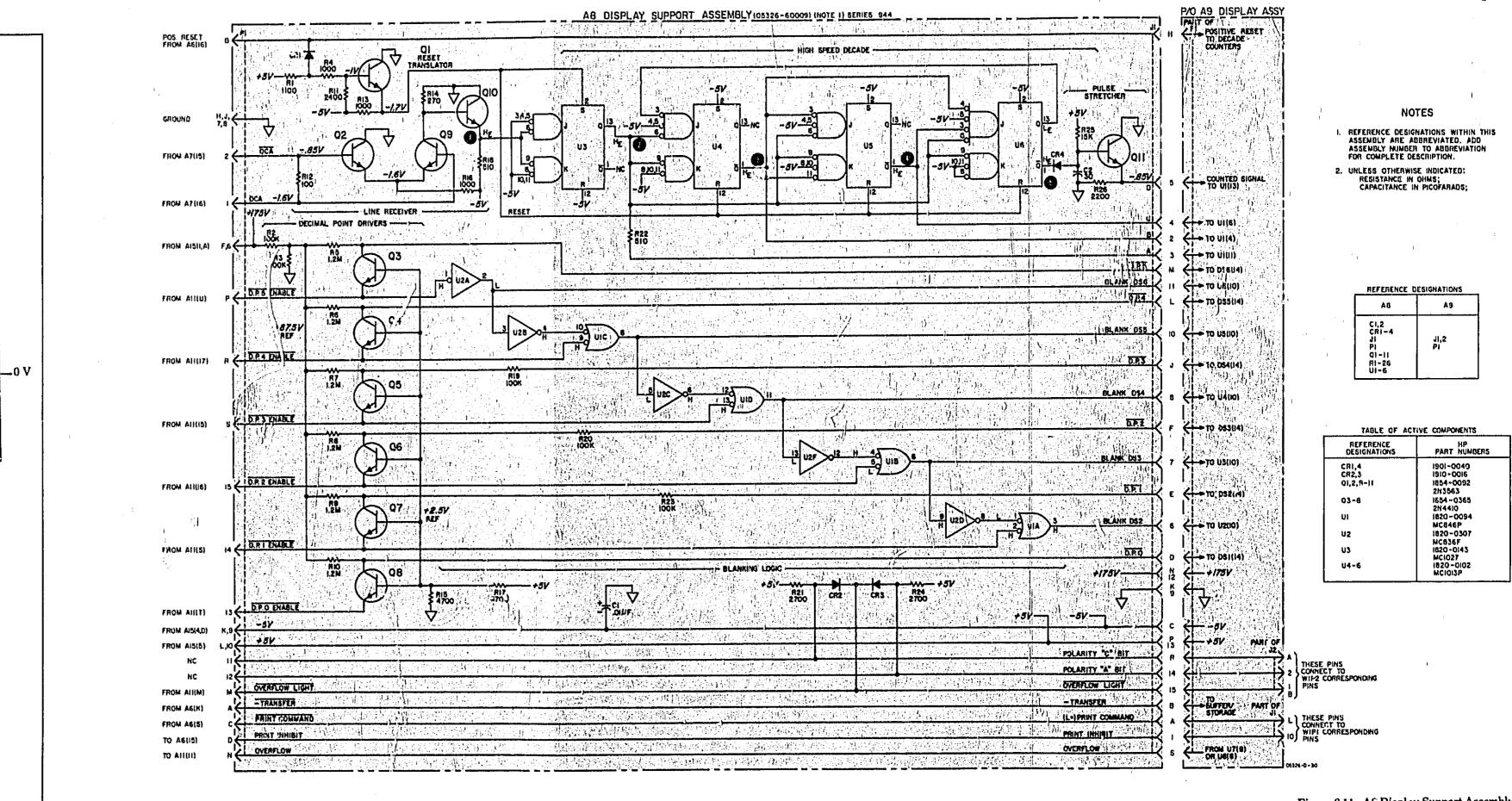
Figure 8-10
A7 FUNCTION CONTROL BOARD ASSEMBLY
(See Page 8-25)

Model 5226/27C Circuit Diagrams



MORE DATA UNDER FOLD

8-26



All waveforms taken with 10:1 divider probe; ground lead is connected to junction of R15, R3.

COUNTER CONTROLS:

Use settings of A2 Assembly

HE,LE — ECL Levels H,L — TTL Levels

DC VOLTAGES:

Set counter controls as stated. Disconnect input signal

Push RESET.

OSCILLOSCOPE CONTROLS:

TIME/CM2 μs/cm

SWEEP MODE AUTO

TRIGGER INT

SLOPE+

Figure 8-11. As Display Support Assembly

0-21

A9 DISPLAY ASSEMBLY OPERATION

Display assembly A9 contains decade counters U2 through U7, buffer storage units U9 through U15. BCD to decimal converters U17 through U23, and display tubes DS1 though DS7.

U1 translates the ECL data from A8 into TTL levels for use by circuits on A9. Each translator of U1 is noninverting. The D output at U1(15) is the counted signal divided by 10 and is used as the input to 10¹ decade U2.

Decade counters U2 through U7 count the number of input pulses while the main gate is open. Each decade provides a .8421 BCD output to the corresponding buffer storage unit. When pin 14 (reset) goes High, the decades reset to zero if pin 10 is High; the decades reset to 15 (blank) if pin 10 is Low. The ECL decade on A8 never blanks. U7 and U5 (Option 001) always blank. The last decade supplies an overflow output at pin 8 when the count exceeds the capacity.

Buffer storage units U9 through U15 receive the BCD outputs of the decades. When the counter operates in the storage-on mode, data is transferred when a low transfer pulse arrives at pin 5 of the buffers. When the transfer line is high, the buffers will store the data to allow a continuous display while a new measurement is being made. During storage-off or totalize mode, BCD data is continuously fed from the buffers to the decoders. The buffers also supply +8421 BCD outputs to A9 J1 and J2 for further distribution to J9 when Option 003 is included.

Decoder drivers U17 through U23 receive the 8421 BCD data and provide a decoded decimal output to light the corresponding numeral on the display tube. The terminal for an illuminated numeral will be approximately +2 volts whereas an extinguished numeral is typically +100 volts. The decimal point terminal (14) of the display tube is <5 volts when lit and about 87.5 volts when extinguished.

A9 TROUBLESHOOTING

The A9 Display Assembly may be set up for troubleshooting with either of two methods. A highly accurate oscillator may be used for a front-panel input signal. Any difference in count from the input signal is then immediately obvious on the display. Check for the proper signal division of the decade counter in previous column. As an alternate method, place the CHK/NORM switch in CHK and the FUNCTION switch in START. Allow the count to totalize until the problem occurs; then, set the FUNCTIC. 'switch to STOP, Use the TIME BASE switch to adjust the rate of counting. When the problem appears, check the circuitry of that column.

Start by checking the Buffer-Storage outputs (U9-U15) for the BCD code of the number that should be displayed, rather than what is displayed (see Table 1). Check that the Buffer-Storage code pulls the proper decimal line low on the BCD-to-Decimal Decoder.

Table 1

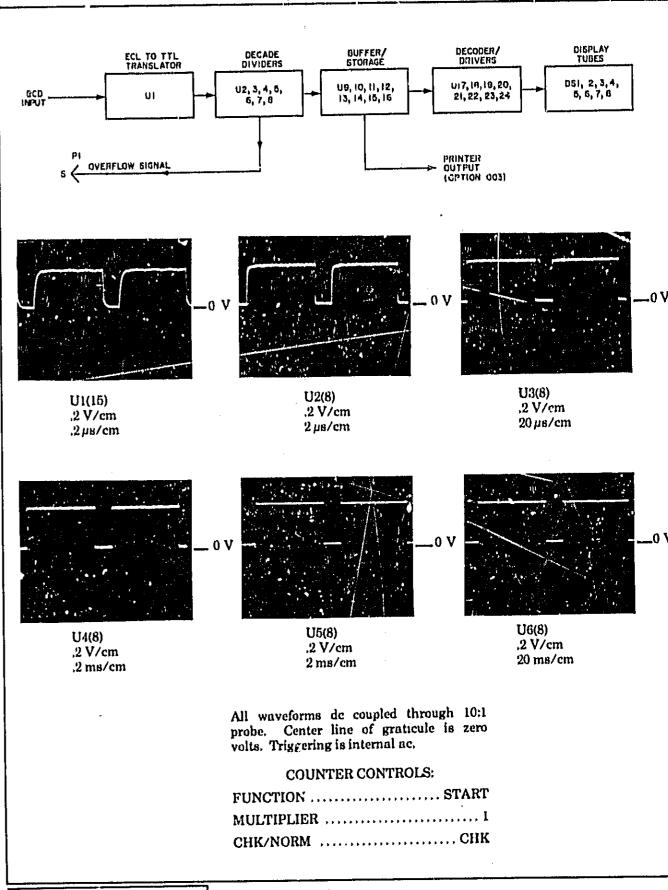
DISPLAYED DIGIT	BUFFER STORAGE BCD (TTL)			
	8	4	2	1
0	H	Н	Н	Н
1	H	н	н	L
2	H	n	L	н
3	H	H	L	L L
4	H	L	Н	н
5	Н	L	Н	L L
6	Н	L	L	Н
7	H	L	L	L
8	L	Н	Н	H
9	L	\ \ H	H	L
Blank	L	<u> </u>	L	<u> </u>

Figure 8-11 A8 DISPLAY SUPPORT ASSEMBLY

(See Page 8-27)

Model 5326/27C Circuit Diagrams

Part of Figure 8-12. A9 Display Assembly



MORE DATA UNDER FOLD

·2X

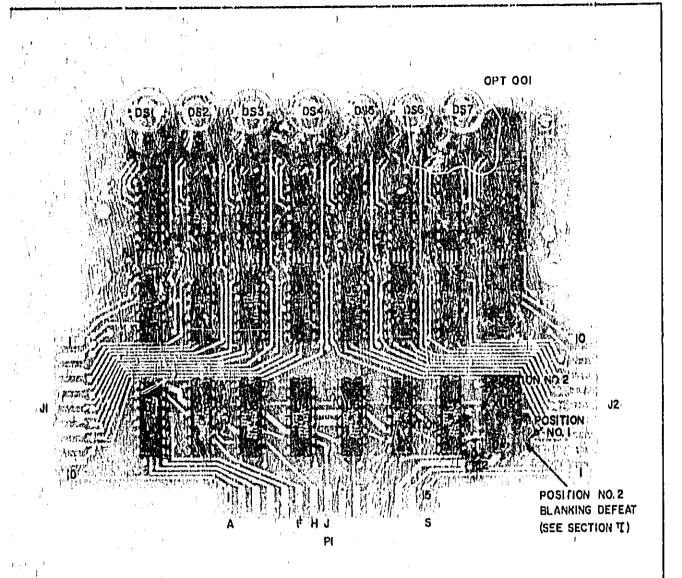


TABLE OF ACTIVE COMPONENTS

REFERÊNCE DESIGNATIONS	PART NUMBER	
A0	1 :	
CRI	ieo - 0040	
CR2	1201 - 0016	
A9 ,		
i u	1820-0275	
ĭ	MCt039P	
U2 - 7	1820-0119	
U9 = 15	1820-0116	
UIB - 23	1820-0092	
UI7	1820-0729	

AB	ØA	ı
CRI, 2	051-7 J1, 2	9
	J1, 2 P1 A1-12	

NO PREFIX WIPI,F2

REFERENCE DESIGNATIONS WITHIN THIS ASSEMBLY ARE ABBREVIATED. ADD ASSEMBLY NUMBER TO ABBREVIATION FOR COMPLETE DESCRIPTION.

2. UNLESS OTHERWISE INDICATED: RESISTANCE IN CHINS;

3. RIO IS REMOVED FROM BOARD IN OPTION COL.

4. DS8, U8, 16,24 ARE ADDED FOR CPTION OOI.

PART OF AB	AO CIEPLAY ASSEMBLY (08326-60008) OPTION OOI (08326-60026) (NOTE I) SERIES 1224A REV ()
	OP7ION 001
FOM S THE THE TOTAL OF THE TOTA	₹800 108 ₹800 108 ₹800 107 7000\$
DISPLAY TUBE	7 DS2 7 DS5 7 DS6 (SAME AS DSI) (SAME AS DSI) (SAME AS DSI) (SAME AS DSI) (SAME AS DSI)
0 1 2 314 6 6 7 8 8	14 do.p.2 14 do.p.3 14 do.p.4 10 do.p.4 10 do.p.4 14 do.p.4 14 do.p.5 10 do.p.4 14 do.p.5 10 do.p.4 14 do.p.5 10 do.p.4 14 do.p.5 10 do.p.6 10 do.
1	
PROM E D.P.1 DECIMAL POINT D.P.2 DRIVENS D.P.3	
1 2 5.6.4	
FROM PART 1 2 3 4 1 12 13 14 15 16	6 1 2 3 4 11 12 13 14 13 16 13 6 13 12 13 14 13 12 13 14 13 14 13 15 16 4 13 1
	S
	paptori i i i i i i i i i i i i i i i i i i
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FROM C COMMOND A COMMOND	
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FROM UZA II C BLANK DS6 OVERFLOW	。
ATTOM N CONTRACTOR STATE OF THE	是有一种的,我们就是一个人的,我们就是一个人的,我们就是一个人的,我们就是一个人的,我们就是一个人的,我们就是一个人的,我们就是一个人的,我们就是一个人的,我们 第一个人的人的人的人,我们就是一个人的人的人,我们就是一个人的人的人,我们就是一个人的人的人的人的人的人的人的人,我们就是一个人的人的人的人的人,我们就是一个人的

Figure 8-12. A9 Display Assembly

A10 RIGHT READOUT OPERATION:

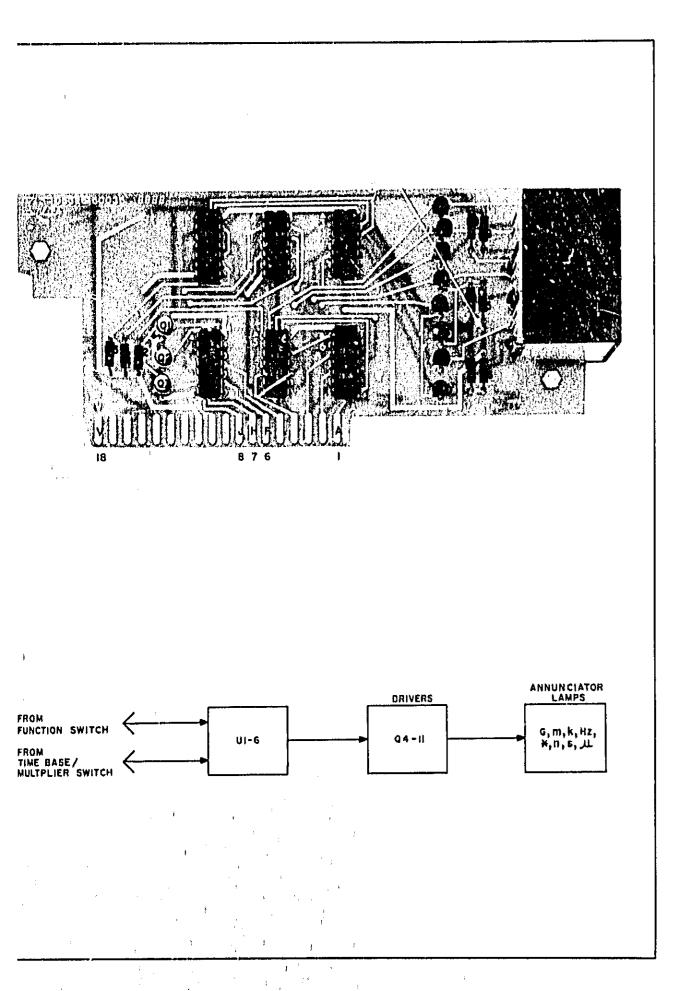
The right readout contains DTL logic to provide the proper measurement units for a given setting of the front-panel controls. A10 logic is negative true, and a low (0.8 volt) to the emitter of any driver transistor will light the given ncon. When a DTL high is applied on the emitters, the transistor reverse biases to turn off the neon lamps. The voltage dividers provide a reference of 1 V (nominal) to the bases of the drivers.

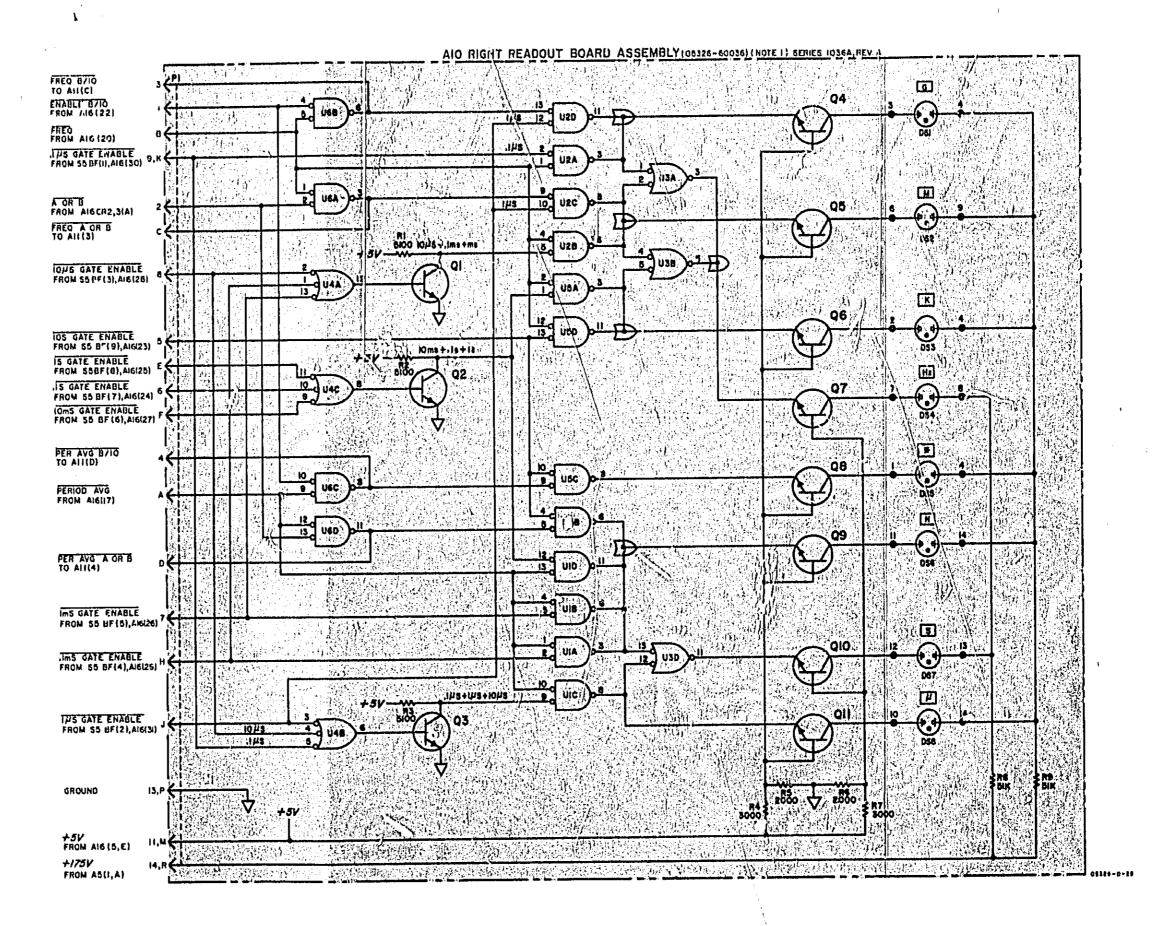
Selecting a function mode and time by pully a pair of these lines low, activating a gate. The low on the gate output will forward bias the driver transistor to turn on the annunciator lamp. For example, selection of frequency and 1 ms makes the output of U2B(6) low, turning on Q5 to light DS2. U3B(6) is also low, lighting DS4.

The asterisk (*) annunciator is activated for the 5327C model only. This occurs when the instrument is in PERIOD AVG, B+10, and 10 s gate time. An asterisk indicates the proper units are not displayed.

A10 TROUBLESHOOTING

If one of the annunciator lights does not work, set the front panel switches to the positions that would normally cause the lamp to be on. Refer to the tables in Figure 3-3 for the appropriate switch settings. Check the gates whose input lines correspond to the switch settings. As an example, the G lamp lights under either one of two conditions: U2D and U6B are activated when the 1 \mus GATE ENABLE. FREQ, and ENABLE B+10 lines are pulled low, or U2A is activated when the 1 \mus GATE ENABLE and FREQ lines are pulled low.





NOTES

- I, REFERENCE DESIGNATIONS WITHIN THIS ASSEMBLY ARE ABBREVIATED, ADD ASSEMBLY NUMBER TO ABBREVIATION FOR COMPLETE DESCRIPTION.
- 2. UNLESS OTHERWISE INDICATED: RESISTANCE IN OHMS; CAPACITANCE IN PICOFARADS;

REFERENCE DESIGNATIONS

AIG
DSI-8 Pl Q!-!! R!-9 U!-6

TABLE OF ACTIVE COMPONENTS

REFERENCE DESIGNATIONS	HP PART NUMBERS
Q1-3	1854~0009
Q4-II	2N709 1854-0474
UI,2,5,6	2N5551
U3	182G -U274 MC1808
U4	1820-0273 MCIBO6
	1620-0310 MC862

Figure 8-13. A10 Right Readout Board Assembly

1.31

A11 LEFT READOUT OPERATION

The left readout contains DTL logic to select the proper decimal point corresponding to the front panel settings. It also contains the switch common drivers for the time base, function, and amplifier common lines (for remote programming), a storage circuit and lamp for the overflow signal, gate light, and EXT light.

The overflow signal from the divide by ten output of A8U7, (U8, Option 001) enters through pin 11 and is differentiated by C3 and R2. Q1 turns on momentarily to set flip U9A&D. During the transfer pulse, the information at U9A&D is transferred to the overflow storage flip-flop U9B&C. The overflow condition drives U9B(6) low to turn Q2 on and light overflow lamp DS1. The next reset pulse clears flip-flop U9A&D; however, U9B&C are not reset until the transfer pulse arrives. With storage off, transfer is on continuously.

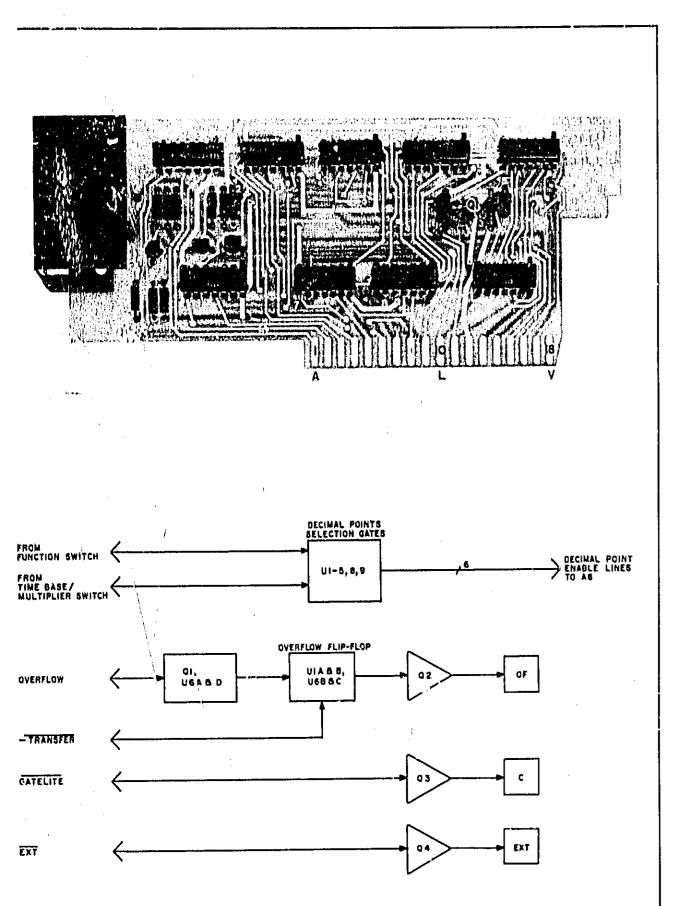
A low at pin 9 turns on Q3 to light the gate lamp, DS2, Similarly, a low at pin A lights the EXT lamp and opens the common lines for the TIME BASE, FUNCTION, and SLOPE switches. This disables these controls to allow remote programming of the unit

A11 TROUBLESHOOTING

If one of the decimal lights does not work, set the front panel switches to the positions that would normally cause the lamp to be on. Refer to the tables in Figure 3-3 for the appropriate switch settings. Check the gates whose input lines correspond to the switch settings, As an example, the D.P.5 ENABLE line goes low under either one of two conditions: U2A is activated when the PER B+10 and 1 s GATE ENABLE, and U2D is activated when the PER A OR B and 10 s GATE ENABLE lines are pulled Low.

To check the overflow circuits, set the FUNCTION switch to START and select a fast gate time. When the most significant digit on the counter's display changes from 9 to 0, both flip-flops in the overflow circuit should set. An an initial test, check U5 for a High on pin 5. The second flip-flop (U9B & U9C) should have a Low on pin 6 and a High on pin 8.

In a frequency or period average measurement, the <u>-TRANSFER</u> line pulses Low, rather than being held Low as in START. If the OF light does not turn off at the end of the display time, check that the <u>-RESET</u> pulse clears flip-flop U9A and D.



Model 5326/27C Circuit Diagrams

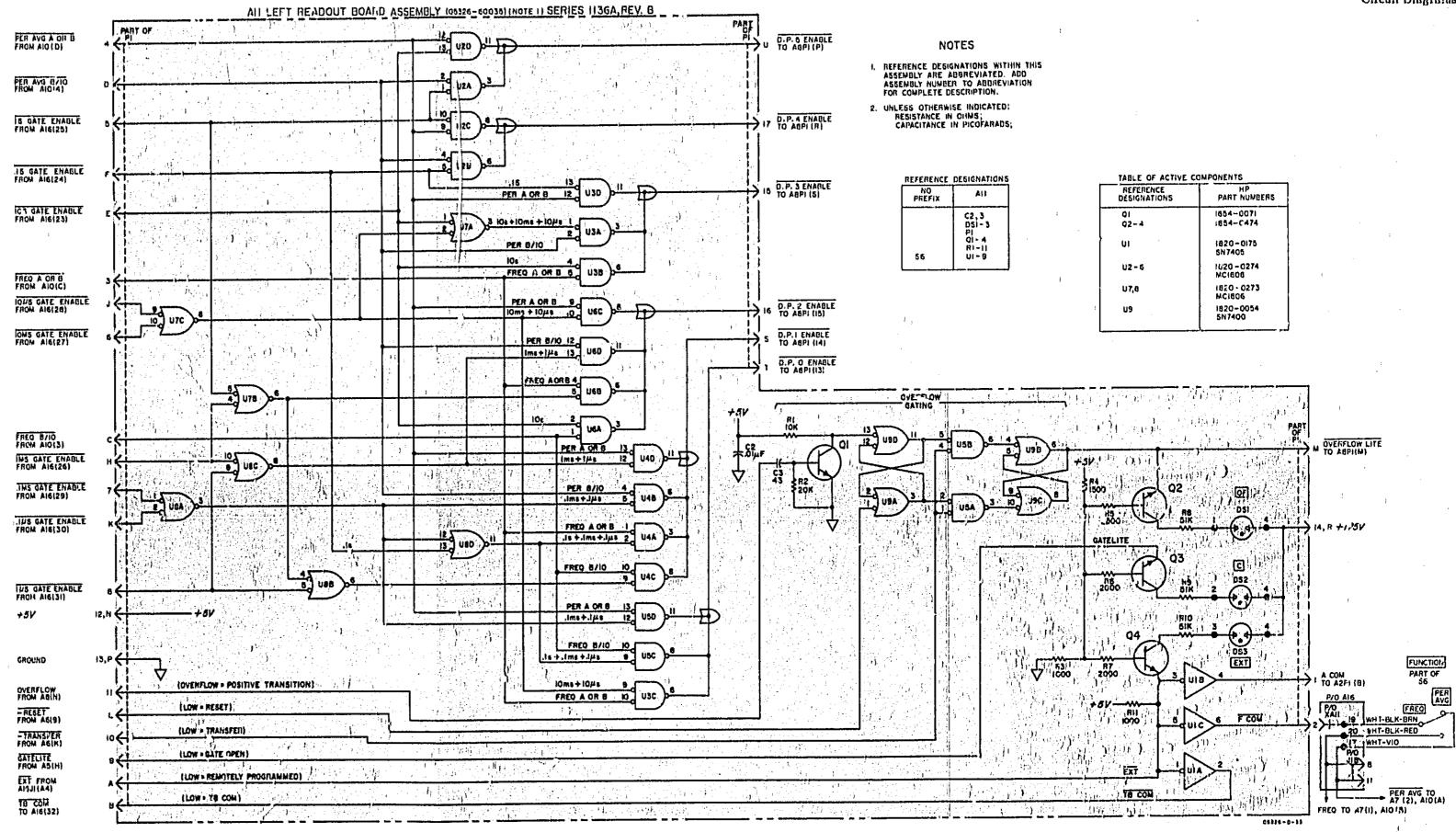


Figure 8-14. All Left Readout Board Assembly

8-33

A15-A16 POWER SUPPLY OPERATION

The power supply provides +175, +16.5 V and +5 V. Transformer TI has a 115/220 primary and secondaries with open circuit voltages of 181 V at the red leads, 21 V at the orange, and 18.6 V between the green leads, with the winding center tapped to ground.

A15 CR6-9 comprise a full-wave bridge whose output is fed to filter C3 and bleeder R3. Q5 is a series pass regulator. A constant reference voltage is developed across CR11 and CR12 through resistor R1. When the output voltage at XA15(1, A) decreases, Q5 increases conduction to increase the output voltage. Q8 is a current limiter that senses the voltage drop across R6. Output current above approximately 60 mA turns on Q8 and shunts base current from Q5, tending to turn Q5 off and limit the current. C1 adds oscillation stability to the regulator.

For the +16.5 V supplies, the orange leads of T1 connect to half wave rectifier CR4 and filter C4. Q1 is a series pass regulator and Q9 performs the same function as CR11&CR12 in the 175 V supply except that R10 provides a means to adjust the output. Assume that a Q1 base current is flowing through R2 and Q6. The resulting Q1 collector current establishes a voltage at the output, which is divided across R9, R10, and R11. If the voltage at the wiper of R10 is greater than that across CR9, Q9 will be turned on, shunting base current from Q1. This will tend to turn off Q1 and lower the regulated voltage. Thus, varying R10 establishes the largest output voltage that can exist before Q9 turns on to cut back Q1. Resistors R17, R18, and diodes CR15-18 provide current limit action at 180 mA similar to the +175 V supply.

Q6 is a preregulator that gives the circuit better line regulation and lower ripple than the Zener diodes of the 175 V supply. With CR1 as a reference, Q6 is a constant current circuit that maintains a Q1 base current independent of variations of the input (line voltage changes and ripple). R4 is needed to establish the current through CR1. The -16.5 V supply is complementary. The 5 V supplies are also complementary and only the + will be discussed.

The output from the TI green leads is fed through full wave rectifier CR10 and CR11 into filter C1. It then passes through overload current limiter R1 and into the series pass regulator Q1, to the 5 V output at Q1C. Q3 is a driver for Q1 and has approximately 5.75 V on its base, developed across CR6 and CR5 by the current from the 16.5 V supply through R7. If the voltage at the emitter 5.1 V, Q3 is turned on providing base current to turn on Q1, raising the output voltage, Q3 turns off when its emitter gets above 5.1 V. C2 is the output filter to maintain a low output impedance of high frequencies.

CR2 clamps the output at 6 V to provide protection for the IC's in case the 16.5 V or 175 V line should momentarily obort to the 5 V line, CR5 provides thermal compensation for Q3.

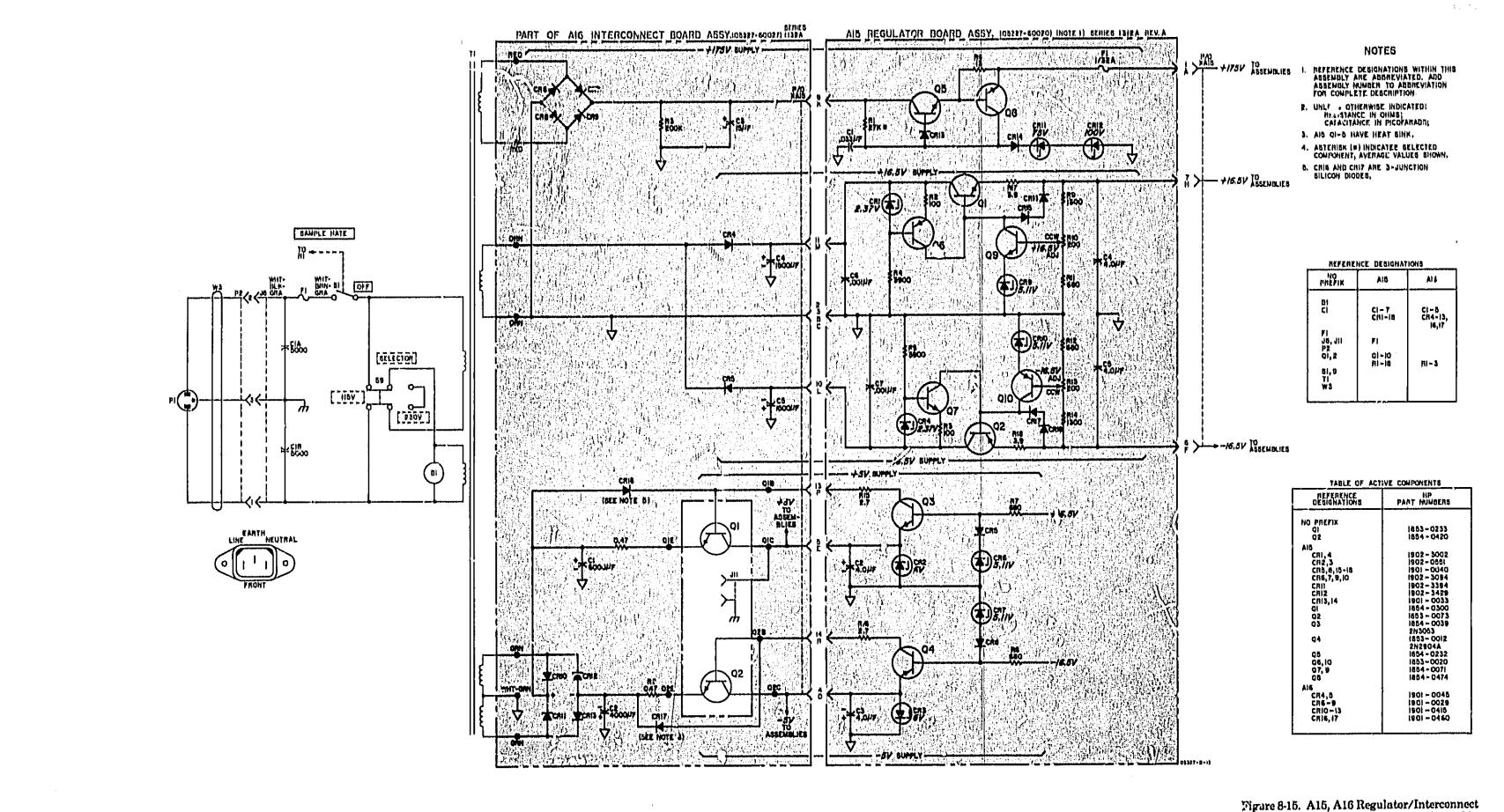
Note that the 16.5 V supply is needed for operation of the 5 V supply. If the + or + 16.5 V supply fails, the corresponding 5 V supply will be inoperative.

Figure 8-14
A11 LEFT READOUT BOARD ASSEMBLY
(See Page 8-33)

Model 5326/27C Circuit Diagrams

Part of Figure 8-15. A15, A16 Regulator/Interconnect Board Assembly REGULATOR REFERENCES CRB HALF WAYE RECTIFIER FULL WAVE RECTIFIERS REGULATORS MORE DATA UNDER FOLD

8.34



Board Assembly

A18 PRESCALER OPERATION

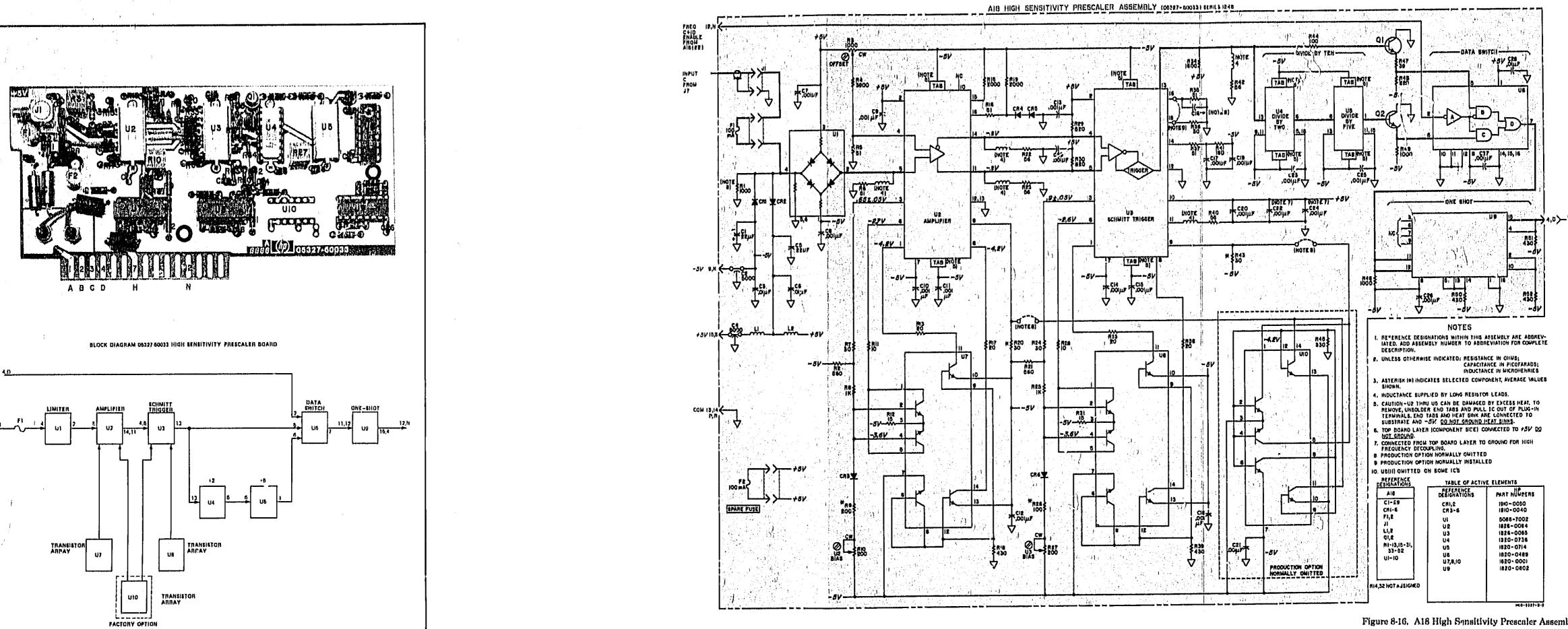
The presenter board serves as a direct amplifier-trigger or as a divideby-ten amplifier with the function controlled by the front-panel INPUT SELECTOR switch. With the switch in the B+10 position, the circuit performs as follows:

The signal is fed into the 500 input of J1. CR1, CR2, and U1 provide protection above 3.5 V rms or 5 V peak. There is about 2 dB loss through U1. The signal is passed to U2 amplifier, which is biased for sensitivity by R3. U3 amplifies the differential input and shapes the signal into a square wave. U4 and U5 combine to divide the signal by ten and Q2 translates the signal from EECL to ECL levels before presenting it to the data switch.

The direct signal, also from U3(13), bypasses the divider network and is sent to the data switch through the level translator Q1. The setting of the INPUT SELECTOR switch determines whether the data switch will accept the direct or prescaled signal. Pin 2 of U6 is High for direct and Low for prescaled. U9 shapes the positive, square-shaped pulses into narrow spikes before sending the signal to A7 Function board. U7, U8, and U10 (a production option) are constant-current sources for the amplifier circuits.

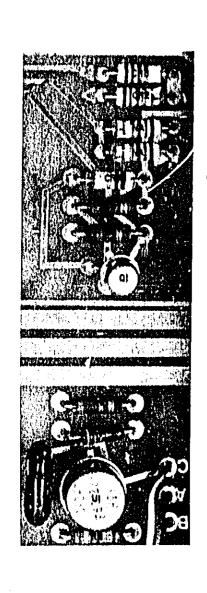
A18 TROUBLESHOOTING

Before troubleshooting the circuits, check the input protection fuse. If problem is in direct mode only, check Q1 and U6. If problem is in prescale mode only, check U4, U5, and U6, and Q2. If a problem is found in the amplifiers (U2 and U3), remove the input signal and check the dc voltages supplied by the constant-current sources U7 and U8.



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Figure 8-16. A18 High Sansitivity Prescaler Assembly



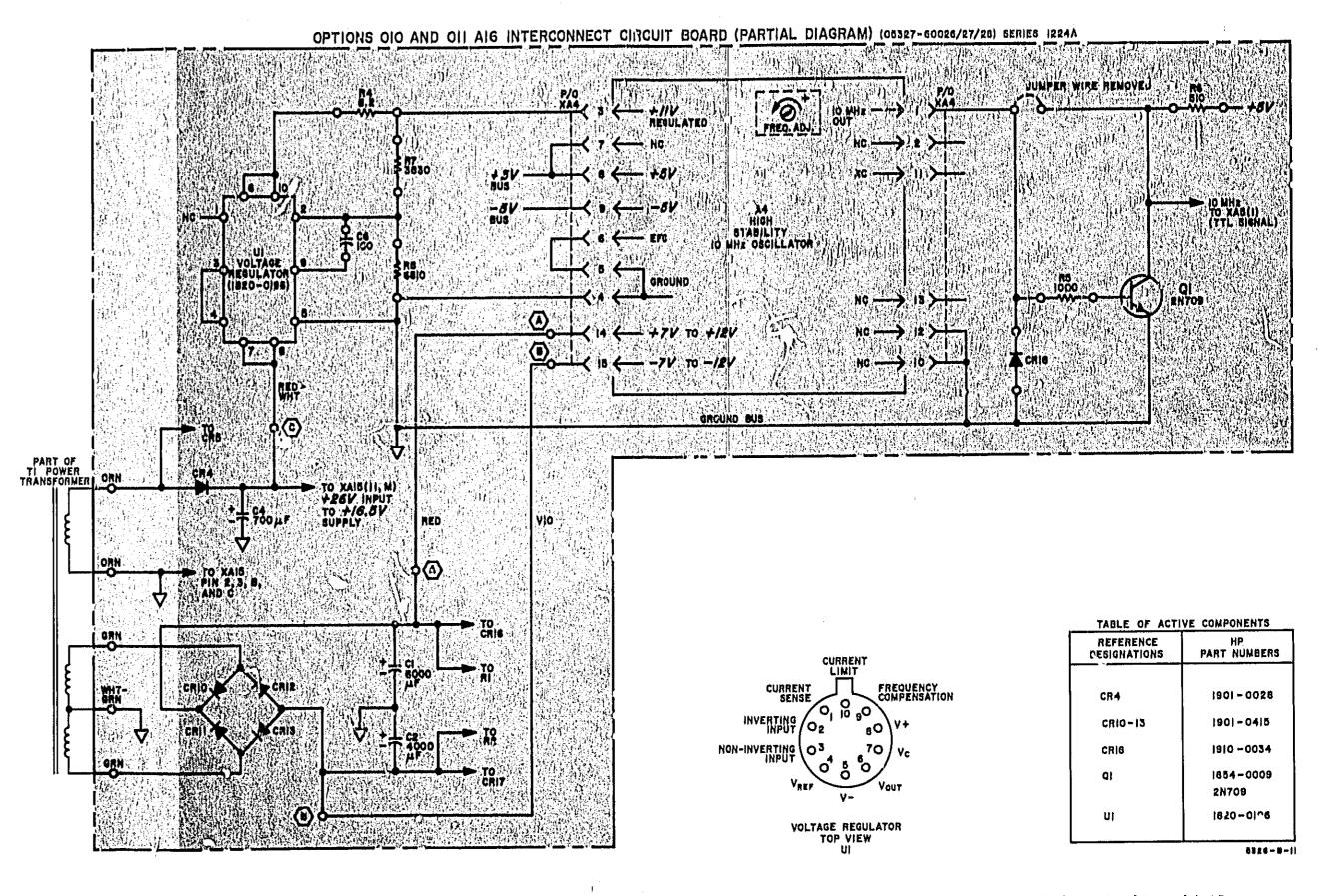
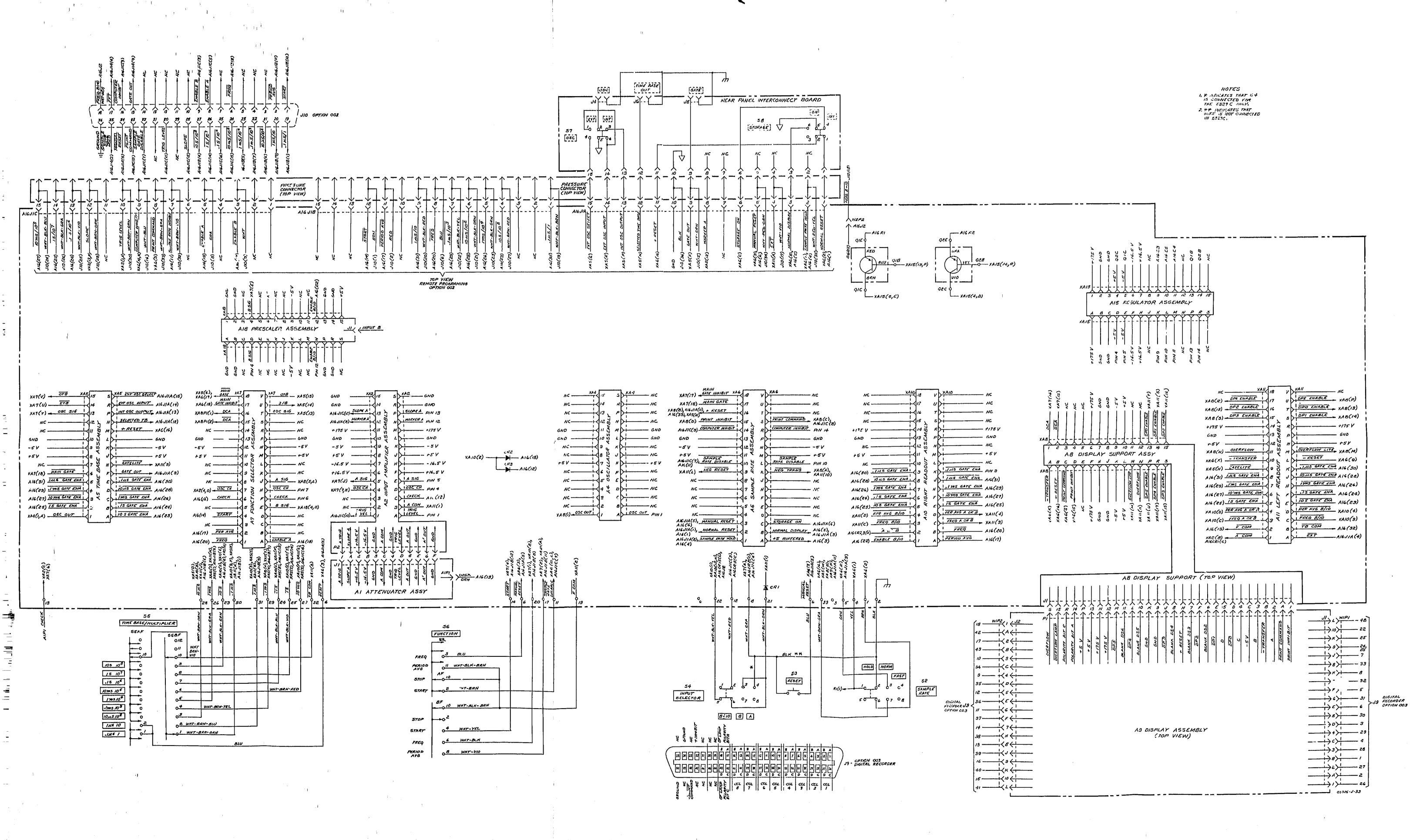


Figure 8-17. Options 010 and 011 and A16 Interconnect Circuit Board



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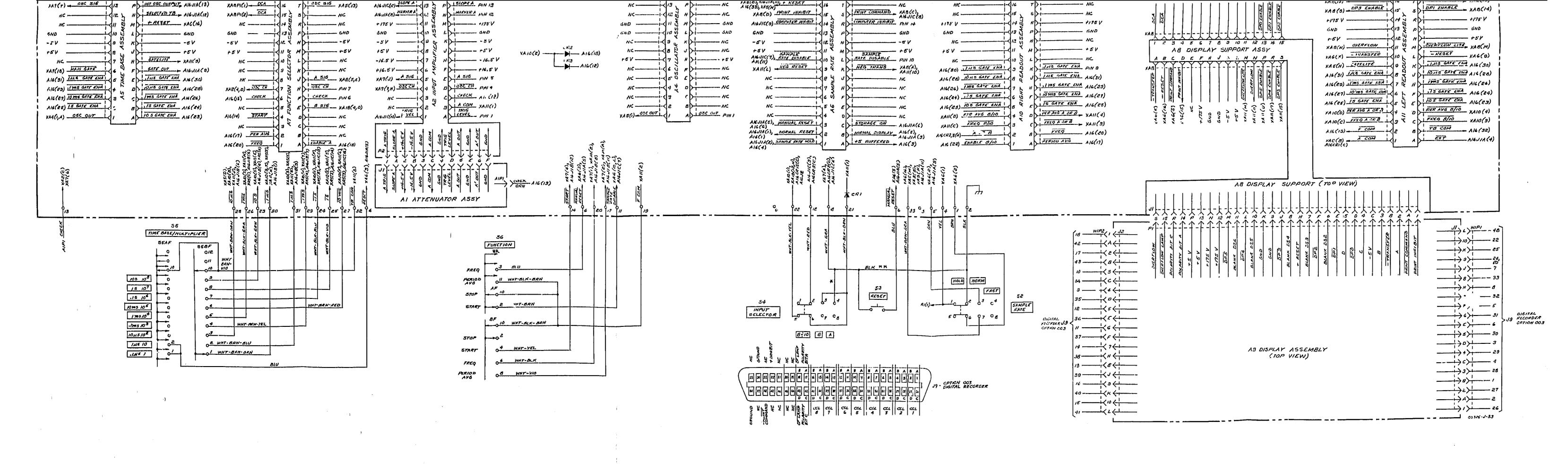


Figure 8-18 5326C/5327C INTERCONNECT DIAGRAM (Stock No. 05326-90022)

MANUAL CHANGES

MANUAL DESCRIPTION =

INSTRUMENT: B326C/B327C Freq. Counters

SERIAL PREFIX: 5326C-1312A

5327C-1312A

DATE PRINTED:

JUNE 1074

HP PART NO: 05326-80038 MICROFICHE NO: 05326-80039 (This change supersedes all earlier dated changes)

• Make all changes listed as ERRATA.

CHANGE DATE October B. 1976

 Check the following table for your instrument's serial prefix or serial number and mails listed change(s) to manual,

IF YOUR INSTRUMENT HAG BERIAL PREFIX OR BERIAL NUMBER	TO YOUR MANUAL MAKE THE	if your instrument has berial prefix or berial number	Make the Following Changes To Your Manual
1420A (5326C/27C)	1	1544A (5327C only)	1, 2, 3
1544A (5326C only)	1, 3	1604A (5327G only)	1, 2, 3, 4
NEW OR REVISED IT	======================================	► 1020A (5326C/27C)	1, 2, 3, 4, 5

NEW OR REVISED ITEN ERRATA

Page 8-11, Table 6-1;

Add A15XF1 1400-0110; 1; FUSEHOLDER: 2-PIN; 28480; 1400-011C. Change "Description" for A16C4 and A16C5 from 700 to 1500 UF.

Page 6-13, Table 6-1:

Change A18U3 from 1826-0085 to 1826-0085 or 1826-0151 in HP and Mfr. Part Number columns. Add "FACTORY SELECT" after description for A18R34 and A18R42.

Page 6-14, Table 6-1:

Change 1200-0147 to 1200-0081 in "HP Part Number" and "Mfr Part Number" columns and "Qty" to 2.

Page 6-17, Table 6-2:

Add 00779 AMP, INC P.O. BOX 3608, Harrisburg, Pa. 17105.

Page B-34

Replace A15 component locator photo with attached Figure 1.

Pege 8-37, Figure 8-16, Schematic Diagram:

Add asterisk (*) by A1BR34 and A1BR42.

In TABLE OF ACTIVE ELEMENTS show 1826-0085 or 1826-0151 for A18U3 and 1820-0736 or 1820-0558 for A18U4.

Page 5-10, Table 5-4, Steps 5c and 5d:

Change 0.65V to 0.9V at A18U2(3),

Change 0.90V to 0.8V at A18U3(3).

Page 8-37, Flyure 8-16, A18 schematic:

Change 0,66V at A1BU2(3) to 0,9V,

Change 0,90V at A18U3(3) to 0,8V.

Page U-11, Table G-1:

Change description for A16C4 and A16C5 frum 700 to 1500 µF,

Page 7-21, Paragraph 7-24:

Add following sentence: "An HP Part No. 05326-00033 adapter plate will also be required for mounting 36-pin remote programming connector J10."

MANUAL CHANGES MODEL 5325C/5327C PAGE 2

ERRATA (Cont'd)

Pages 5.4 and 5.5, 4. PULSE OPLIATION:

In step a, set LEVEL (A) to "SLIGHTLY +" in place of PRESET.

Change step a to read "Adjust pulse generator for positive output for 10 MHz repetition rate, 15 ms pulse . width for 0,3 volts peak to peak indication on oscilloscope,"

Change step d to read "Adjust counter LEVEL A control until counter triggers and counts. Check that counter displays the repetition rate, count light flashes, and trigger A lamp is ON. Record on test card,"

CHANGE 1 (1420A)

Page 6-11, Table 6-1:

Change A15F1 from 2110-0460 to 2110-0487 1/20 Anno.

Change A15R6 from 0686-1305 to 0688-5470 B.2 OHM; 01121; EB8265.

Add A16XF1 1261-3206; 2; SOCKET, MINIATURE SINGLE PIN: 00770; 2-331272-7.

With these changes A15 assembly 05327-60020 is "SERIES 1428A REV. D" Component locator In attached Figure 1 is applicable for A15 with SERIES 1312A or 1420A.

Page 6-12, Table 6-1:

Add A18L3; 05303-80001, 1, COIL, FXD, RF PEAKING, 28480, 05303-80001. Add "FACTORY SELECTED" to A18R34 and A18R42 "Description".

With those changes A1B assembly 05327-60033 is "SERIES 142BA".

Page 8-35, Figure 0-15, A15 schematic diagrams

Chai A15F1 value from 1/32A to 1/20A.

Change A15R6 value from 13 to 8,2 OHM.

Change "SERIES 1312A REV. A" at top of A15 screenavic to "SERIES 1428A REV. D".

Page 8-37 Figure 8-16, A18 schematic diagrams

Add AIRL3 in series with AIBU3(13) output line. Output circuit trace is cut and one end of AIBL3 is connected to the junction of AIBU3(13) and AIBR34. The other and of AIBL3 is connected to the junction of ABR44 and the coll from A18R42,

Change series number at top of A18 schematic diagram to "SERIES 1428A".

Page 6-12, Table 6-1, A16 Connector Board Assyt

Change A1687 from 0698-3163 (3830 OHM) to 0698-3165, 4630 ohms. HP and Mfr. Part Number are the ----, Change SERIES to 1428.

Page 8-39, 1 B-17, Schematic:

Change A.dR7 from 3830 to 4630 ohms.

Change SERIES number at top of schematic from 1224A to 1428A. Make same change on various pages where portions of A16 appear in this manual,

Page 6-13, Table 6-1;

Change AIBB29 and AIBB30 from 0698-5177 (820 OHM) to 0698-5103, 430 OHM FACTORY SELECT.

Add FACTORY SELECT to A18R30 description,

Page 8-37, Figure 8-16, A18 Schematic:

Add asterisk (*) adjacent to A1BR22, A1BR23, A1BR29, A1BR30, and A1BR40. All of these resistors should have FACTORY SELECT as part of their description in Table 6-1.

CHANGE 2 (PART OF 1644A FOR 6327C ONLY)

Page 6-10, Table 5-4, Paragraph 4 Prescaler Adjustments:

Change paragraph 5-9 to read as follows: "Reduce output level until counter's display becomes unstable. Alternately adjust A18R3 and A18R63 for a stable display. Repeat procedure until unable to obtain a stable display. Increase signal level until display just becomes stable and make any final adjustments of AIBR3 and AIBR53,"

Page 6-12, Table 6-1, A18 Replaceable Parts:

Change A18 SERIES number to 1540 in "Description" column,

Add A18C30; 0160-3879; CAPACITOR-FXD 0,01 µF 20% 100 VDCW CER; 28480; 0160-3879, Change A18R22 and A18R23 from 0608-4131 (56 ohms) to 0698-3111; RESISTOR-FXD 30 OHM 5% ,125W CC; 01121; BB3005.

MANUAL CHANGES MODEL 5326C/5327C PAIJE 3

CHANGE 2 (PART OF 1544A FOR 5327C ONLY) (Cont'd)

Page 6-12, Table 6-1, A1B Replaceable Parts: (Cont'd)

Change A18R28 and A18R30 from 0698-5103 (430 ohms) to 0698-5177; RESISTOR-FXD 820 OHM 5% .125W CC; 01121; BB8216.

Change A18R34 from 0608-8073 (1600 ahms) to 0608-5178; RE6|STOR-FXD 1500 OHMS 5% .125W CC; 01121; BB1525.

Add A18R53; 2100-2633; RESISTOR VAR 1000 OHMS 10% COMP SIDE ADJ; 30283; EX50X102. Add A18R54 and A18R55; 0698-3374; RESISTOR-FXD 20 OHM 5% .125W CC; 01121; BB2005, Add A18R55; 0698-6283; RESISTOR-FXD 10 OHMS 5%; .125W CC; 01121; BB1005.

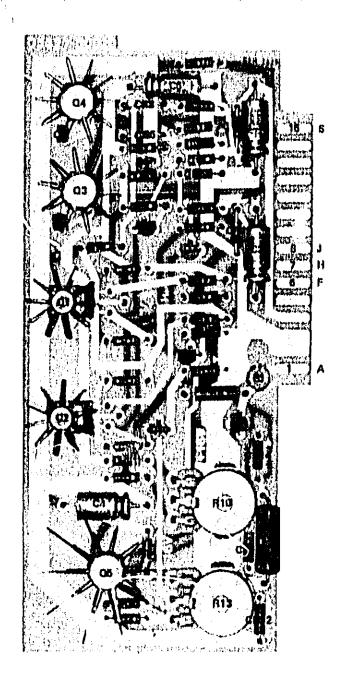


Figure 1. A15 Regulator Board Assembly Component Locator 05327-60020 Series 1312A or 1428A

MANUAL CHANGES MODEL \$3280/6327C PAGE 4

CHANGE 2 (PART OF 1844A FOR 5027C ONLY): (Cont'd)

Page 8-37, Figure 8-16, A18 Prosecuer Assembly Schematics

Add A18C30 (.01 µF) between common and the "45V" and of A18R34,

Add A18R63 (1000 ohms) variable resistor between 16,2V and -6,2V. Connect junction of A18C30 and A18R34 to arm of A18R63 in place of "16V" as shown on schematic diagram.

Change value of A18R34 from 1600 to 1500 class.

Change A1BR22 and A1BR23 from 56 to 30 ohms and remove asterisk (*) adjacent to each resistor,

Change A1BR29 and A1BR30 from 430 to 820 ohms.

Add A18R54 (20 ahms) in series between A18U2 pin 11 and the junction of A18R23, A18R30, and A18U3 pin 5.

Add A18R55 (20 phms) in series between A18U2 pin 14 and the junction of A1BR22, A1BR29, and A18U3 pin 4.

Add asterisk (*) adjacent to A18R64 and A18R66.

Add A18R56 (10 ohms) in series between A18U1 pin 2 and the junction of A18U2 pin 5 and A18R6.

Change "SERIES" number at top of adiamatic from 1428 to 1540,

CHANGE 3 (1544A FOR 5326C) (PA UF 1544A FOR 5327C)

Page 1-3, Table 1-3, Specifications for OPTIONS:

Add to Option 001: B-digit display. "Part of standard instrument; discontinued as an Option."

Page 1-4, Table 1-3, Specifications for OPTIONS:

Add to Option 003: Digital Output (for numerals and polarity only), "Discontinued as an Option and included as part of the standard instrument,"

Dalete Option 010 Temperature Compensated Oscillator. This tiption is discontinued and is no longer available.

Page 7-21, Paragraphs 7-9 and above for OPTIONS:

Paragraph 7-11, Add; "Part of Standard Instrument; Discontinued as an Option,"

Paragraph 7-22, Add: "Part of Standard Instrument: Jiscontinued as an Option."

Paragraph 7-17, Add: "Discontinued as an Option and is no longer available,"

Page 6-8, Table 6-1, A9 Replaceable Parts:

Replace A9 table for 05326-60008 with table for 05326-60025 A9 on page 6-15 of Table 6-1.

Page 6-16, Table 6-1:

Ald parts for Option 003 as part of standard instrument.

The 6326C/5327C Frequency Counters are furnished to., the RACK MOUNTING KIT described in this manual. If ordered at the same time as the instrument, the RACK MOUNTING KIT described in the manual is available to Option 908 at additional cost, if not ordered with an instrument, the RACK MOUNTING KIT is available under HP Part No. 05326-60046. Disregard any number references stating the instrument is supplied with a rack mounting kit.

Page 6-14, Table 6-1, Chassis Replaceable Parts:

Change XF1 fuseholder from 1400-0084 to the following recommended replacement for all instruments. Add the following on Page 6-14 under CHASSIS PARTS:

XF1; 2110-0465; FUSEHOLDER BAYONET CAP; 75915; 345003-020

XF1; 2110-0470; FUSEHOLDER BODY UL/IEC: 75915; 345003-010

XF1; 2950-0054; NUT FUSEHOLDER MTG 1/2-28; 28480; 2950-0054

CHANGE 4 (1604A for 5327C)

Pages 6-12 and 6-13, Table 6-1, A18 (05327-60033) Replaceable Parts:

Change A18 from SERIES 1540 to SERIES 1604.

Add A1BR57; 0683-3113; R: FXD COMP 100 OHM 5% 1/8W; 01121; BB1015.

Page 8-37, Figure 8-16, A18 Schematic Diagram:

Change SERIES 1540 at top of schematic to SERIES 1604.

Add 100 ohm resistor R57 in series with +6V input to pin 10 of SCHMITT TRIGGER A18U3.

MANUAL CHANGES MODEL 53250/5327C PAGE 5

CHANGE 5 (1020A)

Page 6-6, Table 6-1, A6 (05326-60013). Replacement Parts: Change A6 series number to 1620.

Change AGCB from 0160-0163 (.001 µF) to 0160-0288; CAPACITOR, FXD, 1800 PF 10% 200WVDC POLYE; 50288; 202P12292.

Add A6G13; 0180-1736; CAPACITOR-FXD ,22 μ F 10% 35WVDC TANT; 56289; 150D224 X 8036A2.

Page 8-21, Figure 8-9, A6 (05326-60013) Schematic Diagram: Change series number, at top of diagram, from "1224A REV C" to "1620."

Change A6C8 from 1000 to 1800 pF.

Add A6C13 capraitor (.22 μ F) between circuit board common and junction of A6R12, A6U4B(5), A6CR2 and A6CR3. The positive side of the capacitor goes to the SAMPLE RATE DISABLE line from connector pins 10L and the negative side to circuit board common. Add A6C13 in REFERENCE DESIGNATIONS tables.

راج HP MANUAL CHANGES

MAKE ALL CORRECTIONS IN YOUR MANUAL ACCORDING TO ERRATA.

5326C/27C MANUAL TITLE:

MANUAL PRINTED: June, 1974 Check the following table for your instrument secial prefix and make any Indicated changes to the manual:

MANUAL PART NO: 05326-90038

CHANGE DATE: 23rd December, 1976.

SERIAL PREFIX	MAKE C	HANGE	SERIAL PREFIX	MAKE CHANGE	Serial Prefix	MAKE CHANGE
14430	1		15440	1-4		
14430-00230 (26C) 1-	-2				
14430-00192 (270) 1-	-2				
15190] -	-3				

ERRATA

Add

Page 6-11, Table 6-1

: AlbXF1 FUSEHOLDER 2 PIN part no. 1400-0110

Change : A16C4 and A16C5 to C FXD 1500uF part no. 0180-2382

Page 6-13, Table 6-1

: AlBU3 part no. 1826-0085 to "1826-0151 or 1826-0085" Change Add : "FACTORY SELECT" after description for A18R34 and A18R42

Page 6-14, Table 6-1

Change : BUSHING TRANSISTOR part no. 1200-0147 to part no. 1200-0081

Page 8-34

Delete : Alb Component Location and insert "Fig 1" new component Location provided

Page 8-37, Fig 8-16, A18 Schematic: "Asterisk" (*) to A18R34 and A18R42

: 0.65V at Alàu2 (3) to 0.9V Change : 0.9V at A18U3 (3) to 0.8V Change

Add : In TABLE OF ACTIVE ELEMENTS show 1826-0085 or 1826-0151 for A18U3 and

1820-0736 or 1820-0558 for A18U4,

Page 7-21, Paragraph 7-24

: following sentence: "An HP Part No. 05326-00033 adapter plate will Add

also be required for mounting 36-pin remote programming connector J10".

CHANGE 1 Table 6-1, Page 6-11

Change : A15R6 to part no. 0698-5479 R FXD 8,20 5% AlbF1 to part no. 2110-0487 1/20 Amp. Add : AlbXF1 1251-3205 Socket : Miniature

With these changes assy 05327-60020 is Series 1443U REV D

This change supersedes applicable data in Errata.

Page 8-35, Figure 8-15, Al6 Schematic

Change: Al5Fl value from 1/32A to 1/20A

CHANGE 2 Table 6-1

Change : Rear Panel part no. 05326-00032 to part no. 05326-60049

CHANGE 3 Page 6-13, Table 6-1

: AlBR29, 30 to part no. 0698-5103 R FXD 4300 5% AW Change

: Asterisk (*) to AlBR22, 23, 29, 30, 40 to indicate select on test, Add



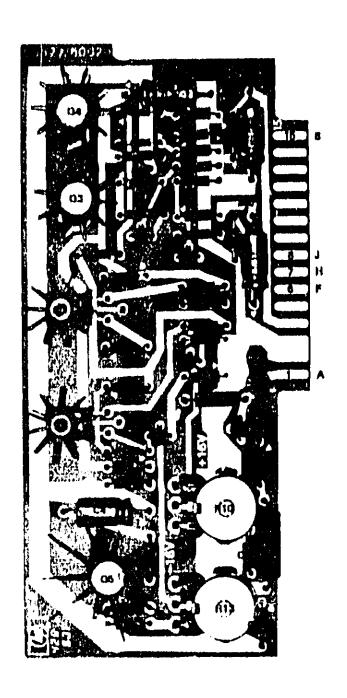


Fig. 1 A15 Component Location.

CHANGE 4

Page 1-3, Table 1-3 Specifications for OPTIONS:

Add

t to Option 001: B-digit display. "Part of standard instrument; discontinued as an Option".

Add

Page 1-4, Table 1-3, Specifications for OPTIONS: to Option 003: Digital Output (for numerals and polarity only). "Discontinued as an Option and included as part of the standard instrument"

Delete : Option 010 Temperature Compensated Oscillator, This Option is discontinued and is no longer available.

Page 7-21, Paragraphs 7-9 and above for OPTIONS:

Paragraph 7-11 Add: "Part of Standard instrument; Discontinued as an Option" Paragraph 7-22 Add: "Part of Standard instrument; Discontinued as an Option" Paragraph 7-17 Add: "Discontinued as an Option and is no longer available"

Page 6-8, Table 6-1, A9 Replaceable Parts: Replace A9 table for U5326-60008 with table for O5326-60025 A9 on page 6-15 of Table 6-1

Page 6-16, Table 6-1:

Add : parts for Option 003 as part of standard instrument.