# 69 A bipolar transistor tester

#### Introduction

This is a circuit which will test normal transistors, i.e. npn or pnp. It has the advantage of being able to test devices while they are still connected in their original circuits. However, when such tests are made, **the circuit containing the transistor under test must not be switched on**.

## The circuit and how it works

The circuit runs from a 9 V battery such as a PP3 or six AA-type 1.5 V cells. Alkaline cells are to be preferred, as their electrolyte leakage properties are better. The circuit shown in **Figure 1** uses a single CMOS integrated circuit type 4001 or 4011. CMOS circuits require special handling precautions which are described in the project *Christmas Tree LEDs*, elsewhere in this book.

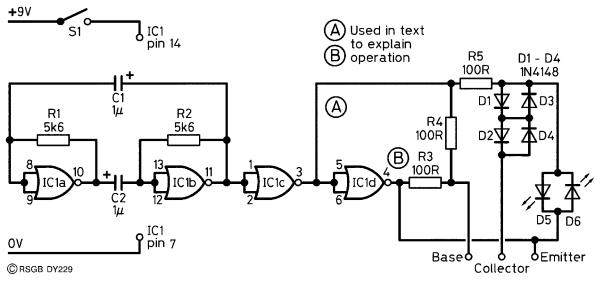


Figure 1 Transistor tester, circuit diagram

Inside the IC are four logic gates (see *Digital Logic Circuits*) which are all connected as inverters, which means that the output signal is always the logical 'opposite' of the input. The first two gates are connected as an oscillator; the circuit being the same as that used in *An Electronic Die*.

The output of the oscillator, at pin 11, is connected to the input of a buffer stage, IC1c, which helps to isolate the oscillator from the circuit that follows it. The buffer output appears on pin 3, which we shall label as *test point* A for future use. Another inverter, IC1d, follows this, its output at pin 4 being labelled *test point* B.

There are two LEDs connected back to back at the circuit output. These are D5 and D6, D5 being red and D6 being green. Whatever the output of the oscillator at any instant, one of the LEDs must be lit and the other unlit. With point A positive and point B zero, the red LED is lit, when A is zero and B is positive, the green LED is lit. Because the oscillator output is repeatedly switching from one polarity to the other, the lit LED is alternately red and green. They switch between the two colours much faster than we can see, so what we think we see are both LEDs lit together.

The two 100 ohm resistors, R3 and R4, provide the bias to the base of the transistor under test. When A is positive and B is zero, the base-emitter junction of the transistor will be forward-biased, and the transistor will switch on (if it is a working npn type). When the transistor is on, it effectively short-circuits D5 (the red LED) and it extinguishes. When point A is zero and B is positive, an npn transistor will be switched off and the

green LED (D6) will light. Thus, for a working npn transistor, only the green LED is lit.

If the transistor under test is a pnp type, it will switch on when A is zero and B positive, thus short-circuiting the red LED (D6). When A is positive and B zero, the transistor is off and the red LED (D5) is lit. Thus for a working pnp transistor, only the red LED is lit.

To summarise, the states of the LEDs indicate the following conditions:

- Both LEDs apparently lit: no transistor connected, or transistor permanently open circuit.
- Neither LED lit: a collector-emitter short-circuit is almost certain.
- Red LED alone: pnp transistor in working order.
- Green LED alone: npn transistor in working order.

#### Construction

The prototype tester was built on a piece of Veroboard measuring 15 strips by 18 holes. Cut the tracks using a track-cutting tool or a 3 mm ( $\frac{1}{8}$  inch) twist drill, as shown in Figure 2. Notice that, in this diagram, there is no

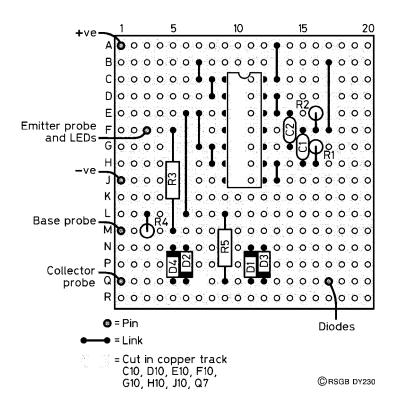


Figure 2 Transistor tester, component layout

track 'I', so try to avoid miscounting when you translate diagram positions to real positions on the board. Hold the board up to a strong light to ensure that there is no copper swarf shorting adjacent tracks together, and that you have made the cuts in the correct places.

Having done this, insert and solder Veropins for all the connections to be made to components not on the board itself. Then solder in the IC holder and the wire links. Then insert the other components in the order resistors, capacitors and diodes D1–D4. Some resistors are mounted vertically so that their connections are on adjacent tracks. Double check the diode polarities – it is easy to make a mistake when wiring diodes in anti-parallel! Next, connect up the off–board components, again making sure that the LEDs have the correct polarity. The probe leads for the emitter, base and collector should be made from different colours of wire and terminated in probe clips (small insulated crocodile clips).

Check carefully for dry joints and errant blobs of solder. Plug the IC into its holder, ensuring that it is inserted the right way round, as shown in Figure 2.

#### Testing

Without a transistor in circuit, and the battery connected, both LEDs should be lit. Connect a known good npn transistor and verify that the green LED lights. Now simulate two transistor faults: disconnect the base lead and both LEDs should light; remove the transistor and connect the emitter and collector leads together. Neither LED should light.

Repeat the tests with a known good pnp transistor. The results should be the same, except that the correct indication should now be a lit red LED. On your computer, make a small label of the bulleted list above, to fit on your tester showing the states of the LEDs and what they mean. It will act as a useful *aide mémoire* when you use the tester in future.

### Using

The circuit will test transistors in isolation or in an existing circuit, i.e. prior to use. You can check the lead identifications in component catalogues such as the Maplin catalogue. The tester is ideal for going through the large bags of unmarked transistors that you can buy for a song at rallies. You can sort them into three piles – npn, pnp and dud!

# Parts list

Resistors, all 0.25	watt, 10% tolerance or better
R1, R2	5600 ohms ( $\Omega$ )
R1, R2 R3, R4, R5	100 ohms ( $\Omega$ )
Capacitors	
C1, C2	1 microfarad ( $\mu$ F) electrolytic, 16 V WKG
Semiconductors	
D1-D4	1N4148 general-purpose silicon diodes
D5	Red LED
D6	Green LED
Integrated circuit	
IČ1	CMOS 4001 or 4011
Additional items	
Veroboard, 15	strips by 18 holes
Veropins	
	d connector (or $6 \times AA$ cells in PP3 clip holder)
Switch, SPST	
Connecting wir	e
One each of kn purposes.	own working npn and pnp transistors for test
Source	

#### Source

Components are available from Maplin.