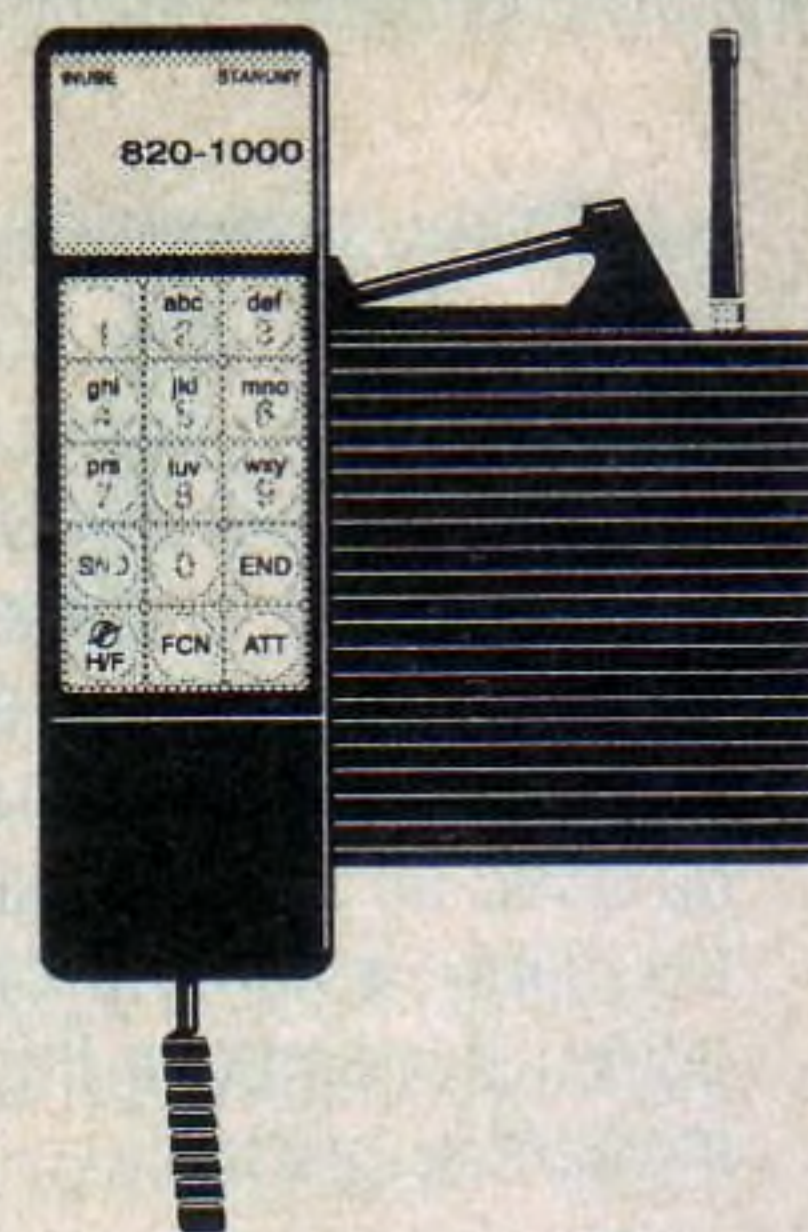


# AMATEUR RADIO

BY DARREN YATES



## Simple 2-transistor CW filter

If you're having trouble picking CW signals out of the mud, then try this handy little circuit. It's a 2-transistor CW filter & it's just the shot for beefing up those buried signals.

Deciphering CW signals on a noisy band can often be quite difficult, particularly for novice operators with only basic equipment. Fortunately, there are various techniques that can be used to "clean-up" the signal.

By far the most common technique is to employ a CW filter. A CW (or continuous wave) transmission, as used for Morse code, is essentially an interrupted carrier; ie, the carrier is switched on and off by the Morse key. When you tune your receiver, you tune it close enough to the incoming carrier to give an audible beat.

Most people tune their receiver to give a beat somewhere below 1kHz. This is particularly the case where you have a noisy signal; a lower audible frequency can be somewhat easier to distinguish from the noise and this is where this CW filter comes in. It is essentially a notch filter set at 750Hz which effectively attenuates noise either side of this frequency and thus greatly improves signal reception.

In use, you tune the receiver so that the audible beat is right on 750Hz, at which point the modulated carrier will stand out well above the noise.

The circuit itself uses just two transistors and a handful of passive components. It is built on a PC board measuring just 46 x 36mm, which should be small enough to fit inside most receivers. The power supply requirements are a 9-12V DC at just a few milliamps and this can easily be derived from an existing supply rail.

Fig.1 shows the frequency response of the filter, as measured on an Audio Precision audio test set. As can be seen, it effectively provides around 40dB of gain at 750Hz.

### Circuit details

Refer now to Fig.2 for the circuit details of the CW Filter. It is basically a 2-transistor amplifier with a twin-T filter in the feedback path.

In greater detail, transistors Q1 and Q2 make up a DC feedback pair, with negative feedback applied from Q2's collector to Q1's emitter via the twin-T filter network. The input signal is applied via a 0.1 $\mu$ F capacitor to Q1's base, while the 120k $\Omega$  and 150k $\Omega$  resistors set the bias for this stage.

The 330 $\Omega$  resistor and series 1 $\mu$ F capacitor roll off the low-frequency response of this first stage, to make the overall filtering more effective. The resulting amplified output on Q1's collector is then fed directly into base of PNP transistor Q2. Note the 33k $\Omega$  load resistor on Q1's collector. This is not strictly necessary but has been included since it significantly reduces distortion.

Finally, the amplified signal on Q2's collector is coupled to the output via a 10 $\mu$ F capacitor.

### Twin-T filter

Six components are used in the twin-T filter network: two 2.2k $\Omega$  resistors, a 1.1k $\Omega$  resistor, two 0.1 $\mu$ F capacitors and a 0.22 $\mu$ F capacitor. Its

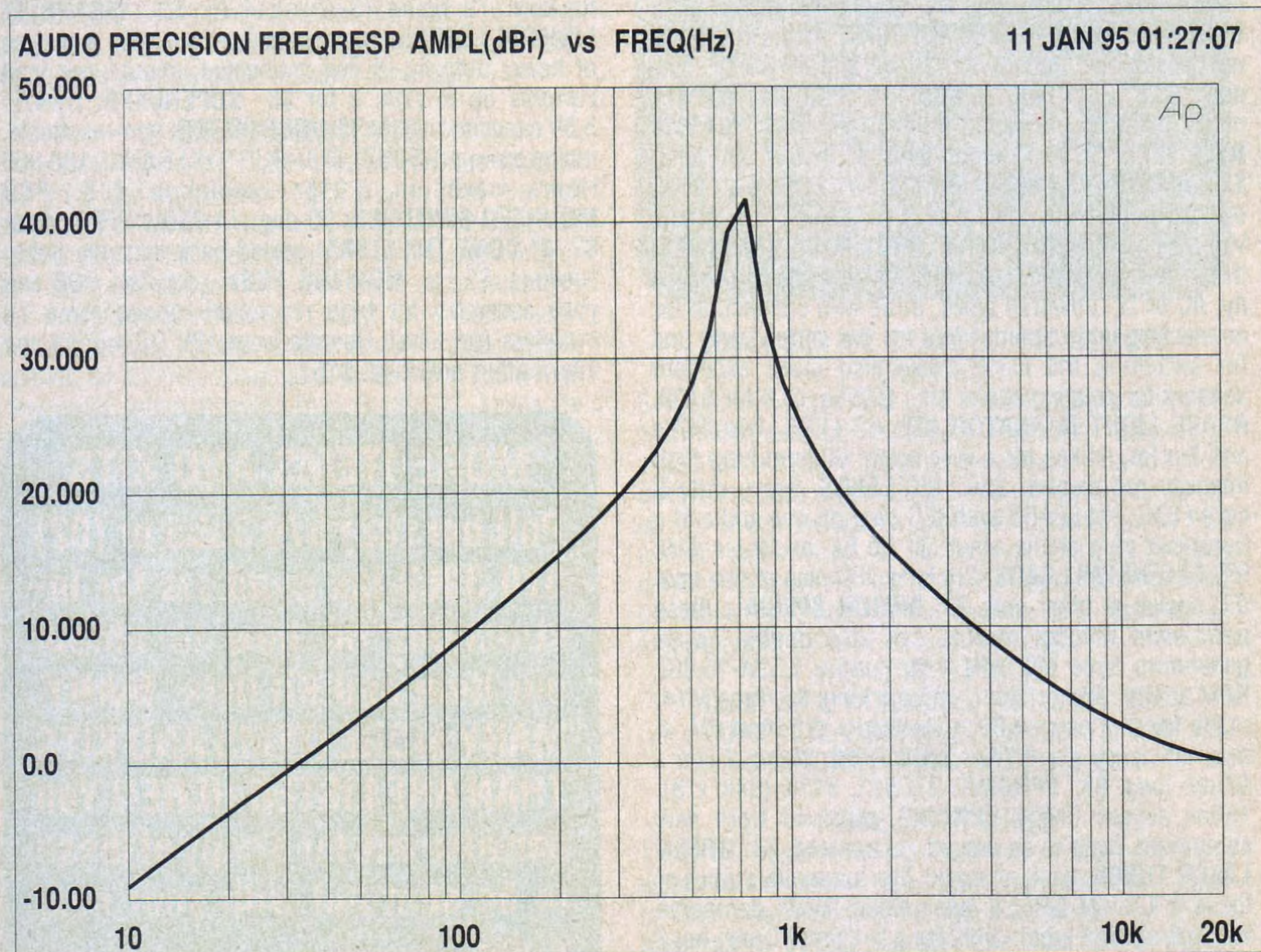


Fig.1: the frequency response of the filter. The passband is centred on 750Hz.



