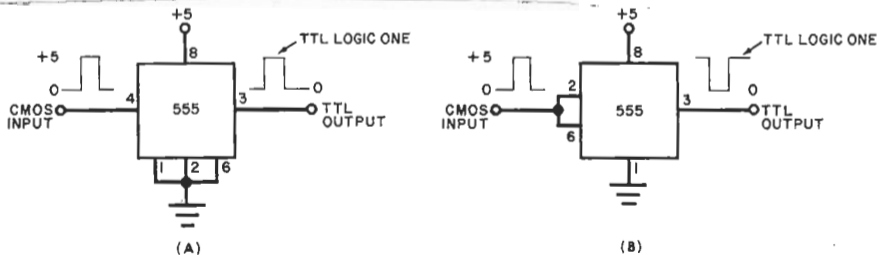


Ted Reiter (1442 Brook Drive, Titusville, FL 32780) suggests that the ubiquitous 555 timer can be used effectively as a CMOS-logic-to-TTL interface device. Given in Fig. 2, his circuits illustrate the use of the 555 as a CMOS/TTL buffer, Fig. 2A, and as an inverter, Fig. 2B. Neither design requires external resistors nor capacitors, and both can be operated on standard 5-volt dc power sources. In addition to serving as a CMOS/TTL interface, the 555 can be used as an interface driver between CMOS logic and relays, etc.



*Fig. 2. Suggested CMOS-to-TTL interfaces using the 555. In a buffer circuit (A); and in an inverter circuit (B).*