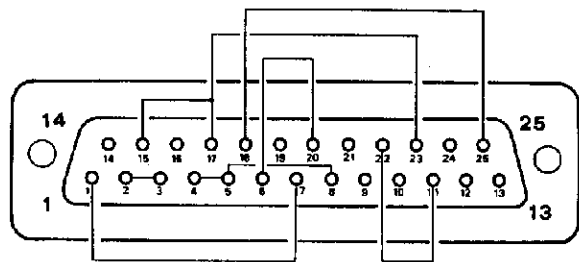


## SYSTEM TEST DIAGNOSTICS

NAME	PIN	TO	PIN	NAME	
-	1	TO	7	GROUND	
TD	2	TO	3	RD	
RTS	4	TO	5	CTS	TO PIN 8 DCD
DSR	6	TO	20	DTR	
-TCLD	11	TO	22	RI	
TC	15	TO	17	RC	TO PIN 23 -
RCLD	18	TO	25	-RCLD	



## SYSTEM TEST DIAGNOSTICS

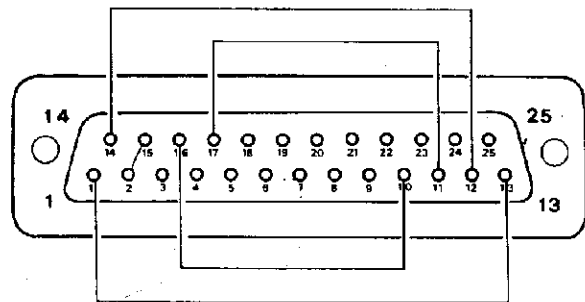


Fig. 6-1 Parallel Loopback Jumper