We are in the process of building an automated component inspection system (CIS). Last time we built a PCbased capacitance meter. Now we will combine the capacitance meter with a volt/ohmmeter on a single PC board. The result will be an accurate, low cost, comput-er-assisted test instrument that interfaces through a standard PC parallel port.

Our CIS meter measures three basic quantities: resistance, voltage, and capacitance. Resistance may vary from 0 to 20 megohms. Voltage may vary from 0 to 2 volts DC, but simple peripheral circuitry can be added to increase range. Capacitance may vary from 20 pF to $20 \mu \mathrm{~F}$.

## Multimeter basics

The preferred instrument for measuring resistance and voltage is a digital multimeter, or DMM. All DMM's are based on a simple, single-range voltmeter. The nice thing is that converting a humble DMM into a measurement powerhouse requires only a handful of resistors and some switches.
Intersil's 7106 is the basic building block of many meters. The 7106 contains an analog-todigital converter (ADC) and it provides a three-digit output capable of driving an LCD directly.
A basic 7106 voltmeter that requires only three resistors and five capacitors is shown in Fig. 1. To calibrate the circuit, you must set the reference voltage at the wiper of R2 to 1.000 volt. That setting results in an overall range of 0.0 to 2.000 volts.

## Increasing range

What if we want to measure a voltage greater than 2.000 ? To do so, we use a simple voltage divider, as shown in Fig. 2. With that simple addition, the incoming voltage is divided by the ratio of the two resistors to produce a voltage with a corresponding value. In this case, the resistance ratio is $10: 1$, so the maximum voltage increases by a factor of ten, giving a total range of $0-20$ volts.

## Measuring resistance

The 7106 compares the incoming voltage to the reference voltage between pins 35 and 36 . By
altering the circuit slightly, we can create an ohmmeter, as shown in Fig. 3. That circuit gives us another voltage divider. If the unknown resistance ( $\mathrm{R}_{\mathrm{IN}}$ ) equals $\mathrm{R}_{\text {REF }}$ the same voltage will appear across both the reference and the measurement inputs, resulting in a reading of 1.000 . As the value of $\mathrm{R}_{\mathrm{IN}}$ varies, the reading will vary accordingly. As for range, if the value of $\mathrm{R}_{\mathrm{REF}}$ were 1 K , the range would be 0 to 2 K . For a range of $200 \mathrm{~K}, \mathrm{R}_{\text {REF }}$ should have a value of 100 K .

## Computer interfacing

By now you're probably thinking, "That's all well and good, but how do you hook up a 7106 to a PC? The output lines only drive an LCD!" Actually, what may at first seem like an odd approach is both simple and practical. To see why, let's look at a standard LCD readout.

As shown in Fig. 4-a, the LCD consists of three seven-segment digits and a single leading " 1 ," making a total of 22 segments. Figure 4-b shows which segments are used to display each integer from zero to nine; Fig. 4-c shows the same values, but without the use of segments c and d . The 4-c versions aren't pretty, but each one is unique, so a computer could read the 7106 segment outputs and translate the values into a more comprehensible form. By ignoring two segments from each digit, sixteen lines are required: five for each digit plus one for the leading digit. Reducing the number of lines in that way is significant because it allows us to use a simple scheme to read each display segment into the computer and then determine what the composite reading was.

A pair of 4051 analog multiplexers does the trick. The 4051 connects one of eight inputs to a single output according to the binary value at three control inputs (A, B, and C). Using two 4051's with common control lines, we can monitor the status of sixteen inputs using only three output and two input lines from the computer. As shown in Table 1 , when A, B, and C are all low, input 0 is connected to the output; when A, B, and C are all high, input 7 is connected to the output.


In our circuit, we connect the sixteen signal inputs of the two 4051's to the segment-drive outputs of the 7106, and use the computer to control the A, B, and C control inputs. We also use the computer to monitor the pin-3 outputs of the 4051's.

## The circuit

The complete circuit is shown in Fig. 5. The circuit is slightly different from the basic circuit in that the reference and voltage inputs are routed to a three-pole, double-throw switch (S1), which selects ohms or volts. In the "volts" position, the calibrate potentiometer (R10) is connected to the reference inputs of the 7106. In the "ohms" position, the reference inputs are connected to one of several reference resistors, as selected by analog-switch IC7.
A 4030 exclusive-or gate (IC2) converts the segment-drive signals to DC levels. The 7106 provides segment drive and bp (backplane) signals, both of which are square waves. When a segment is to be illuminated, the phase of the segment output is shifted 180 degrees with respect to BP . By sending the BP and segment signals through an exclusive-or gate, a steady low is obtained when a segment is off, and a high when it is on. That steady-state level can then be read by the PC through the PE and SLCT inputs.

A normally-open pushbutton switch (S2) allows a diode or

# EXPERIMENTING WITH PC=BASED TEST EQUIPMENT 



> This time we build a low-cost PC-based voltage, resistance, and capacitance meter:

JAMES J. BARBARELLO


A separate set of terminals provides a quick connection for diode/transistor testing.

Check your work carefully, making sure all semiconductors and the power leads were installed correctly, and that there are no solder bridges or opens on the PC board. If you have an oscilloscope handy, apply power and check for a square wave between pins 38 and 40 of ICl ; the frequency should be between 40


FIG. 1-A BASIC DMM can be built around Intersil's versatile 7106 and a few resistors and capacitors.


FIG. 2-INCREASE INPUT-VOLTAGE range by adding a voltage divider. In this circuit, range increases by a factor of ten.


FIG. 3-MEASURE RESISTANCE with the 7106 as shown here.
and 50 kHz . That measurement is not strictly necessary, but making it confirms that the circuit is operating.

## Software

The software consists of several independent QuickBASIC programs for calibrating the device and making different measurements. Unfortunately, there is not enough space to publish all of the programs. However, the software is available from the REBBS (file: PCTEST2.ZIP, 516-293-2283, 1200/2400, 8N1)

TABLE 1-4051 DECODING

| $A$ | $B$ | $C$ | $" 0 "$ | $" 1 "$ | $" 2 "$ | $" 3 "$ | $" 4 "$ | $" 5 "$ | $" 6 "$ | $" 7 "$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 0 | 0 | 0 | $X$ |  |  |  |  |  |  |  |
| 0 | 0 | 1 |  | $X$ |  |  |  |  |  |  |
| 0 | 1 | 0 |  |  | X |  |  |  |  |  |
| 0 | 1 | 1 |  |  |  | X |  |  |  |  |
| 1 | 0 | 0 |  |  |  |  | X |  |  |  |
| 1 | 0 | 1 |  |  |  |  |  | X |  |  |
| 1 | 1 | 0 |  |  |  |  |  |  | X |  |
| 1 | 1 | 1 |  |  |  |  |  |  |  | X |



FIG. 4-THE 7106 DRIVES A $31 / 2$-DIGIT LCD DISPLAY (a). Each integer from 0-9 is formed from a unique combination of segments (b); by ignoring segments c and d , each digit remains unique, but fewer lines allow a cleaner PC interface.

## PARTS LIST

All resistors are $1 / 4$-watt, $5 \%$, unless otherwise noted.
R1, R4, R7-100,000 ohms
R2-470,000 ohms
R3, R9- 10 megohms
R5, R11, R12- 1000 ohms
R6- 1 megohm
R8- 10,000 ohms
R10-100,000 ohms
Capacitors
C1-100 pF, 5\%
C2, C3-0.1 $\mu \mathrm{F}, 10 \%$
C4- $0.047 \mu \mathrm{~F}, 10 \%$
C5-0.47 $\mu \mathrm{F}, 10 \%$
Semiconductors
IC1-7106CPL $3 ½$-digit LCD A/D converter
IC2-CD4030 quad xor gate
IC3, IC5-CD4051 8-channel multiplexer
IC4-555 timer
IC6, IC7-CD4066 quad bilateral switch

D1-1N4734 5.6-volt Zener diode

## Other components

P1-DB25 male connector
S1-3-pole, double-throw slide switch
S2-normally-open SPST pushbutton switch

Miscellaneous: 9 -volt DC, $1 / 2$-amp wall transformer; case; solderless breadboard strip; PC board.

Note: A compiled version of the software that also contains datalogging capability is available on $51 / 4^{\prime \prime}$ double-density PC diskette for $\$ 12.00$ from JJ Barbarello, RD\#3, Box 241H, Tennent Road, Manalapan, NJ 07726. The author will be happy to answer any questions. Please include a self-addressed stamped envelope for reply.


FIG. 5-COMPLETE SCHEMATIC. Data lines D0-D3 select one of eight segment-output lines through the 4051's (IC5, IC6). Range selection is accomplished by data lines D3-D6, which select one of four resistors through analog-switch IC7. The selected resistor, if any, is paralleled with R9, thereby decreasing effective resistance.


FIG. 6-MOUNT ALL COMPONENTS as shown here, installing the twelve jumpers first.
and from the author, as mentioned in the parts list. Listing 1 shows the resistance-measurement program.

## Calibration and use

Both the hardware and the software must be calibrated. Begin hardware calibration by connecting a DMM across the wiper and the lower end of R10, and set S1 to the "volts" position. Apply power and adjust R10 until the DMM reads 1.000 volt.

Begin software calibration by executing the OHMFAX program. The screen will initially show null calibration factors for the five resistance ranges, R1-R5. The program asks whether you want to revise those values; press Y and enter the following initial values: R1, 10000; R2, 1000; R3, 100; R4, 10; R5, 1. The Revise question will appear again; press N this time. Then the program asks whether to save the values you just entered; press Y. Now the

program ends. You will repeat the procedure later, after the meter is up and running, to refine the calibration factors.
To measure resistance, power up your computer and connect P1 to your computer's LPT1 port. Plug in the power cube and execute the OHMS program. Note that you have five ranges (Range 1-Range 5) to choose from; Range 5 is the default. To change ranges, simply press the corresponding number on the keyboard, and the selected range will be highlighted on the screen. Place S1 in the "ohms" position, and connect a test resistor to the $\mathrm{R}+$ and $\mathrm{R}-$ terminals. Press M on the keyboard to make a measurement. As with many DMMs, readings on the higher resistance ranges take somewhat longer than on the lower ranges.

To refine calibration you'll need a DMM. Select Range 5 and measure a 1 K resistor. Remove the resistor and measure it with the digital multimeter. If the readings are not the same, use the following formula to calculate a new calibration factor for Range 5:
$\mathrm{R} 5=1+\left(\mathrm{R}_{\mathrm{DMM}}-\mathrm{R}_{\mathrm{CIS}}\right) \times 2$ where $R_{D M M}$ is the DMM reading and $\mathrm{R}_{\text {CIS }}$ is the reading with our meter.

Repeat that procedure with a 10 K resistor for Range 4, a 100 K resistor for Range 3, al megohmresistor for Range 2, and a $10-$ megohm resistor for Range 1. Calculate approximate new factors using the following formulas:

$$
\begin{aligned}
& \mathrm{R} 4=10+\left(\mathrm{R}_{\mathrm{DMM}}-\mathrm{R}_{\mathrm{CIS}}\right) \times 2 \\
& \mathrm{R} 3=100+\left(\mathrm{R}_{\mathrm{DMM}}-\mathrm{R}_{\mathrm{CII}}\right) \times 2 \\
& \mathrm{R} 2=1000+\left(\mathrm{R}_{\mathrm{DMM}}-\mathrm{R}_{\mathrm{CII}}\right) \times 2 \\
& \mathrm{R} 1=10000+\left(\mathrm{R}_{\mathrm{DMM}}-\mathrm{R}_{\mathrm{CIS}}\right) \times 2
\end{aligned}
$$

End the program by pressing $E$ on the keyboard. Recalibrate using OHMFAX, entering the calculated factors. Repeat the entire process several times until the CIS and DMM readings match each other.

As for voltage, you can measure an input between 0 and 1.999 volts DC. To extend the range, use a simple voltage divider, as shown back in Fig. 2.

To measure voltage, place $S 1$ in the "volts" position and execute the DIODE program. When ready to measure, press $M$ on the keyboard. To measure the forward voltage drop across a diode or the


FOIL PATTERN shown actual size.
base-emitter junction of a transistor, insert the device into the appropriate terminals and press S2; doing so forward biases the junction and thereby allows you to measure the voltage drop across it.

Capacitance measurement works as described in May. Place the capacitor to be measured in the $\mathrm{C}+$ and $\mathrm{C}-$ terminals; the position of S1 is irrelevant. Execute the CAP program. When ready to measure, press $M$ on the keyboard. Note: The capacitance meter described last time used parallel port line D6 to switch the low range resistor in, but the current version uses line D7. Hence there is a minor change to the CAP program. The line imme-
diately below the RESTART: label must be changed from OUT A,64 to OUT A, 128.

## Conclusion

This project is a versatile com-puter-assisted test instrument. You can increase its versatility by modifying the software. For example, you could include data logging or the ability to specify pass/fail criteria for different components. In that way data could be analyzed to determine the quality of components supplied by different vendors. In the final installment of this threepart series, we'll build an IC tester to round out our computercontrolled component inspection system.

R-E

