# COMPUTERISED ELECTRICAL EQUIPMENT CONTROL 

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Controlling electrical devices from a PC is great fun. Here is a Windows-based program written in ' C ' language for controlling up to eight devices from the PC's parallel port termed as printer port (LPT). The program accepts the input in decimal numbers and outputs at the data output pins of the PC's parallel port for controlling the connected devices.

## PC's parallel port

The parallel port is made up of three ports, namely, data port, status port and control port. It is found on the back of the PC as a D-type, 25-pin female connector. Here, we are concerned only with data lines D0 through D7 terminated at pins 2 through 9.

The data port is a write-only port, which means it can be used only to output data. Pins 18 through 25 of the connector are grounded. Control port is read/write capable, which means it can be used both for outputting and inputting some data to/from the external hardware. Status port is a readonly port, which means it can be used only to read data from the external hardware.

Table below shows pin details of
the standard parallel port (SPP) and their traditional usage. The base address of the first parallel port (LPT1) is 378 (hex) or 888 (decimal). The data port of the parallel port can be accessed at its base address. The status port can be accessed at base address +1 , i.e., 0379 hex (or 889 decimal). The control port can be accessed at base address +2 , i.e., 037A hex (or 890 decimal). In case you are using LPT2 port, then substitute the base address of LPT2 as

| PARTS LIST |  |
| :---: | :---: |
| Semiconductor: |  |
| IC1-IC4 | - CD4013 D-type flip-flop |
| IC5-IC12 | - MCT2E optocoupler |
| IC13 | - 78055 V regulator |
| T1-T8 | - BC548 npn transistor |
| D1-D8 | - 1N4007 rectifier diode |
| BR1 | - 1A, bridge rectifier |
| Resistors (all $1 / 4$-watt, $\pm 5 \%$ carbon, unless stated otherwise): |  |
| R1-R8 | - 100-ohm |
| R9-R17 | - 10-kilo-ohm |
| Capacitors: |  |
| C1 | - $1000 \mu \mathrm{~F}, 25 \mathrm{~V}$ electrolytic |
| C2 | - $0.1 \mu \mathrm{~F}$ ceramic |
| Miscellaneous: |  |
| RL1-RL8 | - 5V, 100-ohm, 1C/O relay |
| S1 | - Push-to-on switch |
| X1 | - 230 V AC primary to 9 V , 250 mA secondary transformer <br> - 25-pin D-type parallel-port male connector |

## Pin Details of the Parallel Port

| Pin number | Tradifional use | Port name | Read/Write | Port address | Port bit |
| :--- | :--- | :--- | :--- | :--- | :--- |
| $2-4$ | Data out | Data port | W | Base | D0-D2 |
| $5-9$ | Data out | - | W | Base | D3-D7 |
| 1 | Strobe | Control port | R/W | Base+2 | C0 |
| 14 | Auto feed | - | R/W | Base+2 | C1 |
| 16 | Initialise | - | R/W | Base+2 | C2 |
| 17 | Select input | - | R/W | Base+2 | C3 |
| 15 | Error | Status port | R | Base+1 | S3 |
| 13 | Select | - | R | Base+1 | S4 |
| 12 | Paper end | - | R | Base+1 | S5 |
| 10 | ACK | - | R | Base+1 | S6 |
| 11 | Busy | - | R | Base+1 | S7 |

0278 (hex) in place of 0378 (hex).
Fig. 1 shows the circuit for interfacing the PC's parallel port to the devices to be controlled. The parallel port outputs the control signals generated by the software. The control signals are not continuous but a single clock pulse. For every 'on' or 'off' control, only a single clock pulse is sent from the parallel port to the circuit.

Data pins D0 through D7 of the parallel port are connected to pin 1 of optocouplers IC5 through IC12 via resistors R1 through R8, respectively. Optocouplers ensure complete isolation of the parallel port's data pins from the relay driver circuit.

Each optocoupler consists of an infrared light-emitting diode (LED) and an npn phototransistor. When a high going pulse is available on the data pin, the internal LED drives the phototransistor of optocoupler MCT2E and it provides a clock pulse to the corresponding flip-flop (IC CD4013) section.

IC CD4013 is a dual D-type flipflop with independent set, clear and clock inputs and a single output. It accepts data when its clock pin is low and transfers it to the output on the positive-going edge of the clock. The high $Q$ output of the flip-flop drives the corresponding transistor to energise the relay and switch on/off the device.

The flip-flops are set up for toggle $\bar{m}$ ode by connecting their D inputs to Q outputs. Set inputs of all the flipflops are grounded. Switch S1 is used to reset the flip-flops manually.

Fig. 2 shows the circuit of the power supply. The AC mains is stepped down by transformer X1 to deliver secondary output of 9 V at 250 mA . The transformer output is rectified by fullwave bridge rectifier BR1, filtered by capacitor C1 and regulated by IC13 to


Fig. 1: Circuit diagram for computerised electrical equipment control


Fig. 2: Power supply circuit


Fig. 3: Screenshot of the main screen for computerised electrical equipment control
provide regulated 5 V supply. Capacitor C2 bypasses any ripple in the regulated output.

Before attempting to control the devices, the program (Control.exe) is to be run. A screen as shown in Fig. 3 appears which shows the current status of each device (on=1, off $=0$ ). To switch on a device, press the corresponding device number on the keyboard. This makes a high control pulse available on the data pin of the respective parallel-port pin and
the toggle flip-flop switches from 'off' state to 'on' state to energise the relay and switch on the device. The status of the corresponding device on screen changes automatically.

Similarly, to turn the device 'off,' press the device number on the keyboard. This again makes a high control pulse available on the data pin and the corresponding toggle flip-flop switches from 'on' state to 'off' state to de-energise the relay and turn the device 'off.'

The program can be terminated simply by pressing the 'E' key, but before that you should turn all the devices 'off.' If you try to terminate the program without shutting down any of the devices, the message "Please shutdown all the equipment" will appear on the screen for a short period followed by the main screen.


Fig. 4: Combined actual-size, single-side PCB layout for computerised electrical equipment control and power supply circuits


Fig. 5: Component layout for the $P C B$

In case you press any invalid key (not used in the software), the message "Invalid key pressed wait 2 seconds" is displayed followed by the
main screen.
The combined single-side PCB layout for the equipment control and power supply circuits is shown in Fig. 4
and and its component layout in Fig. 5.
Download source code: http:// www.efymag.com/admin/issuepdf/ Equipment\%20Control.zip

GONTROL.C

| /*COMPUTERISED ELECTRICAL EQUIPMENT CON- | tone(); | printf(" $\backslash \mathrm{n} \backslash \mathrm{n} \backslash \mathrm{n} \backslash \mathrm{nPress}$ any key"); |
| :---: | :---: | :---: |
| TROL*/ | outportb(p,16); | getch(); |
| \#include<stdio.h> | delay(500); | outportb(p,0);exit(0); |
| \#include<conio.h> | outportb(p,0); |  |
| \#include<dos.h> void main() | break; | case ' $\mathrm{E}^{\prime}$ ': |
|  | case '6': | $\mathrm{if}((\mathrm{a}\|\mathrm{b}\| \mathrm{c}\|\mathrm{d}\| \mathrm{e}\|\mathrm{f}\| \mathrm{g} \mid \mathrm{h})==1)$ |
| void tone(void); int $\mathrm{p}=0 \times 0378$; | $\mathrm{f}=!\mathrm{f}$; |  |
| int $\mathrm{p}=0 \times 0378$; ${ }^{\text {char }} \mathrm{ex}$ [23] ${ }^{\text {c }}$ Created by V MARIYAPPAN" . | tone(); | clrscr(); |
| char ex[23]=\{"Created by V.MARIYAPPAN" $\}$; int j; | outportb(p,32); delay(500); | textcolor(10);gotoxy(20,12); <br> cprintf("PLEASE SHUT DOWN ALL THE EQUIP |
| char ex1[34]=\{"For Further Details \& Improvements" \}; | outportb(p,0); | MENTS"); |
| int k ; <br> char ex2[40]=\{"Contact: Email-marietech2003@yahoo. | break; | $\begin{aligned} & \text { sound(200); } \\ & \text { delay(500); } \end{aligned}$ |
| co.in"\}; | case '7': | nosound(); |
| int l; | $\mathrm{g}=\mathrm{l} \mathrm{g}$; | delay(3000); |
| char ex3[23]=\{"Programming Language: $\mathrm{C}^{\prime \prime}$ \}; | tone(); | break; |
| int m; | outportb(p,64); |  |
| int u[10]; | delay(500); | else |
| int i; | outportb(p,0); |  |
| static a,b,c,d,e,f,g,h; char no; | break; | $\begin{aligned} & \text { clrscr(); } \\ & \text { for }(\mathrm{j}=0 ; \mathrm{j}<23 ; \mathrm{j}++) \end{aligned}$ |
| clrscr(); | case ' 8 ': |  |
| textcolor(15);gotoxy(20,6); <br> cprintf("COMPUTERISED ELECTRICAL EQUIPMENT | $\begin{aligned} & \mathrm{h}=!\mathrm{h} ; \\ & \text { tone(); } \end{aligned}$ | textcolor(10);gotoxy $(20+\mathrm{j}, 12)$; cprintf("\%c",ex[j]); |
| cprintf( "COMPUTERISED ELECTRICAL EQUIPMENT <br> CONTROL"); | tone(); outportb(p,128); | cprintt(" $\%{ }^{c}{ }^{\text {c }}$,ex[j]); <br> sound (2500+j); |
| textcolor(11);gotoxy (20,7); | delay(500); | delay(30); |
| cprintf("------------------------------------->-1); | outportb(p,0); | nosound(); |
| textcolor(11);gotoxy(10,10); <br> cprintf("EQUIPMENT NO: $1 \begin{array}{lllllll}1 & 2 & 3 & 4 & 5 & 6 & 7\end{array}$ | break; | $\text { for }(m=0 ; m<23 ; m++)$ |
| $\left.8^{\prime \prime}\right)$; | case ' e ': |  |
| textcolor(11);gotoxy(10,12); <br> cprintf("STATUS: \%d \%d \%d \%d \%d \%d | $i f((a\|b\| c\|d\| e\|f\| g \mid h)==1)$ | textcolor(10);gotoxy (20+m,13); cprintf(" $\% \mathrm{c}^{\prime \prime}$,ex3[m]); |
| \%d \%d", a, b, c, d,e,f,g,h); | clrscr(); | sound ( $3500+\mathrm{m}$ ); |
| textcolor(10);gotoxy $(9,16)$; <br> cprintf("FOR ‘ON' AND 'OFF' AN EQUIPMENT PRESS | textcolor(10);gotoxy $(20,12)$; <br> cprintf("PLEASE SHUT DOWN ALL THE EQUIP- | delay(30); <br> nosound(): |
| CORRESPONDING EQUIP.NO."); | MENTS"); | $\begin{aligned} & \text { no } \\ & \} \end{aligned}$ |
| textcolor(11);gotoxy(28,18); | sound(200); | for (k=0;k<34;k++) |
| cprintf("STATUS $0=$ OFF STATUS $1=O N$ "); | delay(500); |  |
| textcolor(12);gotoxy(32,20); | nosound(); | textcolor(10);gotoxy(20+k,14); |
| no=getch(); | break; | $\text { sound }(3000+\mathrm{k}) \text {; }$ |
| switch(no) |  | delay (30); |
|  | else | nosound(); |
| case ' 1 ': |  |  |
| $\mathrm{a}=$ !a; | clrscr(); | for(l=0;1<40; $1++$ ) |
| tone(); | for $(\mathrm{j}=0 ; \mathrm{j}<23 \mathrm{j}+\mathrm{+}$ ) |  |
| outportb(p,1); |  | textcolor(10);gotoxy (20+1,15); |
| delay(500); <br> outportb(p,0); | textcolor(10);gotoxy(20+j,12); crrintf(" \% c" ${ }^{\prime \prime}$ [j]): | cprintf("\%c",ex2[1]); <br> sound ( $3500+1$ ). |
| outportb(p,0); break; | $\begin{aligned} & \text { cprintf(" \% } \% \text { "', ex }[\mathrm{j}]) \text {; } \\ & \text { sound }(3000+\mathrm{j}) \text {; } \end{aligned}$ | $\begin{aligned} & \text { sound }(3500+1) \text {; } \\ & \text { delay }(30) \text {; } \end{aligned}$ |
|  | delay (30); | nosound(); |
| case ' 2 ': | nosound(); |  |
| $\mathrm{b}=!\mathrm{b}$; |  | printf(" n \ $\mathrm{n} \backslash \mathrm{n} \backslash \mathrm{nPress}$ any key"); |
| tone(); | for (m=0;m<23;m++) | getch(); |
| outportb(p,2); <br> delay(500); | \{ textcolor(10);gotoxy(20+m,13); | outportb(p,0);exit(0); |
| outportb(p,0); | crintf("\% \%", ex3[m]); |  |
| break; | $\begin{aligned} & \text { sound }(1800+\mathrm{m}) \text {; } \\ & \text { delay }(30) \text {; } \end{aligned}$ | default: <br> clrscr(); |
| case ' 3 ': | nosound(); | sound(500); |
| $\mathrm{c}=!\mathrm{c} ;{ }^{\text {c }}$, |  | delay(100); |
| tone(); | for (k=0;k<34;k++) | nosound(); |
| outportb(p,4); delay(500): | \{ textcolor(10) $\cdot \operatorname{gotoxy}(20+\mathrm{k}, 14)$; | textcolor(11);gotoxy $(30,12)$; |
| delay(500); <br> outportb(p,0); | textcolor(10);gotoxy(20+k,14); cprintf("\%c",ex1[k]); | cprintf("INVALID KEY PRESSED"); textcolor(11);gotoxy(33,14); |
| break; | $\begin{aligned} & \text { sound (2000+k); } \\ & \text { delay }(30) \text {; } \end{aligned}$ | cprintf("WAIT 2 SECONDS"); delay(3000); |
| case ' 4 ': | nosound(); | break; |
| d=!d; |  | $\text { \} }$ |
| tone(); | for(l=0;1<40;1++) | main(); |
| outportb(p,8); <br> delay(500); | \{ textcolor(10);gotoxy(20+1,15); | \} void tone(void) |
| outportb(p,0); | cprintf("\% $\%$ ", ex2[l]); | \{ |
| break; | $\begin{aligned} & \text { sound }(2500+1) \text {; } \\ & \text { delay }(30) \text {; } \end{aligned}$ | sound(1000); delay(100); |
| case ' 5 ': | nosound(); | nosound(); |
| $\mathrm{e}=$ !e; |  |  |

