



NOTE: REAR PANEL JACKS N° J4, J5, J6, J7 SHOWN ON THIS SCHEMATIC FOR CLARITY ONLY.



LAST USED:		RECORD OF CHANGES:		A/D A STEREO TAPPED DELAY, STD-1	
R 215	T 26	2-1-88	CORRECTED PIN NOS 4 AND 5.	MOTHER BOARD PN: 80-100G	
C 126	S 9	12-10-81	DELETE: R21, R46, R79, R142, C16, C44, C54, C92, T4, T11, T17.	SCALE: 50:1	APPROVED BY: [signature]
D 15			47pf added by hand soldering near U5.	DATE: 7-21-83	DESIGNED: 3-31-83
L 5		2-8-82	R117, 800K -> 100K; R119, 220K -> 150K; R113, 47K -> 27K.	SUPERSEDES R1HC,D,E,F,G	
U 27			R127, 47K -> 27K.	DRAWING NUMBER: R:14J	
Q 5					
M 17					