8/9/83 DA

ADA SIGNAL PROCESSORS
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Flanger Calibration
PCB 10-185P/10-189

1) Check power supply voltage
measure pin 3 of IC7 for regulated supply voltage
of +15v ±4%

2) Calibrate clock
measure clock with frequency counter at clock test point (between IC 6 and R14)
T5 is clock freq. range T4 is clock freq. offset
turn front panel range control fully counter clockwise
calibrate T4 and T5 (trims are inter active) for clock freq. range
of 34.8 Khz to 1300 Khz when front panel Manual control is rotated
from fully counter clockwise to fully clockwise.

3) calibrate sweep level trim
after manual sweep range of 34.8 Khz to 1300 Khz has been achieved
turn range control fully clockwise and speed control fully counter clockwise
trim T3 for LFO sweep from 34.8 Khz to 1300 Khz
(note: if sweep goes too low LFO may stall)

4) calibrate BBD bias
monitor pin 8 of IC 2 with scope
inject 1 Khz sine wave at input
increase input signal to clipping
trim T1 for symetrical clipping

5) trim maximum regeneration trim T2 for near oscillation throughout the
sweep range with front panel regeneration full.