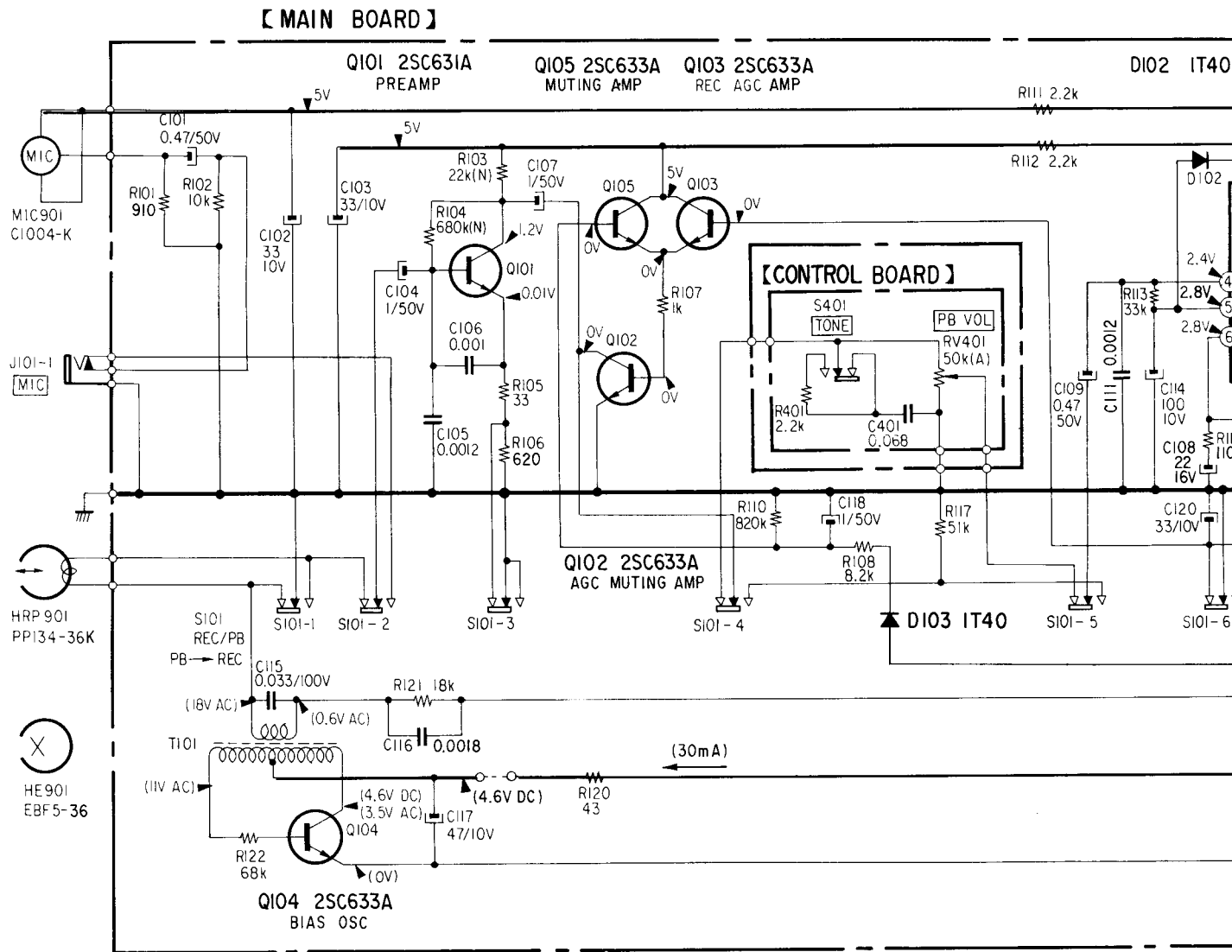


Note: The components identified by shading and Δ mark are critical for safety. Replace only with part number specified.


4-2. SCHEMATIC DIAGRAM

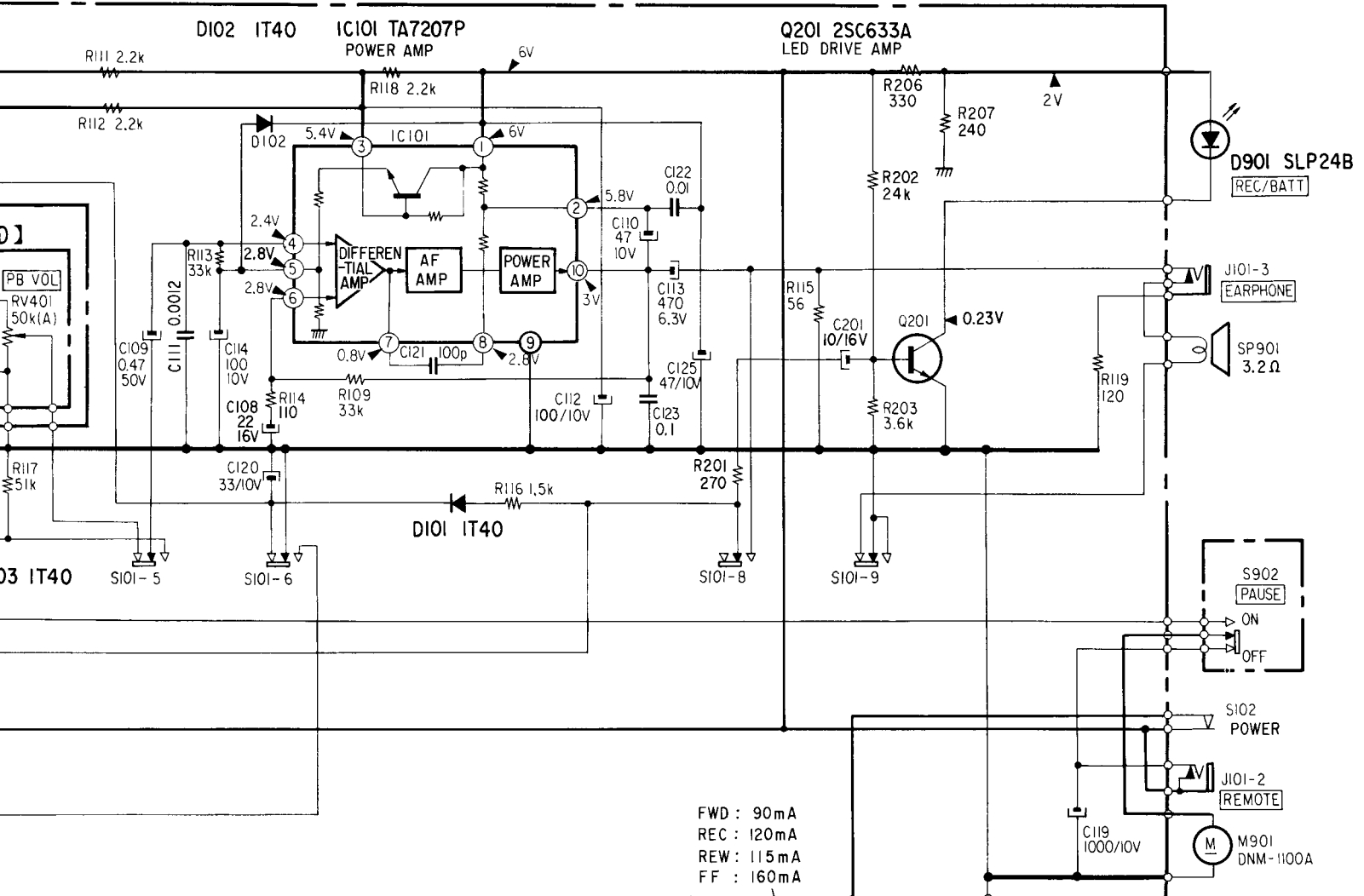


Note:

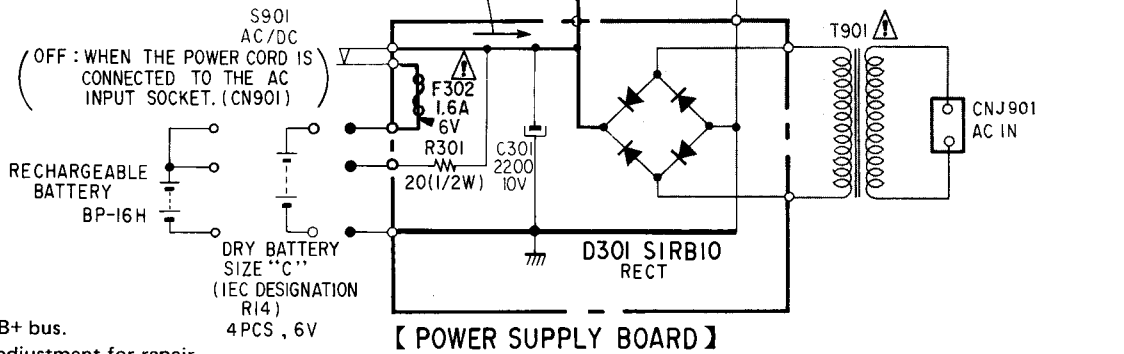
- All capacitors are in μF and ceramic unless otherwise noted. 50WV or less are not indicated except for electrolytics. $\text{pF} = \mu\mu\text{F}$, elect = electrolytic
- All resistors are in ohms, $\frac{1}{4}\text{W}$ unless otherwise noted. $\text{k}\Omega = 1000\Omega$; $\text{M}\Omega = 1000\text{k}\Omega$
- (N) : low-noise resistor.
- : panel designation.

- Voltages are dc with respect to ground unless otherwise noted.
- Readings are taken under no-signal (detuned) conditions with a VOM ($20\text{k}\Omega/\text{V}$).
(V): REC
- Voltage variations may be noted due to normal production tolerances.
- Transistor base-emitter voltages are measured on the 2.5V range.
- Total current is measured with no cassette installed.

Note: Les composants identifiés par un trame et une marque  sont critiques pour la sécurité. Ne les remplacer que par une pièce portant le numéro spécifié.



FWD : 90mA
 REC : 120mA
 REW : 115mA
 FF : 160mA



unless otherwise
 (uned) conditions
 to normal pro-
 ured on the 2.5V
 cassette installed.

- — : B+ bus.
- □ : adjustment for repair.
- Switch

Ref. No.	Switch	Position
S101	REC/PB	PB
S102	POWER	OFF
S901	AC/DC	ON
S902	PAUSE	OFF