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TAS5001, Digital Audio PWM Processor

DEVICE STATUS: **ACTIVE**

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DATASHEET

[tas5001.pdf](#) (229 kb)
Rev.A

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PRODUCT INFORMATION

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DEVELOPMENT TOOLS

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TECHNICAL DOCUMENTS

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PARAMETER NAME	TAS5001
Fs (max) (kHz)	96
Fs (min) (kHz)	44.1
Dynamic Range (dB)	96
THD+N (System Performance) (%)	Less Than 0.08
Bits	16, 20, 24

DEVELOPMENT SUPPORT

- [KnowledgeBase](#)
- [Training](#)

APPLICATIONS

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FEATURES

- TAS5001 + TAS5100 TDAA System – High Quality Digital Audio Amplification
- 96-dB Dynamic Range (TAS5001 Device)
- 93-dB Dynamic Range (TAS5001 and TAS5100 System Measured at Speaker Terminals)
- THD+N < 0.08% (1 kHz, 0 to 30 W RMS Into 6) (TAS5001 & TAS5100 System Measured at Speaker Terminals)
- Power Efficiency Is 90% Into 8- Load
- 16-, 20-, or 24-Bit Input Data
- 32-kHz, 44.1-kHz, 48-kHz, 88.2-kHz, 96-kHz Sampling Rates
- Economical 48-Pin TQFP Package
- Lower-Jitter Internal PLL
- 3.3-V Power Supply
- Mute
- Clicks and Pops Reduction (Patent Pending)
- APPLICATIONS
 - DVD Audio
 - Home Theater
 - Car Audio Amplifiers and Head Units
 - Internet Music Appliance
 - Mini/Micro Component Systems

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