



**YTS434-F**

**mLAN-PH1(mLAN<sup>1</sup> Packet Handler 1)**

## **IEEE1394 Digital Audio/MIDI Transmission LSI**

### **INTRODUCTION**

The YTS434-F is to be used with MD8413 Link chip from FUJIFILM Microdevices<sup>2</sup> to enable isochronous transmission of Digital Audio and MIDI over IEEE1394 bus.

### **FEATURES**

- Complies with "Audio and Music Data Transmission Protocol V1.0"<sup>3</sup>.
- Simultaneous reception/transmission of 8 channels of digital audio, and MIDI.
- Handles all aspects of Audio transmission/reception. No extra loading on software.
- MIDI reception/transmission by external microprocessor for more flexibility.
- Audio I/O Ports support I2S, JEDEC, and JAPAN data formats. The audio stream for each output pin may be selected from any position (within the packet) of any isochronous channel.
- PLL generation of Audio sync signals. (External PLL may also be used)
- Built-in DIR(digital audio receiver) and DIT(digital audio transmitter).
- Up to 4 chips may be used together to expand channel handling capabilities.
- 128pin QFP
- CMOS +5V, +3.3V


### **YAMAHA Corporation**

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<sup>1</sup> mLAN and  are trademark pending from YAMAHA corporation.

<sup>2</sup> Refer to <http://www.fujifilm.co.jp/ffm/>.

<sup>3</sup> Specification is available from <http://www.1394ta.org/abouttech/specifications/techspec.html>.

**YTS434-F**  
**BLOCK DIAGRAM**

