

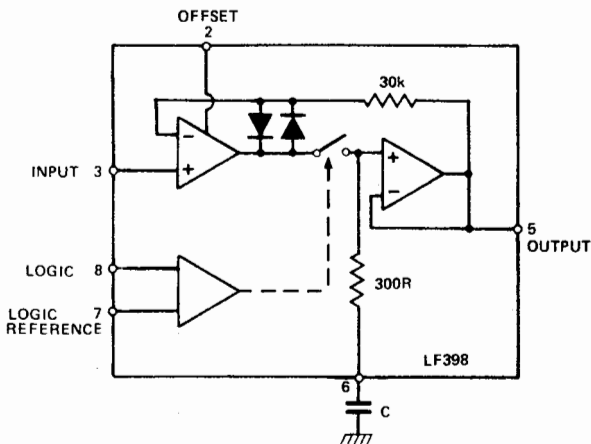
# 45

## Monolithic Sample And Hold

Logic high = sample

Logic low = hold

Logic reference = TTL or CMOS



Use a printed circuit guard ring (connected to the output voltage) around the hold capacitor