## Single op amp achieves double-hysteresis-transfer characteristic

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1In process-control applications requiring discontinuous controllers, the most elementary choice is a two-position-mode or on/off controller. A typical example of such a con-
troller is a space heater. If the temperature drops below a setpoint, the heater turns on, and, if the temperature rises above the setpoint, it turns off. In the analog domain, the basis for the ba-


Figure 1 One straightforward way of obtaining a double-hysteresis-transfer characteristic uses three op amps with voltage references and zener diodes.


Figure 2 The I/O-transfer characteristic of the circuit in Figure1 exhibits two hysteresis bands.

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sic implementation of a two-position controller is an analog comparator or an open-loop operational amplifier. However, to avoid false switching, the typical implementation uses the wellknown Schmitt trigger.

A logical extension of the two-position control mode is to provide sever-al-rather than two-intermediate settings of the controller's output. You can use this discontinuous-control mode to reduce the cycling behavior, overshoot, or undershoot inherent in the two-position mode. In fact, however, it is usually speedier to use some other mode when the two-position mode is unsatisfactory. The most common example is the three-position controller. Figure 1 shows one simple way to implement this controller. In this configuration, the Schmitt triggers around the operational amplifiers, $A_{1}$ and $A_{2}$, which implement the negative and positive hysteresis, respectively. You can replace $A_{1}$ and $A_{2}$ with analog comparators, such as an LM311 or similar. Figure 2 shows the I/O-transfer characteristic of the circuit in Figure 1:

$$
V_{M}=V_{R E F} \frac{R_{1}+R_{2}}{R_{2}}
$$

## designideas

$$
\mathrm{V}_{\mathrm{H}}=\mathrm{V}_{\mathrm{M}}+\left(\mathrm{V}_{\mathrm{Z}}+\mathrm{V}_{\mathrm{Y}}\right) \frac{\mathrm{R}_{1}}{\mathrm{R}_{2}}
$$

and

$$
\mathrm{V}_{\mathrm{L}}=\mathrm{V}_{\mathrm{M}}-\left(\mathrm{V}_{\mathrm{Z}}+\mathrm{V}_{\mathrm{Y}}\right) \frac{\mathrm{R}_{1}}{\mathrm{R}_{2}}
$$

$V_{Z}$ and $V_{Y}$ are, respectively, the breakdown and the forward voltages of the four zener diodes.

Figure 3 shows a more efficient way to implement a three-position controller. The circuit's basis is a single operational amplifier, and it needs no reference voltages. The input and output diodes determine the upper high-voltage and lower low-voltage switchingthreshold levels and the hysteresis of the comparator. Putting $\mathrm{V}_{\text {IN }}(\mathrm{t})$ in the middle band eliminates the input diodes from the circuit, and the circuit is essentially a voltage follower with positive feedback. The output voltage follows $\mathrm{V}_{\mathrm{A}}(\mathrm{t})$, but the positive feedback establishing $\mathrm{V}_{\mathrm{A}}(\mathrm{t})$ sets this voltage at some fraction of the output voltage. So, two constraints define the output level in this circuit state: $\mathrm{V}_{\mathrm{OUT}}(\mathrm{t})=\mathrm{V}_{\mathrm{A}}(\mathrm{t})$, and

$$
\mathrm{V}_{\mathrm{A}}(\mathrm{t})=\mathrm{V}_{\text {OUT }}(\mathrm{t}) \frac{\mathrm{R}_{1}}{\mathrm{R}_{1}+\mathrm{R}_{2}}
$$

The only condition satisfying these two constraints is that $\mathrm{V}_{\text {OUT }}$ and $\mathrm{V}_{\mathrm{A}}=0 \mathrm{~V}$; so, the output remains at 0 V when the input diodes are reverse-biased. A OV output state continues until input voltage increases with positive or negative values. Then, one of the two input zener diodes conducts, driving the amplifier output positive or negative at an input voltage of $\pm \mathrm{V}_{\mathrm{H}}$. In this condition, when absolute input voltage decreases, the amplifier output again goes to OV at an input voltage of $\pm \mathrm{V}_{\mathrm{L}}$. Thus, the design equations for $\mathrm{V}_{\mathrm{H}}$ and $\mathrm{V}_{\mathrm{L}}$ are $\mathrm{V}_{\mathrm{H}}=\mathrm{V}_{\mathrm{Z1}}+\mathrm{V}_{\gamma}$, and

Figure 4 shows the $I /{ }^{\prime} O$-transfer characteristic of the circuit with the values in Figure 3, where $D_{1 A}$ and $D_{1 B}$ $\mathrm{V}_{\mathrm{L}}=\left(\mathrm{V}_{\mathrm{Z} 1}+\mathrm{V}_{\mathrm{Y} 1}\right)-\left(\mathrm{V}_{\mathrm{Z} 2}+\mathrm{V}_{\mathrm{Y} 2}\right) \frac{\mathrm{R}_{1}}{\mathrm{R}_{1}+\mathrm{R}_{2}}$.
are 6.8 V 1 N 4099 zener diodes and $\mathrm{D}_{2 \mathrm{~A}}$ and $D_{2 B}$ are 3 V 1 N 5225 zener diodes. Figure 5 shows the output voltage when you apply a triangular waveform at the circuit's input.EDN


Figure 3 This circuit achieves dual hysteresis using only one op amp.


Figure 4 This oscilloscope trace shows the transfer characteristic of the circuit in Figure 3.


Figure 5 This oscilloscope trace shows the response of the circuit in Figure 3 to a triangular input waveform.

