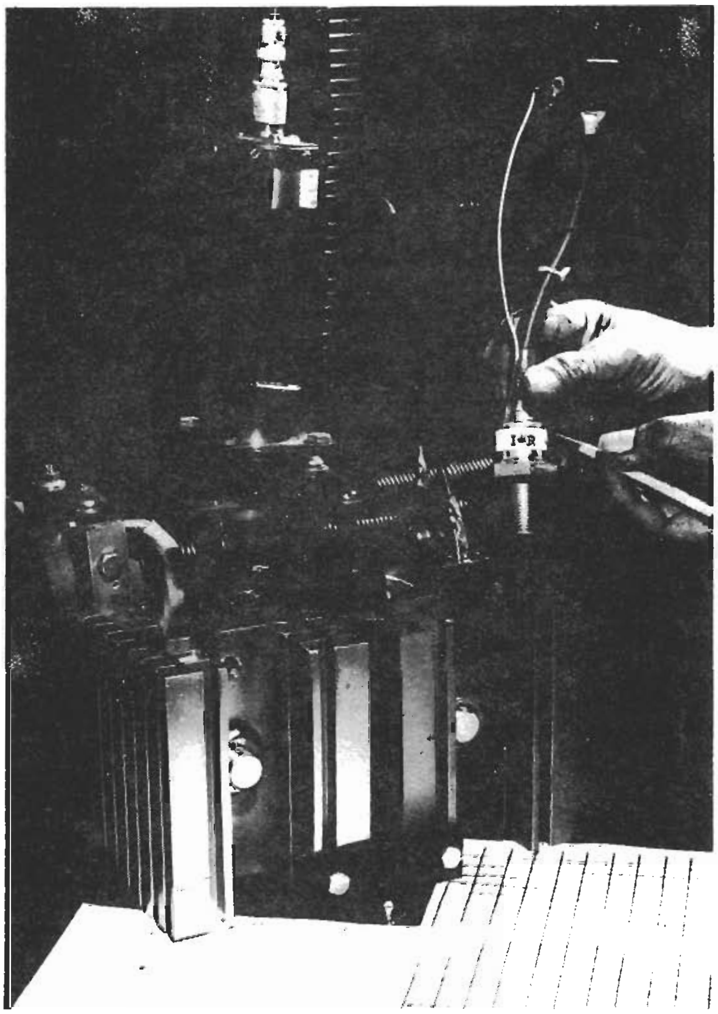

Need a triac for 200 amps at 1,000 volts? It's available now. Here's a look at this startlingly different device, and some examples of how it can be used.

Applying the power-logic triac

BY DAVE COOPER
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THE ADVENT of silicon-controlled rectifiers that handle many kilowatts of power with high control gain and low power circuit losses has had a sharp impact on industrial controls.

This activity, coupled with an increasing demand for more accurate and sophisticated control functions, has forced the development of more advanced control and logic devices, including integrated circuits. It was only a matter of time before this same drive for design of multiple-control functions in a single package was extended to higher power devices. The development of the silicon bidirectional thyristor (silicon symmetrical switch, diac, triac, etc.) was the first step in extending the principle to a-c power semiconductors. However, the power-handling capability of the first devices was not great enough to enable their use in heavy industrial systems.

A new epitaxial logic-triac is now available which overcomes these deficiencies. It can control more than 140 kw of a-c power. The power logic-triac (IR series 200AC) is capable of carrying 200 amp rms current and has a main terminal peak operating voltage rating of up to 1,000 v. The 1,000-v rated device has a guaranteed minimum breakover voltage of 1,100 v. *(This unit will control more than 20 times the power of any other existing triac.)* In addition, by incorporating a selective gate characteristic, the logic-triac becomes a uniquely versatile power-control device.

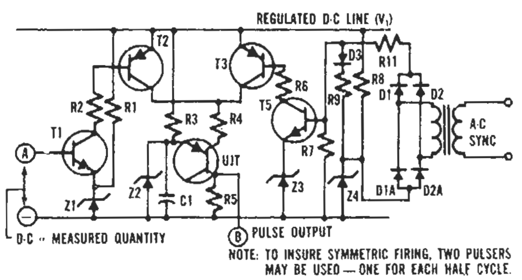
Applications for the logic-triac include:

- Heating controls.
- D-C motor drives.
- D-C motor reversing drives.
- Light flashers.
- Transformer static tap changers.
- Chopper drives with dynamic braking for d-c motors.
- Bridge inverters.

The logic-triac can switch to the ON state when a signal is applied to a single-gate connection. It can be made to conduct in either direction by applying either a unidirectional gate signal or a properly phased bidirectional gate signal.

With one polarity of gate bias the logic-triac can be characterized as a pnpn switch in parallel with an npnp switch, both devices having a common gate.

ZERO CROSSOVER FIRING



A BRIEF EXPLANATION OF THE DRIVING CIRCUIT TO TRIGGER THE LOGIC-TRIAC

Transistor T1 and zener diode Z1 comprise the reference amplifier which establishes an error signal. If the measured quantity is greater than the reference, T2 will turn ON pulling the upper base of the UJT (unijunction transistor) to the regulated d-c line voltage. The zener diode across C1 is chosen so that it clamps the emitter of the UJT to a low enough voltage so that if the UJT upper base is at V1 the UJT cannot fire. Thus, regardless of what else is happening in the circuit, if the measured quantity is above the reference, the load current will be cut off when the UJT stops pulsing.

Examining the right side of the circuit, we see that the output of a small transformer (12 v output is suitable) is rectified and added to a zener voltage (Z4). The sum of these two voltages is compared to a reference zener (Z3). Should this sum exceed the reference level (indicating a line voltage far from crossover), transistors T5 and T3 are turned on pulling the upper base of the UJT to V1 and also preventing the UJT from firing.

Therefore, if the line voltage is considerably greater than zero or if the measured quantity exceeds the reference, no firing can take place.

On the other hand, if the measured quantity is low and the line voltage is near zero, the UJT will fire and supply power to the load. Values of R3 and C1 must be selected so that the pulse from the UJT will bridge the crossover from the declining edge of the sine wave to the increasing edge of the sine wave. This can be somewhat alleviated by connecting a small capacitor from the junction of R11 and D3 to the negative of V1.

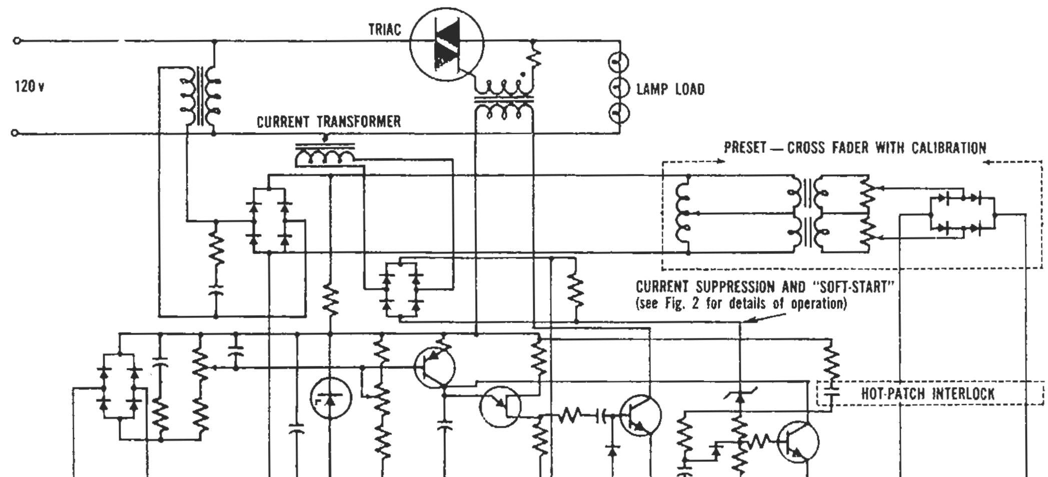


Fig. 1. Power lamp dimmer and cross fader controls four 5-kw spot lights. Current-suppression circuit limits inrush currents.

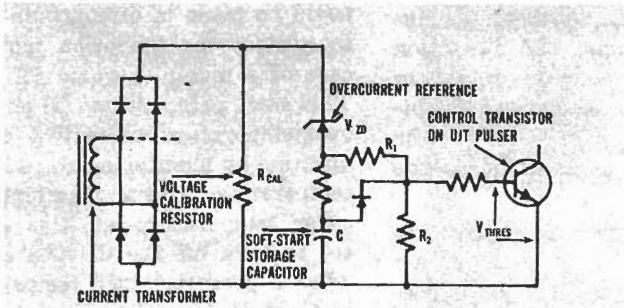


Fig. 2.

Fig. 2. The soft-start circuit. Here's how it works: When current $I_{primary}$ in current transformer satisfies

$$I_{primary} \times a \times R_{cal} \quad V_{ZD} - \left(\frac{V_{thres} \times R_1 - R_2}{R_2} \right)$$

where a = current transformer turns ratio, then the UJT control transistor begins to turn on — shunting the pulser capacitor in the UJT relaxation oscillator, reducing the pulse rate at the same rate as C charges. When the current transformer output decreases, C discharges through the base to the emitter of the control transistor delaying it from turning off and providing soft restoration of the full output to the lamps.

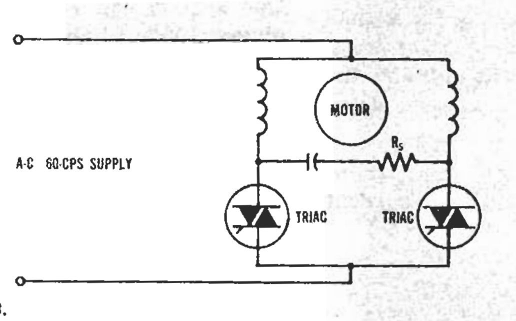


Fig. 3.

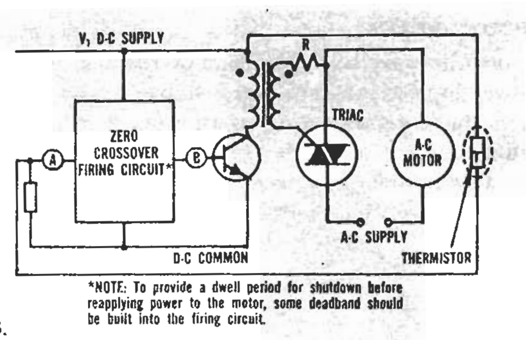


Fig. 5.

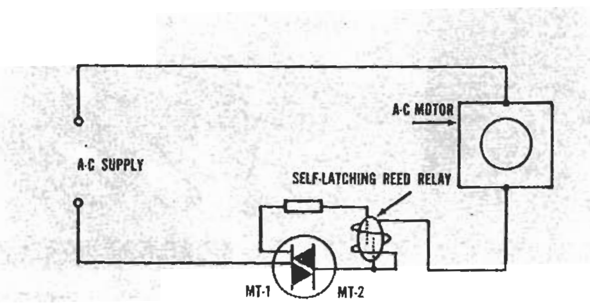


Fig. 4.

Fig. 3. Single-phase induction motor drive uses two triacs. With this circuit, the motor can be made to turn in either direction.

Fig. 4. Overcurrent protection for an induction motor is provided by this triac circuit. It can also be used for overtemperature protection.

Fig. 5. Overtemperature protection requires monitoring the winding temperature of the motor with a thermistor.

With the opposite gate bias polarity the device acts as a single pnpn controlled rectifier. Due to separation of conduction areas, the carriers of conduction in one direction do not influence the blocking capabilities of the device in the other direction. The 200AC power logic-triac has a guaranteed dv/dt capability of 20 v/μsec and a di/dt limit of 50 amp/μsec. Because of the epitaxial process and the ability to maintain good process control, all 200AC power logic-triacs may be turned on in either direction by overvoltage or high dv/dt without causing damage to the device (assuming no other ratings are exceeded).

A-C control applications

The logic-triac provides simple control of large amounts of a-c power and the selective gate characteristic enables control of d-c by simply reversing the gate bias. The following circuit description illustrates the a-c control characteristics of the logic-triac.

A power lamp dimmer and cross fader circuit is shown in Fig. 1. The power circuit consists of a single triac controlling four 5-kw spot lights. In this type of circuit, lamp inrush currents must be limited. Theoretically, inrush currents are limited only by the cold filament resistance of the lamps. Also, the line impedance may act as a limitation. These currents should be within the allowable peak current rating of the triac. This can be done by incorporating a current limit control (for soft start of the firing circuit) and a timed phase advance indicated by "A" in Fig. 1. In practice, the short time overcurrent capability of the triac should not visibly detain the lamp load starting, especially compared to older types of installations (magnetic amplifiers or motor-driven variable autotransformers).

The advantages of using the logic-triac as opposed to magnetic devices for lamp brilliance control are basically the same as for other applications. And the 8-oz logic-triac offers a considerable weight and size advantage over the many pounds of saturable reactors that would be needed to do the same job.

If some lamps are already operating on the dimmer and a cold lamp is suddenly connected into the load, the inrush current is superimposed on the existing steady-state load current. To protect the triac, the "soft-start" current should be activated (perhaps by an interlock on the "hot-patch" switch shown in Fig. 1). Details of the soft-start circuit are shown in Fig. 2.

When lamp filament resonance is a problem because of the steep wavefront presented by phase-control waveshapes, line inductive reactance can be inserted to reduce harmonics in the filaments. This inductance will also minimize inrush currents.

Motor drives

Another circuit application of the power logic-triac is in motor drives. It is made attractive by the unique single-control characteristic of the a-c power-control device. A single-phase induction motor reversing drive can be developed, for instance, by using two triacs as shown in Fig. 3. By properly phasing the signal with the line, the induction motor can be made to rotate in either direction. Because of the bidirectional characteristics of the triacs, both can be mounted on the same heat sink.

When size and reliability are paramount in a particular a-c motor-drive application, it is possible to drive a 10-hp motor using the 200AC logic-triac with no external means of limiting the inrush current to the motor. With external means of limiting this current, the 200AC power logic-triac can drive considerably larger motors.

If the motor current were monitored by a self-latching current relay (a reed switch relay would be ideal), the triac driving circuit could be made to discontinue firing to protect the induction motor in case of a motor overload (Fig. 4). This same principle can be used for overtemperature protection of the machine by monitoring the winding temperature. Use a thermistor or other temperature-sensitive device set to turn off the driving circuit when a predetermined temperature is reached (Fig. 5). Used in conjunction with zero crossover firing, this system can keep the triac on almost 100% of the time.

A list of potential applications for the logic-triac was presented earlier. This list will grow rapidly as engineers find new ways to solve their problems with this device. ●

Reprinted From ELECTRONIC PRODUCTS Magazine — December 1967 Issue

Controlling ac loads with C-MOS bilateral switches

by Arthur Johnson
Darlington, Md.

Power to an ac load can be efficiently controlled by an integrated complementary-MOS quad bilateral switch and a capacitively triggered sensitive-gate triac. The necessary gate-triggering current comes, not from the low-voltage C-MOS power supply, but from the ac line.

Capacitor-triggering is best for firing the triac because it produces the maximum current (at 90° phase shift) when the ac voltage crosses the zero-voltage level. Therefore, the fullest possible use is made of gate-triggering current. Also, the triac is switched into conduction at a low voltage to reduce switching transients, and maximum power is delivered to the load.

The driver circuit for ac loads is drawn in the diagram. Because the on-resistance of each C-MOS bilateral switch is several hundred ohms, circuit voltages could falsely trigger the triac. The triac gate therefore needs to be isolated by the series switch, which, in turn, needs to be protected in its nonconducting state by the shunt switch from possibly damaging high voltages.

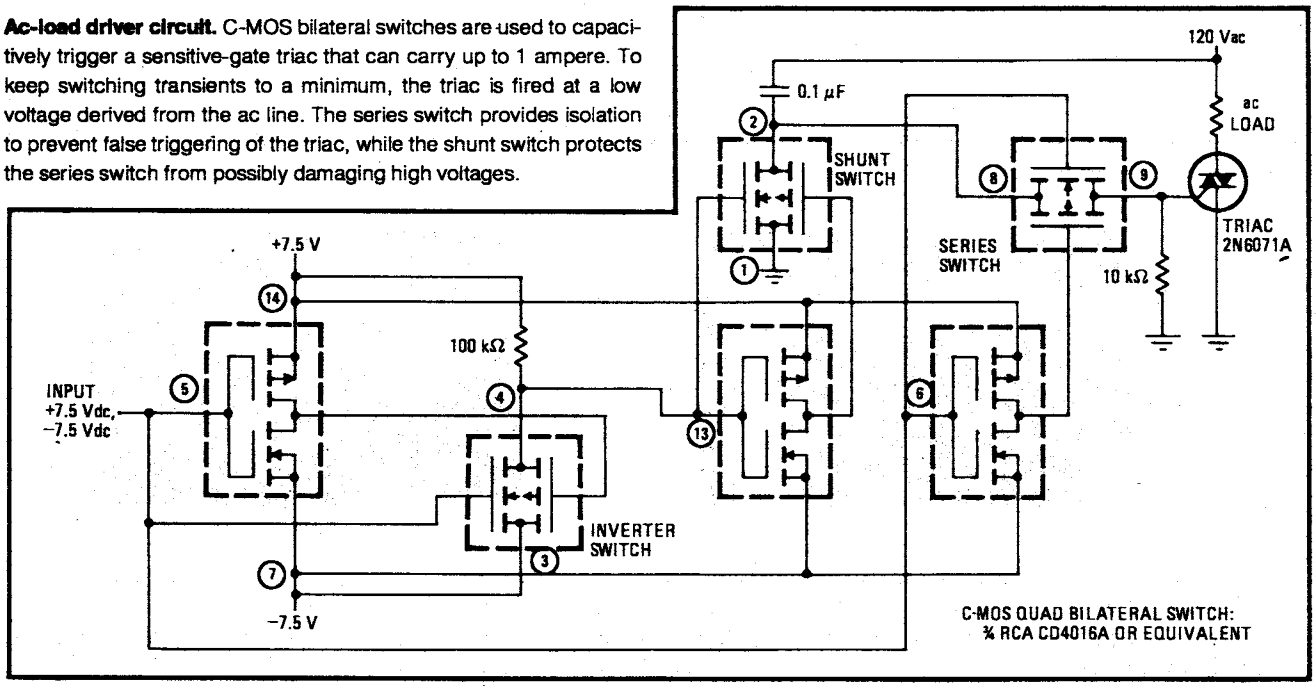
Two power-supply voltages, +7.5 volts and -7.5 v, are needed to control both positive and negative ac voltage excursions. This may prove to be a minor inconvenience. But since the necessary gate-triggering current does not have to come from these supplies, they may be simple half-wave-rectified high-resistance sources.

The sensitive-gate triac used here has a maximum current-carrying capacity of 1 ampere. If a larger load must be handled, a triac with higher ratings can be controlled by the smaller triac. In this way, a large load can be controlled without wasting a large amount of energy.

The capacitor value is chosen to provide the required triac-triggering current of 5 milliamperes maximum:

$$C = (5\ mA)/2\pi f E_{max}$$

where f is the ac frequency and e_{max} is the zero-to-peak ac voltage level. □



Ac-load driver circuit. C-MOS bilateral switches are used to capacitively trigger a sensitive-gate triac that can carry up to 1 ampere. To keep switching transients to a minimum, the triac is fired at a low voltage derived from the ac line. The series switch provides isolation to prevent false triggering of the triac, while the shunt switch protects the series switch from possibly damaging high voltages.

POWER LOGIC-TRIAC...
the inside story

by John Gault, Project Manager,
Research and Development

Introduction

An examination of the construction of International Rectifier's new power logic-triac might lead one to believe that except for a modification of the gating in one portion of the device, the logic-triac could be viewed as two anti-parallel silicon controlled rectifiers constructed in one wafer of silicon. This similarity is illustrated by Figure 1(a), a block diagram of two SCR's connected in anti-parallel, and Figure 1(b), a block diagram of a logic-triac. If the gating on the right hand SCR were changed and the control PNP regions connected as illustrated in Figure 1(c) the similarity would be complete.

Gating

As might be expected, the gating characteristics of the logic-triac are different from those of the anti-parallel SCR's. For the anti-parallel SCR's, a gate signal is applied between Gate 1 and Terminal 1 when Terminal 1 is negative, and between Gate 2 and Terminal 2 when Terminal 2 is negative. This method of operation requires two separate gate circuits.

In the logic-triac, Gate 1 and Gate 2 are connected together and operated from a single gate circuit connected between the gates and Terminal 1. The easiest firing mode for AC control is achieved by biasing the gates positive when Terminal 1 is negative, and negative when Terminal 1 is positive. Triggering for AC control is also possible with negative bias on the gates during both half cycles. For DC control, a positive bias will result in an operation similar to an SCR. This type of operation was made possible by a design in which a positive gate bias will not fire the device when Terminal 1 is positive.

When Terminal 1 is negative, triggering takes place in the same manner as in an SCR. The positive bias at Gate 1 with respect to the top N type cathode, causes injection of electrons from this cathode into the P type region as shown in Figure 2. A large percentage of these injected electrons are collected by junction J-2 which is reversed biased. This collector current in turn induces a forward bias across junction J-3 which results in injection of holes into the central N type region. Some of these holes recombine with electrons but a small percentage of them are collected by reverse biased junction J-2. This hole injection occurs over a larger area of junction J-3 than the area of the initial electron injection from J-1. The ratio of the areas is a function of the diffusion length of electrons in the upper P region and the resistivity of the central N region.

The collected holes induce an additional injection of electrons from J-1 over an even larger area than the hole injection area. This counter injection continues until the entire area under the upper N type region or cathode is conducting and the reverse bias across J-2 has collapsed. Although the counter injection has been described as a stepwise process, since collection is not an abrupt occurrance, the growth of the conduction area is a fairly smooth continuous process.

Since Terminal 1 is also connected to the upper P type region some shunting of the gate signal will occur. This shunt current is, however, minimized by judicious placement of Gate 1. The same type of shunting occurs in an SCR with a shorted emitter construction.

As shown in Figure 3 when Terminal 1 is positive, a negative bias on Gate 2 will cause injection from the N type gate region. Many of these injected electrons will recombine with holes. This current can be considered to be as a parasitic diode current between Gate 2 and Terminal 1 since it has no useful function. Some of the injected electrons, however, will be collected by J-2 in the vicinity of

COVER

The environmental oven shown on the cover has been modified to use IR's 200 Amp logic-triac in a three phase A.C. power control system.

A variety of applications has been developed in the past as designers applied the attributes of new semiconductors to their circuitry. The power triac will undoubtedly nurture the development of circuits which have never before been technically or economically achievable. The list of applications is expected to grow as the more aggressive manufacturers begin designing the power triac into the end product.

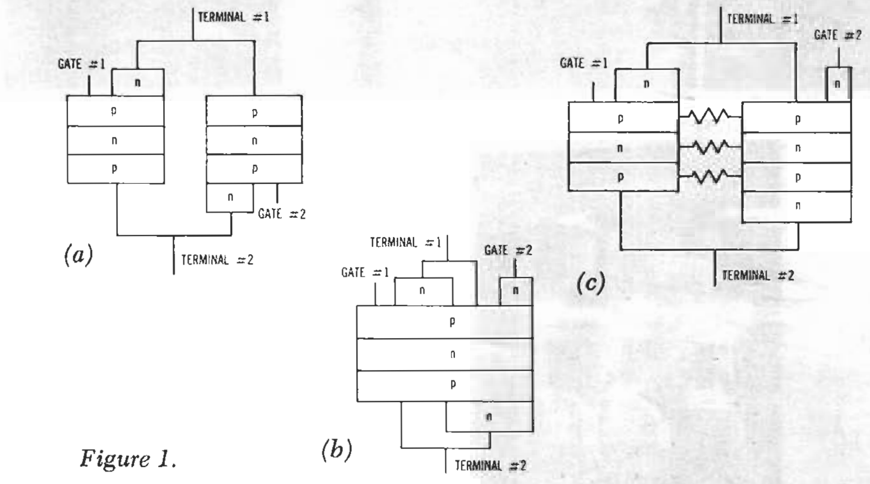


Figure 1.

Gate 2 and cause J-2 to become forward biased. Since Terminal 1 is positive with respect to Gate 2, J-2 at point (1) will have a greater forward bias than at point (2). This forward bias will cause an injection of holes primarily at point (1) into the central N type region. Some of these injected holes will be collected by junction J-3 which is now reversed biased. This will induce injection of electrons from J-4 into the lower P type region which in turn will be collected by J-3. Here again the counter injection will continue until the entire area over the lower N type region or cathode is turned on.

As long as the current through the device is maintained above a certain minimum level (holding current) this positive feedback will continue and the device will continue to conduct.

Turn-Off Time

Except for gating, the logic-triac is a symmetric device. The turn-off mechanism, when the device has been conducting in one direction is virtually the same as turn-off in the other direction.

Consider the case depicted in Figure 4 where Terminal 1 is negative with respect to Terminal 2 and the left hand portion of the device is conducting. Region P-1 and region N-2 in the left hand portion are flooded with minority carriers. Majority carriers are not shown.

When the polarity of the device is reversed some of these minority carriers will recombine with majority carriers and most of the others will be collected by junctions J-1 and J-3. The collection of these stored minority carriers results in the reverse recovery current. This current causes the injection of additional minority carriers from junction J-2. Both electrons are injected into P-1 and holes are injected into N-2 by junction J-2. The primary effect is the injection of holes into N-2. This additional injection prolongs the recovery process but since the α of the P-1, N-2, P-2 section is quite low at these current densities only a small percentage of them ever reach J-3.

If a sufficient number of holes are collected at section R of J-3 to induce injection of electrons by J-4 into P-2 section R (See Figure 5) the device will turn on. This can only occur if a large number of holes have diffused into section R from section L of N-2 or if a sufficient number of holes are injected into section R of N-2 by J-2 during the recovery phase of section L.

This problem is minimized by constructing the device with a horizontal separation between N-1 and N-3 of several minority carrier diffusion lengths and obtaining a high enough sheet resistance of N-2 to minimize injection of carriers from J-2 into section R of N-2.

The development of International Rectifier's new 200 ampere power logic-triac is the result of the continuous research effort at IR, in order to provide engineers with the design tools necessary for our industry.

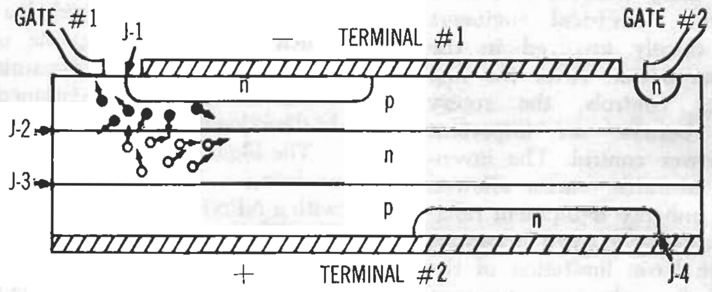


Figure 2.

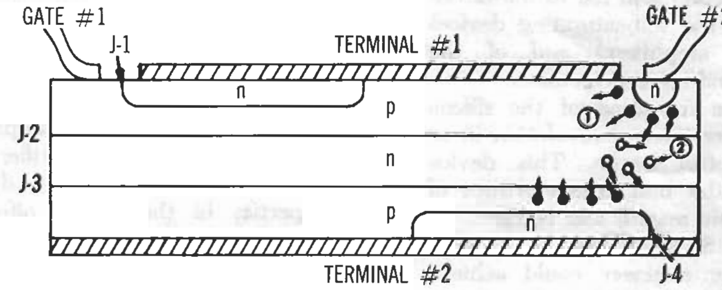


Figure 3.

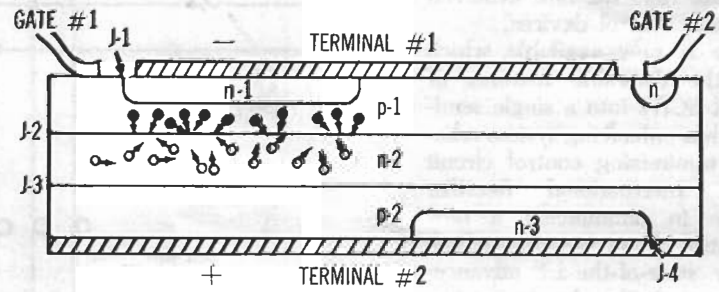


Figure 4.

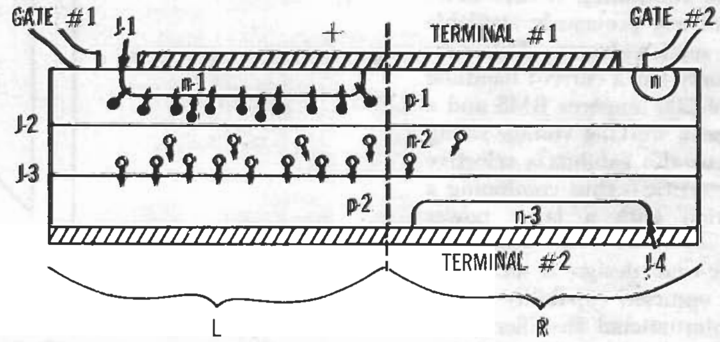


Figure 5.

AC & DC POWER CONTROL with a single semiconductor

by Dave Cooper
Manager, Special Projects Engineering

Since the early work of Heaviside and Steinmetz, electrical engineers have been deeply involved in the control of ac power. After the first potentiometer controls, the rotory transformer became an important means of power control. The invention of the saturable reactor allowed designers to enhance equipment reliability and improve control accuracy. However the basic limitation of the device to half cycle time response limited the figure of merit (gain-time constant factor) and led to the development of the self-saturating devices (magnetic amplifiers) and of the thyration and ignitron rectifiers.

With the invention of the silicon controlled rectifier, a revolution in ac power control began. This device combined the best characteristics of the saturable reactor and rectifier. By using two SCR's connected in anti-parallel the engineer could achieve control of large amounts of ac power with a figure of merit orders of magnitude greater than the best achieved with magnetic control devices.

A device is now available which combines the desirable features of anti-parallel SCR's into a single semiconductor thus enhancing system reliability and minimizing control circuit complexity. International Rectifier takes pride in announcing a new power control semiconductor which is a major state-of-the-art advancement in ac control — the logic-triac. The International Rectifier logic-triac is capable of controlling twenty times the power of any previously available ac control semiconductor. This new device not only has a current handling capability of 200 amperes RMS and a maximum peak working voltage rating of 1000V but also exhibits a selective gate characteristic — thus combining a logic function with a large power capability.

The logic-triac design is an extension of the epitaxial capability developed by International Rectifier in its power SCR technology. The stability of the device has been established by extensive blocking life tests at elevated temperatures, considerably above the maximum specified operating temperature. The epitaxial structure with no area overlap has yielded a device which is not only capable of long term stability but has dynamic properties never before combined in a bi-directional thyristor.

The logic-triac can be characterized as being a PNPN device in parallel with a NPNP device — both being triggered from a single source. By applying a positive gate signal with respect to main terminal one, the device will only conduct with main terminal two positive and main terminal one negative. By applying a negative gate signal with respect to main terminal one the device will conduct with either polarity of main terminal voltage.

With the large area control provided by the International Rectifier logic-triac design, the excellent dynamic properties of the device offer considerable flexibility to the circuit designer. The IR device (200AC100, 200AC80, 200AC60 and 200AC40) has an allowable di/dt of 50 amperes per microsecond and a guaranteed dv/dt of 20 volts per microsecond.

Control of almost 140 kw of ac power is now attainable with a single semiconductor. In order to illustrate the inherent advantages of the logic-triac to the Circuit Designer we will examine a few applications of the device.

A.C. Motor Control

For years the control of ac motors has been a major challenge to engineers. With the announcement of the International Rectifier logic-triac, a powerful new tool is available to circuit designers.

Speed control of dc motors has long been possible with a variety of control devices. The cumbersome proportions of the dc motor, its high control losses and required periodic maintenance have served as an incentive in the search for better and more versatile methods of ac motor speed control. With the advent of controlled

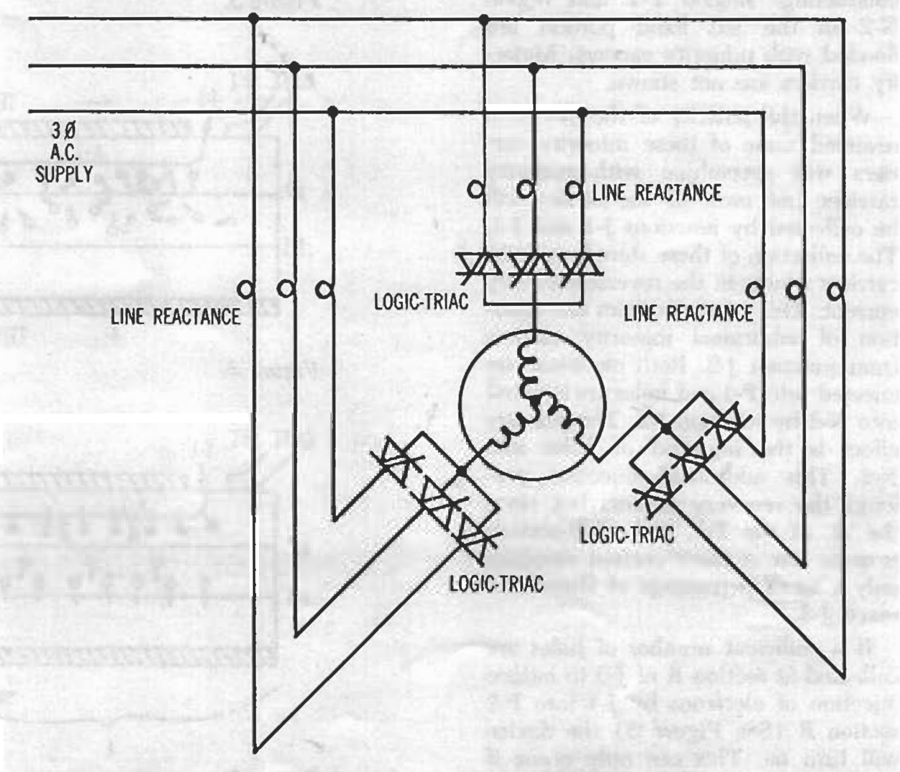


Figure 1. Triac Cycloconverter.

power tubes, new circuits were devised to provide controlled variable frequency for excitation of induction machines. Inverters were constructed with good characteristics but had extremely limited frequency range due to the response limitations of the tubes. Cycloconverters were devised which provided means of obtaining variable ratio frequency changers, but when using power tubes they were somewhat inefficient and required large amounts of control power. Methods were also devised to vary the effective resistance between slip rings of the three phase motor thus altering the effective slip of the machine to achieve a degree of speed control. All of these techniques have yielded successful controls for various applications in industry. Two SCR's connected in anti-parallel have been applied with a great degree of success to many of these circuits. With the availability of a high-power logic-triac, a new era of ac speed control system simplification is at hand.

Consider the cycloconverter of Figure 1. The circuit diagram shown using 9 triacs rather than the 18 SCR's normally required in this circuit presents a clear picture through use of this new power control device. This system could be mounted on three heat sinks with no interface insulation necessary. Under the same conditions, an SCR system would require 12 heat sinks.

As previously mentioned, another method of speed control for a three phase machine is achieved by means of varying the effective resistance be-

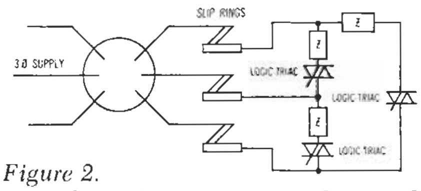


Figure 2.
Three Phase Motor Speed Control.

tween slip rings. As shown in Figure 2, this can be implimented by properly phase controlling 3 logic-triacs rather than by use of anti-parallel SCR's and a similar savings in driving circuitry as well as heat sinks can be achieved as in the case of the cycloconverter.

It is sometimes desirable to periodically cause reversal of induction motors in drive systems. This has previously been done by means of

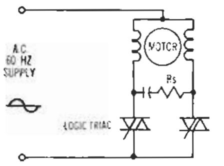
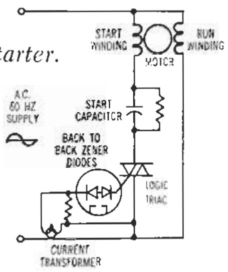


Figure 3.
Induction Motor Reversing Drive.

ac contactors or by controlling two sets of anti-parallel SCR's. Using the logic-triac as shown in Figure 3, a 20 h.p. motor can be driven without any special precautions for surge protection. The system can be constructed using only one heat sink and two triacs, rather than the three heat sinks and four SCR's previously required.

The circuit shown in Figure 4 is

Figure 4.
Static Motor Starter.



ideal for use as an automatic starter for a single phase ac motor. Up to the present time, the circuit has been restricted to a small ac motor application. With the 200 ampere International Rectifier logic-triac, this system has far greater potential and becomes economically feasible in many more applications.

Phase control has long been a powerful tool in the design of control equipment. However, the steep wave fronts generated by ordinary phase control, propagate system electrical noise, radio frequency interference (r.f.i.) and power systems line voltage disturbance. The technique of firing the power control semiconductors at near zero supply voltage virtually eliminiates these disturbances. Stress levels of insulation in machines or transformers driven by control systems utilizing this technique are greatly reduced. This is known as pulse burst modulation or zero cross-over firing (See Appendix I). The resulting wave shapes of this control technique are illustrated in Figure 5.

By gating the logic-triac in the various ways shown by these waves shapes, the three types of logic-triac control can be explained. In the first

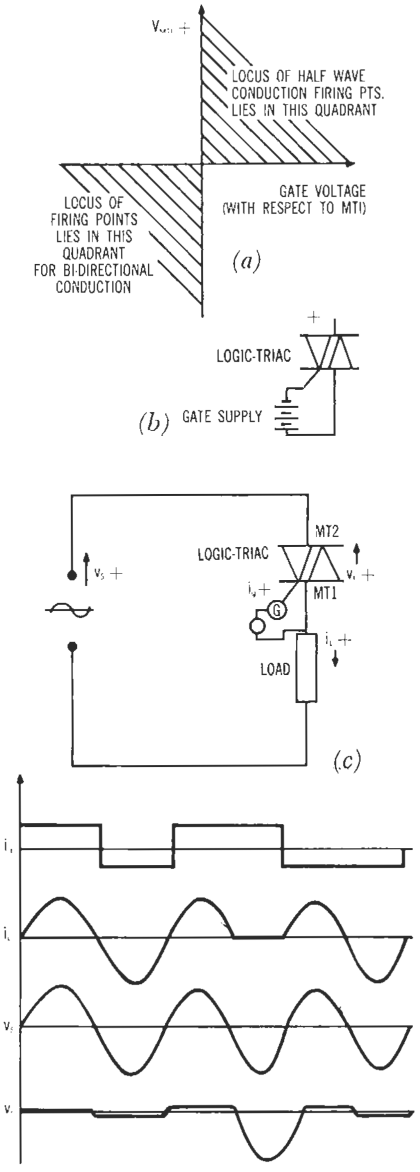


Figure 5. Power Triac Gate Firing

full cycle shown, the logic-triac is first driven by a bi-directional gate signal and made to conduct in each direction successively; thus conducting the full supply to the load. In the second cycle of supply voltage shown in Figure 5 the logic-triac is gated positive with respect to main terminal one and conducts in only one direction, thus providing half-wave control to the load. The logic-triac will not conduct if the main terminal two (MT2) voltage is negative with respect to main terminal one (MT1) and if the gate signal is positive with respect to MT1. The third cycle shown of the supply voltage is controlled by a uni-directional gate signal to the logic-triac which is

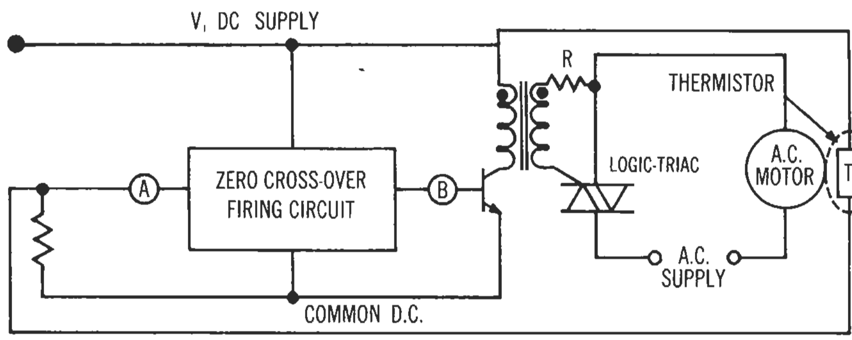


Figure 6. Over Temperature Protection

negative to the gate with respect to MT1 of the device. These versatile control characteristics of the logic-triac will simplify control design and provide considerable latitude to the circuit designer.

Utilizing this zero cross-over technique in ac motor circuits, yields some interesting applications for the logic-triac. For instance, shown in Figure 6 is a combination for over temperature protection of a single phase motor without use of electromechanical devices. With a thermistor located in proximity with the motor windings, the circuit can be designed to stop pulsing the logic-triac at a predetermined temperature and with some dead band designed into the system, the power will be removed from the machine until the temperature is considerably reduced.

A system for over current protection of an ac machine is illustrated

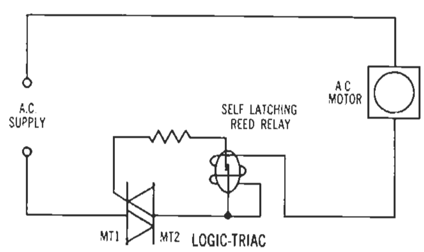


Figure 7. Over Current Protection

in Figure 7. By using a latching reed relay, activated by the motor current, the logic-triac can be made to turn off when the motor current becomes excessive. With the overload capabilities of the 200AC logic-triac and its 200 ampere steady state rating, some sizable motors can be protected with simplicity and reliability.

Heating Controls

Although individual room control of electric power in the home has become increasingly popular, the advantages of central forced air heating are often desirable. In many areas — with long periods of extreme cold — the advantage of a central heating system through which the air in the home is circulated, filtered and humidified are undeniable. The availability of a logic-triac operating on a 220V line and carrying 200 amperes makes a simple, reliable central heating system possible. When used in conjunction with pulse burst modulation, the system can be designed without the r.f.i. resulting from ordinary phase control or electro-mechanical control. In figure 8 a typical circuit for a heating system is shown, requiring no relays, contactors, or thermostat contacts. A temperature sensitive resistor can be used as the sensor and a potentiometer as the control. This design has advantages of extreme accuracy, with a temperature error of less than 1°F. By eliminating the temperature sensor in the drive circuit, this same principle can be applied to welding equipment which may be operated manually or automatically.

The fan used in a forced air heating system can be controlled in this same manner. By reversing the positions of the thermostat (potentiometer) and temperature sensor (negative temperature coefficient resistor) shown in Figure 8 and locating the resistor near the heat source, the fan can be turned on (as shown in figure 9) when the air becomes warm enough to circulate. This technique serves equally well in industrial heating and other fan control applications.

Lighting Circuit Applications

Another very active area for appli-

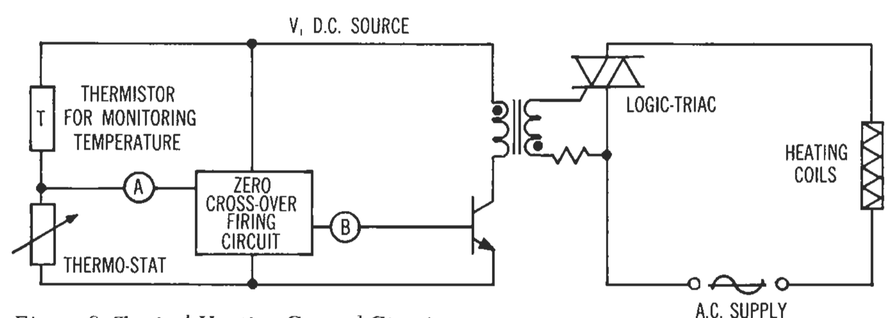


Figure 8. Typical Heating Control Circuit

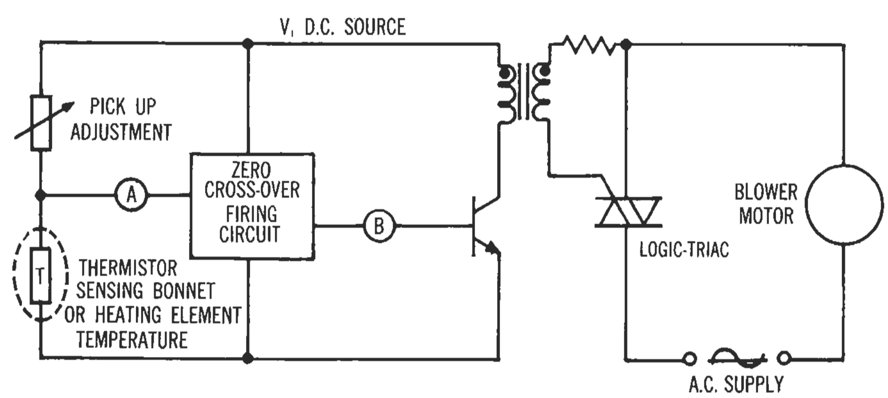


Figure 9. Typical Blower Fan Control

cation of ac static control is in various types of lighting systems. By phase controlling the logic-triac, the light intensity of banks of incandescent lamps can be adjusted with a single device. Thus, large theater light dimming systems can be greatly simplified by using a single logic-triac rather than anti-parallel SCR's. In figure 10, a

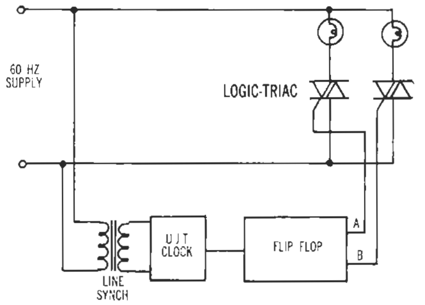


Figure 10. Light Flasher.

configuration for a light flasher is shown requiring two logic-triacs and one heat sink rather than the four SCR's and three heat sinks previously required.

When motor loads are switched on to an ac line which is also supplying fluorescent lamps – there is often a visible flicker in the lamps due to the surge loading of the line. This can often be objectionable especially when the motor starting is frequent. For example, compressor motors are often required to start frequently and run a relatively short time. One method of minimizing the effect of such flicker is by automatically correcting the line voltage during high surge currents. This is done by inserting a section of step up transformer to boost the line voltage temporarily until the surge disappears.

The logic-triac is an excellent device for this purpose because of the simplicity of the driving function and ease of heat sinking. A tap change similar to that shown in Figure 11 can

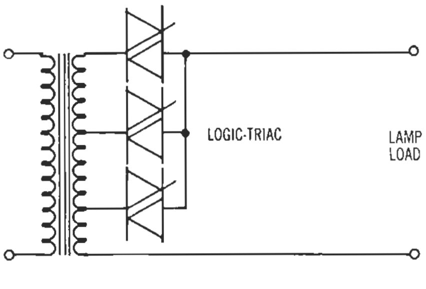


Figure 11. Static Tap Changer for Discrete Load Steps.

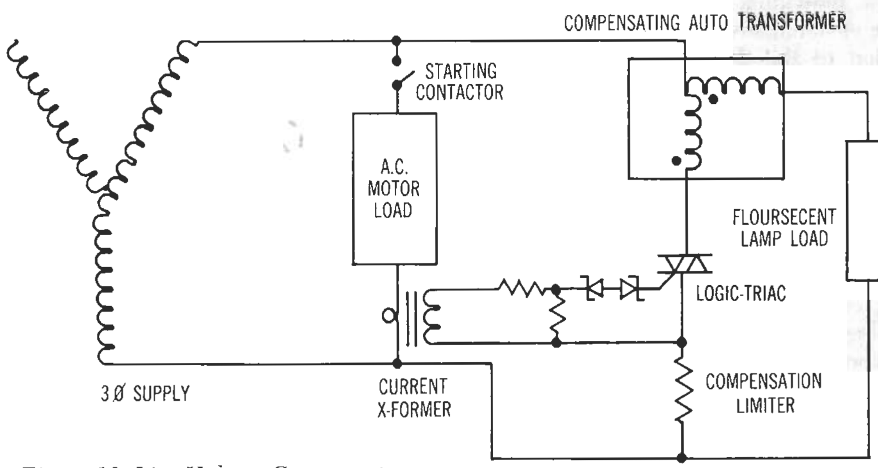


Figure 12. Line Voltage Compensator.

be used as a line compensator. Line compensation can also be accomplished by using a logic-triac and a current transformer as shown in Figure 12. As the starting contactor, in series with the motor load, is closed, the motor surge current begins to flow. This current is generally three to six times the running current of the machine. The surge causes a voltage drop on the line inductance and transient inductance of the supply alternator which causes the line voltage, as seen by the fluorescent lamps, to drop. If the surge current is sensed by a current transformer as shown in Figure 12 and caused to fire the logic-triac into a compensating auto-transformer, the flicker resulting from motor starting can be greatly minimized.

The degree of compensation can be adjusted by the resistor in series with the logic-triac, while the surge current at which compensation takes place can be changed by adjusting the resistor divider across the secondary of the current transformer. By utilizing this circuit across each line, the three phase system can be automatically compensated.

High Current DC or AC Supplies

In the electro chemical industry, power supply outputs range into hundreds of killowatts at extremely high currents but often at quite low voltages. System cost generally dictates use of a multi-phase step down transformer with rectified output and phase controlled input. Many rectifier-transformer configurations are useful depending upon the load requirements and the power source available. Some transformer control arrangements are shown in Figures 13, 14 and 15.

AC control, on the primary of the transformer, yields worthwhile savings in semiconductor cost and system efficiency. Use of the logic-triac for this application is ideal from the standpoint of control circuit simplification and reduction in the number of heat sinks. The circuit of Figure 13

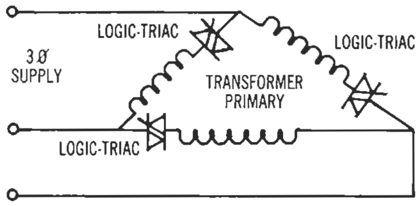


Figure 13. Delta Connected Transformer Phase Control

is attractive from the standpoint of semiconductor rating but requires careful volt-second balance to avoid high circulating currents. In all cases, load short circuits can cause semiconductor damage unless sufficient limiting inductance is provided somewhere in the system. It is often wise to provide some sort of system in the primary side of the transformer to

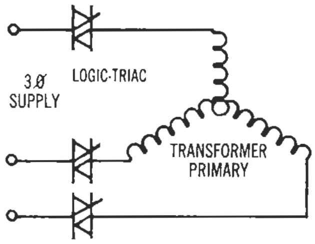


Figure 14. Y Connected Transformer Line Control